## SPEC CPU®2017 Integer Speed Result

### Tyrone Systems

(Tests Sponsor: Netweb Pte Ltd)

**DS400TR-54/R**

(2.60 GHz, Intel Xeon Gold 6240)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>= 9.95</th>
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<tbody>
<tr>
<td>SPECspeed®2017_int_peak</td>
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**CPU2017 License:** 006042  
**Test Sponsor:** Netweb Pte Ltd  
**Tested by:** Netweb

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<td>6.82</td>
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</tbody>
</table>

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### Hardware

- **CPU Name:** Intel Xeon Gold 6240  
- **Max MHz:** 3900  
- **Nominal:** 2600  
- **Enabled:** 36 cores, 2 chips, 2 threads/core  
- **Orderable:** 1, 2 (chip)s  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 24.75 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933P-R)  
- **Storage:** 1 x 480 GB SSD  
- **Other:** None

### Software

- **OS:** CentOS Linux release 7.7.1908 (Core)  
- **Compiler:** C/C++: Version 19.0.4.243 of Intel C/C++  
- **Fortran:** Version 19.0.4.243 of Intel Fortran  
- **Firmware:** Version 3.1a released Jun-2019  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** None
Results Table

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<tr>
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</tbody>
</table>

Compiler Notes

SPEC has learned that this result, which used an evaluation compiler, was submitted contrary to the compiler license terms. Intel has granted a one-time waiver for this result.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
DS400TR-54/R
(2.60 GHz, Intel Xeon Gold 6240)

SPECspeed®2017_int_base = 9.95
SPECspeed®2017_int_peak = 10.2

General Notes (Continued)

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7ed6b1e6e46a485a0011
running on NODE4 Mon Nov 4 08:44:51 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 6240 CPU @ 2.60GHz
    2. "physical id"s (chips)
    72 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 18
siblings : 36
  physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
  physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27

From lscpu:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian
  CPU(s): 72
  On-line CPU(s) list: 0-71
  Thread(s) per core: 2
  Core(s) per socket: 18
  Socket(s): 2
  NUMA node(s): 2
  Vendor ID: GenuineIntel
  CPU family: 6
  Model: 85
  Model name: Intel(R) Xeon(R) Gold 6240 CPU @ 2.60GHz

(Continued on next page)


### Platform Notes (Continued)

- Stepping: 7
- CPU MHz: 999.914
- CPU max MHz: 3900.0000
- CPU min MHz: 1000.0000
- BogoMIPS: 5200.00
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 1024K
- L3 cache: 25344K
- NUMA node0 CPU(s): 0-17,36-53
- NUMA node1 CPU(s): 18-35,54-71
- Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 sdbg fма cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpb cat_13 cdп_13 intel_pqqin intel_pt ssbd mba ibp bтtп btrs_enхanced tпr_shadow vmн flexprioп ty ept vpid fsgsбase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cmq mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cmq_llc cmq_occоп_llc cmq_mbм_total cmq_mbм_local dtherm idа arat pln pts pku ospke avx512_vnni md_сlear spec_ctrl intel_stбp flush_l1d arch_capabilities

From /proc/cpuinfo cache data

cache size: 25344 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)
	node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53
	node 0 size: 195229 MB
	node 0 free: 164825 MB
	node 1 cpus: 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71
	node 1 size: 196608 MB
	node 1 free: 168016 MB
	node distances:
	node 0 1

0: 10 21

1: 21 10

From /proc/meminfo

MemTotal: 394859660 KB
HugePages_Total: 0
Hugepagesize: 2048 KB

(Continued on next page)
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</table>

**CPU2017 License:** 006042  
**Test Sponsor:** Netweb Pte Ltd  
**Tested by:** Netweb

**Test Date:** Nov-2019  
**Hardware Availability:** Sep-2019  
**Software Availability:** Aug-2019

### Platform Notes (Continued)

From `/etc/*release* /etc/*version*`:
- centos-release: CentOS Linux release 7.7.1908 (Core)  
- centos-release-upstream: Derived from Red Hat Enterprise Linux 7.7 (Source)  
- os-release:
  - NAME="CentOS Linux"  
  - VERSION="7 (Core)"  
  - ID="centos"  
  - ID_LIKE="rhel fedora"  
  - VERSION_ID="7"  
  - PRETTY_NAME="CentOS Linux 7 (Core)"  
  - ANSI_COLOR="0;31"  
  - CPE_NAME="cpe:/o:centos:centos:7"

- redhat-release: CentOS Linux release 7.7.1908 (Core)  
- system-release: CentOS Linux release 7.7.1908 (Core)  
- system-release-cpe: cpe:/o:centos:centos:7

**uname -a:**
```
Linux NODE4 3.10.0-1062.el7.x86_64 #1 SMP Wed Aug 7 18:08:02 UTC 2019 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

- CVE-2018-3620 (L1 Terminal Fault): Not affected  
- Microarchitectural Data Sampling: Not affected  
- CVE-2017-5754 (Melttdown): Not affected  
- CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp  
- CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitation  
- CVE-2017-5715 (Spectre variant 2): Mitigation: Full retpoline, IBPB

**run-level 3 Nov 2 16:41**

**SPEC is set to:** /home/cpu2017  
**Filesystem**

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<tr>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>xfs</td>
<td>392G</td>
<td>208G</td>
<td>185G</td>
<td>53%</td>
<td>/home</td>
</tr>
</tbody>
</table>

From `/sys/devices/virtual/dmi/id`:
- BIOS: American Megatrends Inc. 3.1a 06/11/2019  
- Vendor: Tyrone Systems  
- Product: X11DAi-N  
- Serial: 123456789

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are

(Continued on next page)
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SPECspeed®2017_int_peak = 10.2

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

Platform Notes (Continued)

frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
   4x NO DIMM NO DIMM
   12x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| C       | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) | 625.x264_s(base, peak) 657.xz_s(base, peak) |
-----------------------------------------------------------------------------
| Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,       |
| Version 19.0.4.243 Build 20190416                                         |
| Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.            |
| icc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.   |
-----------------------------------------------------------------------------

==============================================================================
| C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) | 641.leela_s(base, peak) |
-----------------------------------------------------------------------------
| Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,   |
| Version 19.0.4.243 Build 20190416                                        |
| Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.           |
| icpc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC. |
-----------------------------------------------------------------------------

==============================================================================
| Fortran | 648.exchange2_s(base, peak)                                              |
-----------------------------------------------------------------------------
| Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, |
| Version 19.0.4.243 Build 20190416                                       |
| Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.            |
| ifort: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.|
-----------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

(Continued on next page)
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**SPECspeed®2017_int_base = 9.95**

**SPECspeed®2017_int_peak = 10.2**

---

Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort -m64

---

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

---

Base Optimization Flags

C benchmarks:
-W1, -z, muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-W1, -z, muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64
-lgkmalloc

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs

---

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

(Continued on next page)
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CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

Test Date: Nov-2019
Hardware Availability: Sep-2019
Software Availability: Aug-2019

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -gopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -gopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -gopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
DS400TR-54/R
(2.60 GHz, Intel Xeon Gold 6240)

SPECspeed®2017_int_base = 9.95
SPECspeed®2017_int_peak = 10.2

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

Test Date: Nov-2019
Hardware Availability: Sep-2019
Software Availability: Aug-2019

Peak Optimization Flags (Continued)

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64
-lqkmalloc

623.xalancbmk_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64
-lqkmalloc

631.deepsjeng_s: Same as 623.xalancbmk_s

641.leela_s: Same as 623.xalancbmk_s

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2019-11-04 08:44:50-0500.
Report generated on 2020-10-29 14:56:54 by CPU2017 PDF formatter v6255.