### NEC Corporation

#### NEC Express5800/T110j-S (Intel Core i3-9300)

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation  
**Test Date:** Oct-2019  
**Hardware Availability:** Nov-2019  
**Software Availability:** Aug-2019

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**Hardware**

- **CPU Name:** Intel Core i3-9300  
- **Max MHz:** 4300  
- **Nominal:** 3700  
- **Enabled:** 4 cores, 1 chip  
- **Orderable:** 1 chip  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 256 KB I+D on chip per core  
- **L3:** 8 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 64 GB (4 x 16 GB 2Rx8 PC4-2666V-E, running at 2400)  
- **Storage:** 1 x 1 TB SATA, 7200 RPM  
- **Other:** None

**Software**

- **OS:** Red Hat Enterprise Linux Server release 7.7 (Maipo)  
- **Kernel:** 3.10.0-1062.el7.x86_64  
- **Compiler:** C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux; Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux  
- **Parallel:** No  
- **Firmware:** NEC BIOS Version F01 08/21/2019 released Nov-2019  
- **File System:** ext4  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** --
NEC Corporation

Express5800/T110j-S (Intel Core i3-9300)

SPECrates®2017_int_base = 27.2
SPECrates®2017_int_peak = 28.1

Results Table

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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
IRQ balance service was stopped using "systemctl stop irqbalance.service"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"

General Notes

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

NEC Corporation
Express5800/T110j-S (Intel Core i3-9300)

SPECr at®2017_int_base = 27.2
SPECr at®2017_int_peak = 28.1

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Tested by: NEC Corporation

Test Date: Oct-2019
Hardware Availability: Nov-2019
Software Availability: Aug-2019

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS Settings:
VT-x: Disabled
Energy Efficient P-state: Disabled
Energy Efficient Turbo: Disabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011
running on t110js Tue Oct 29 12:02:29 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Core(TM) i3-9300 CPU @ 3.70GHz
 1 "physical id"s (chips)
 4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 4
physical 0: cores 0 1 2 3

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 4
On-line CPU(s) list: 0-3
Thread(s) per core: 1
Core(s) per socket: 4
Socket(s): 1

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

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Platform Notes (Continued)

NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 158
Model name: Intel(R) Core(TM) i3-9300 CPU @ 3.70GHz
Stepping: 11
CPU MHz: 4200.665
CPU max MHz: 4300.0000
CPU min MHz: 800.0000
BogoMIPS: 7392.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 256K
L3 cache: 8192K
NUMA node0 CPU(s): 0-3
Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave f16c rdrand lahf_lm abm 3dnowprefetch intel_pt ssbd ibrs ibpb tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid mxrsens adx smap clflushopt xsaveopt xsave cxtpr dtherm ida arat pln pts hwp hwp_notify hwp_act_window hwp_epp md_clear spec_ctrl intel_stibp flush_l1d

/proc/cpuinfo cache data
cache size: 8192 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 1 nodes (0)
nodes 0 cpus: 0 1 2 3
node 0 size: 65284 MB
node 0 free: 63401 MB
node distances:
node 0
  0: 10

From /proc/meminfo
MemTotal: 65719160 KB
HugePages_Total: 0
Hugepagesize: 2048 KB

From /etc/*release*/ /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux Server"

(Continued on next page)
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Test Date: Oct-2019

Platform Notes (Continued)

VERSION="7.7 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VARIANT="Server"
VARIANT_ID="server"
VERSION_ID="7.7"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.7 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.7 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.7 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.7:ga:server

uname -a:
Linux t110js 3.10.0-1062.el7.x86_64 #1 SMP Thu Jul 18 20:25:13 UTC 2019 x86_64 x86_64
x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault): Mitigation: PTE Inversion
Microarchitectural Data Sampling: Mitigation: Clear CPU buffers; SMT disabled
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full retpoline, IBPB

run-level 3 Oct 29 11:56

SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 ext4 908G 72G 790G 9% /

From /sys/devices/virtual/dmi/id
BIOS: American Megatrends Inc. F01 08/21/2019
Vendor: NEC
Product: Express5800/T110j-S [N8100-2804Y]
Serial: 0000002

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
4x Samsung M391A2K43BB1-CTD 16 GB 2 rank 2667

(End of data from sysinfo program)
NEC Corporation
Express5800/T110j-S (Intel Core i3-9300)

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Compiler Version Notes

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Compiler Version Notes (Continued)

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Copyright (C) 1985–2019 Intel Corporation. All rights reserved.

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C++ | 523.xalancbmk_r(peak)
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| 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985–2019 Intel Corporation. All rights reserved.

==============================================================================
Fortran | 548.exchange2_r(base, peak)
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985–2019 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64

(Continued on next page)
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Base Portability Flags (Continued)

502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-l/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-l/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-l/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64 -std=c11


C++ benchmarks (except as noted below):
icpc -m64

523.xalancbmk_r: icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/ia32_lin

(Continued on next page)
### NEC Corporation

**Express5800/T110j-S (Intel Core i3-9300)**

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**Peak Compiler Invocation (Continued)**

Fortran benchmarks:  
`ifort -m64`

---

**Peak Portability Flags**

- `500.perlbench_r`: `-DSPEC_LP64 -DSPEC_LINUX_X64`
- `502.gcc_r`: `-D_FILE_OFFSET_BITS=64`
- `505.mcf_r`: `-DSPEC_LP64`
- `520.omnetpp_r`: `-DSPEC_LP64`
- `523.xalancbmk_r`: `-D_FILE OFFSET BITS=64 -DSPEC_LINUX`
- `525.x264_r`: `-DSPEC_LP64`
- `531.deepsjeng_r`: `-DSPEC_LP64`
- `541.leela_r`: `-DSPEC_LP64`
- `548.exchange2_r`: `-DSPEC_LP64`
- `557.xz_r`: `-DSPEC_LP64`

---

**Peak Optimization Flags**

C benchmarks:

- `500.perlbench_r`: `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=4  
-fno-strict-overflow  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64  
-lqkmalloc`

- `502.gcc_r`: `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=4  
-fno-alias  
-L/usr/local/je5.0.1-32/lib -ljemalloc`

- `505.mcf_r`:  
`basepeak = yes`

- `525.x264_r`: `-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -fno-alias  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64  
-lqkmalloc`

- `557.xz_r`: `-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64  
-lqkmalloc`
PEC CPU®2017 Integer Rate Result
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NEC Corporation
Express5800/T110j-S (Intel Core i3-9300)

SPECrate®2017_int_base = 27.2
SPECrate®2017_int_peak = 28.1

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Tested by: NEC Corporation

Test Date: Oct-2019
Hardware Availability: Nov-2019
Software Availability: Aug-2019

Peak Optimization Flags (Continued)

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and Libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

541.leela_r: Same as 531.deepsjeng_r

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and Libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

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Tested with SPEC CPU®2017 v1.1.0 on 2019-10-28 23:02:28-0400.