# SPEC CPU®2017 Integer Speed Result

## NEC Corporation

**Express5800/T110j-S (Intel Xeon E-2246G)**

| SPECspeed®2017_int_base = 11.5 | SPECspeed®2017_int_peak = 11.8 |

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation  
**Test Date:** Nov-2019  
**Hardware Availability:** Nov-2019  
**Software Availability:** Aug-2019

### Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
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<tbody>
<tr>
<td>600.perlbench_s</td>
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<tr>
<td>602.gcc_s</td>
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<tr>
<td>605.mcf_s</td>
<td>12</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>12</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>12</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>12</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>12</td>
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<tr>
<td>641.leela_s</td>
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<tr>
<td>648.exchange2_s</td>
<td>12</td>
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<tr>
<td>657.xz_s</td>
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### SPECspeed®2017 int_base (11.5)

<table>
<thead>
<tr>
<th>Benchmark</th>
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<td>600.perlbench_s</td>
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<td>648.exchange2_s</td>
<td>21.1</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>21.1</td>
</tr>
</tbody>
</table>

### SPECspeed®2017 int_peak (11.8)

### Hardware

- **CPU Name:** Intel Xeon E-2246G  
- **Max MHz:** 4800  
- **Nominal:** 3600  
- **Enabled:** 6 cores, 1 chip, 2 threads/core  
- **Orderable:** 1 chip  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **Cache L2:** 256 KB I+D on chip per core  
- **Cache L3:** 12 MB I+D on chip per chip  
- **Memory:** 64 GB (4 x 16 GB 2Rx8 PC4-2666V-E)  
- **Storage:** 1 x 1 TB SATA, 7200 RPM  
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux Server release 7.7 (Maipo)  
- **Kernel:** 3.10.0-1062.el7.x86_64  
- **Compiler:** C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux;  
  Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux  
- **Parallel:** Yes  
- **Firmware:** NEC BIOS Version F01 08/21/2019 released Nov-2019  
- **File System:** ext4  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** --
NEC Corporation

Express5800/T110j-S (Intel Xeon E-2246G)

SPEC®2017_int_base = 11.5

SPEC®2017_int_peak = 11.8

Results Table

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<th>Ratio</th>
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</tbody>
</table>

SPECspeed®2017_int_base = 11.5

SPECspeed®2017_int_peak = 11.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
SPEC CPU®2017 Integer Speed Result

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SPECspeed®2017_int_base = 11.5
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CPU2017 License: 9006
Test Sponsor: NEC Corporation
Tested by: NEC Corporation

Test Date: Nov-2019
Hardware Availability: Nov-2019
Software Availability: Aug-2019

General Notes (Continued)

Built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
Sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases

Platform Notes

BIOS Settings:
VT-x: Disabled
Energy Efficient P-state: Disabled
Energy Efficient Turbo: Disabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edbbie6e46a485a0011
running on t110js Sat Nov 9 17:31:26 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) E-2246G CPU @ 3.60GHz
  1 "physical id"s (chips)
  12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 12
physical 0: cores 0 1 2 3 4 5

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 12
On-line CPU(s) list: 0-11
Thread(s) per core: 2
Core(s) per socket: 6
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 158
Model name: Intel(R) Xeon(R) E-2246G CPU @ 3.60GHz
Stepping: 10
CPU MHz: 4743.017
CPU max MHz: 4800.0000
CPU min MHz: 800.0000

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

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Platform Notes (Continued)

BogoMIPS: 7200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 256K
L3 cache: 12288K
NUMA node0 CPU(s): 0-11
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl pge mca cmov

/proc/cpuinfo cache data
  cache size: 12288 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11
  node 0 size: 65283 MB
  node 0 free: 63376 MB
  node distances:
    node 0
  0: 10

From /proc/meminfo
  MemTotal: 65717608 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.7 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VARIANT="Server"
    VARIANT_ID="server"
    VERSION_ID="7.7"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.7 (Maipo)"
  redhat-release: Red Hat Enterprise Linux Server release 7.7 (Maipo)

(Continued on next page)
Platform Notes (Continued)

system-release: Red Hat Enterprise Linux Server release 7.7 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.7:ga:server

uname -a:
Linux t110js 3.10.0-1062.el7.x86_64 #1 SMP Thu Jul 18 20:25:13 UTC 2019 x86_64 x86_64
x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault): Mitigation: PTE Inversion
Microarchitectural Data Sampling: Mitigation: Clear CPU buffers; SMT vulnerable
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer
sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full retpoline, IBPB

run-level 3 Nov 9 17:25

SPEC is set to: /home/cpu2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 ext4 908G 76G 786G 9% /

From /sys/devices/virtual/dmi/id
BIOS: American Megatrends Inc. F01 08/21/2019
Vendor: NEC
Product: Express5800/T110j-S [N8100-2802Y]
Serial: 0000002

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
4x Samsung M391A2K43BB1-CTD 16 GB 2 rank 2667

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base,
   peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
==============================================================================

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
NEC Corporation
Express5800/T110j-S (Intel Xeon E-2246G)

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Compiler Version Notes (Continued)

Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
| C++    | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)                      |
|        | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)                        |
------------------------------------------------------------------------------

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
| Fortran | 648.exchange2_s(base, peak)                                               |
------------------------------------------------------------------------------

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64

(Continued on next page)
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Base Portability Flags (Continued)

657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-W1,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-W1,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Peak Portability Flags

Same as Base Portability Flags
NEC Corporation

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Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -gopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=4 -gopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -gopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

623.xalancbmk_s: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

631.deepsjeng_s: Same as 623.xalancbmk_s
641.leela_s: Same as 623.xalancbmk_s

Fortran benchmarks:

(Continued on next page)
### NEC Corporation

**Express5800/T110j-S (Intel Xeon E-2246G)**

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</tr>
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#### Peak Optimization Flags (Continued)

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:


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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2019-11-09 03:31:26-0500.
