### SPEC CPU®2017 Integer Rate Result

Supermicro
SuperServer 5019C-WR (X11SCW-F, Intel Celeron G4900T)

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_int_base</td>
<td>8.11</td>
</tr>
<tr>
<td>SPECrate®2017_int_peak</td>
<td>8.67</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro  
**Test Date:** Nov-2019

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>CPU Name:</strong> Intel Celeron G4900T</td>
<td><strong>OS:</strong> SUSE Linux Enterprise Server 12 SP3 (x86_64)</td>
</tr>
<tr>
<td><strong>Max MHz:</strong> 2900</td>
<td><strong>Kernel:</strong> 4.4.114-94.11-default</td>
</tr>
<tr>
<td><strong>Nominal:</strong> 2900</td>
<td><strong>Compiler:</strong> C/C++: Version 19.0.0.117 of Intel C/C++ Compiler for Linux; Fortran: Version 19.0.0.117 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td><strong>Enabled:</strong> 2 cores, 1 chip</td>
<td><strong>Parallel:</strong> No</td>
</tr>
<tr>
<td><strong>Orderable:</strong> 1 chip</td>
<td><strong>Firmware:</strong> Version 1.0b released May-2019</td>
</tr>
<tr>
<td><strong>Cache L1:</strong> 32 KB I + 32 KB D on chip per core</td>
<td><strong>File System:</strong> xfs</td>
</tr>
<tr>
<td><strong>L2:</strong> 256 KB I+D on chip per core</td>
<td><strong>System State:</strong> Run level 3 (multi-user)</td>
</tr>
<tr>
<td><strong>L3:</strong> 2 MB I+D on chip per core</td>
<td><strong>Base Pointers:</strong> 64-bit</td>
</tr>
<tr>
<td><strong>Other:</strong> None</td>
<td><strong>Peak Pointers:</strong> 32/64-bit</td>
</tr>
<tr>
<td><strong>Memory:</strong> 64 GB (4 x 16 GB 2Rx8 PC4-2666V-E, running at 2400)</td>
<td><strong>Other:</strong> jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td><strong>Storage:</strong> 1 x 200 GB SATA III SSD</td>
<td><strong>Power Management:</strong> --</td>
</tr>
<tr>
<td><strong>Other:</strong> None</td>
<td></td>
</tr>
</tbody>
</table>

---

### SPECrate Results

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base</th>
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</tbody>
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---

### SPECrate Test Results

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench_r</td>
<td>2</td>
<td>9.12</td>
<td>8.54</td>
</tr>
<tr>
<td>gcc_r</td>
<td>2</td>
<td>9.79</td>
<td>9.64</td>
</tr>
<tr>
<td>mcf_r</td>
<td>2</td>
<td>9.57</td>
<td>9.57</td>
</tr>
<tr>
<td>omnetpp_r</td>
<td>2</td>
<td>5.37</td>
<td>3.81</td>
</tr>
<tr>
<td>xalancbmk_r</td>
<td>2</td>
<td>9.19</td>
<td>11.2</td>
</tr>
<tr>
<td>x264_r</td>
<td>2</td>
<td>7.54</td>
<td>14.4</td>
</tr>
<tr>
<td>deepsjeng_r</td>
<td>2</td>
<td>7.78</td>
<td>10.9</td>
</tr>
<tr>
<td>leela_r</td>
<td>2</td>
<td>6.49</td>
<td>6.35</td>
</tr>
<tr>
<td>exchange2_r</td>
<td>2</td>
<td>12.2</td>
<td>12.2</td>
</tr>
<tr>
<td>xz_r</td>
<td>2</td>
<td>4.61</td>
<td>4.61</td>
</tr>
</tbody>
</table>

---

**Note:** All results are average values of runs over two different hardware availability dates. The Hardware Availability date is Nov-2018, and the Software Availability date is Sep-2018.
Supermicro

SuperServer 5019C-WR (X11SCW-F, Intel Celeron G4900T)

SPEC CPU®2017 Integer Rate Result

SPECrate®2017_int_base = 8.11
SPECrate®2017_int_peak = 8.67

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>2</td>
<td>414</td>
<td>7.70</td>
<td>415</td>
<td>7.67</td>
<td>414</td>
<td>7.68</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>2</td>
<td>331</td>
<td>8.55</td>
<td>331</td>
<td>8.54</td>
<td>332</td>
<td>8.54</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>2</td>
<td>335</td>
<td>9.65</td>
<td>335</td>
<td>9.64</td>
<td>339</td>
<td>9.53</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>2</td>
<td>487</td>
<td>5.38</td>
<td>489</td>
<td>5.37</td>
<td>489</td>
<td>5.36</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>2</td>
<td>243</td>
<td>14.4</td>
<td>244</td>
<td>14.4</td>
<td>244</td>
<td>14.4</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>2</td>
<td>304</td>
<td>7.55</td>
<td>304</td>
<td>7.53</td>
<td>304</td>
<td>7.54</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>2</td>
<td>511</td>
<td>6.48</td>
<td>510</td>
<td>6.49</td>
<td>510</td>
<td>6.50</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>2</td>
<td>430</td>
<td>12.2</td>
<td>433</td>
<td>12.1</td>
<td>429</td>
<td>12.2</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>2</td>
<td>469</td>
<td>4.61</td>
<td>469</td>
<td>4.61</td>
<td>468</td>
<td>4.61</td>
</tr>
</tbody>
</table>

SPECrate®2017_int_base = 8.11
SPECrate®2017_int_peak = 8.67

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
Memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
    sync; echo 3>/proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Jemalloc, a general purpose malloc implementation

(Continued on next page)
General Notes (Continued)

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-65nv Fri Nov 1 10:33:03 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Celeron(R) G4900T CPU @ 2.90GHz
  1 "physical id"s (chips)
  2 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 2
siblings : 2
physical 0: cores 0 1

From lscpu:

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 2
On-line CPU(s) list: 0,1
Thread(s) per core: 1
Core(s) per socket: 2
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 158
Model name: Intel(R) Celeron(R) G4900T CPU @ 2.90GHz
Stepping: 11
CPU MHz: 2894.543
CPU max MHz: 2900.0000
CPU min MHz: 800.0000
BogoMIPS: 5807.98
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 256K

(Continued on next page)
Supermicro
SuperServer 5019C-WR (X11SCW-F , Intel Celeron G4900T)

**SPEC CPU®2017 Integer Rate Result**

Copyright 2017-2019 Standard Performance Evaluation Corporation

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>001176</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Supermicro</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Supermicro</td>
</tr>
</tbody>
</table>

**SPECrate®2017_int_base = 8.11**

**SPECrate®2017_int_peak = 8.67**

**Platform Notes (Continued)**

L3 cache: 2048K
NUMA node0 CPU(s): 0,1
Flags: fpu vme de pse tsc msr pae mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 sdbg cx16
xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave rdram
lahf_lm abm 3dnowprefetch arat epb invpcid_single pln pts dtherm hwp hwp_notify
hwp_act_window hwp_epp intel_pt rsb_ctxtsw spec_ctrl retpole kaiser tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust smep erms invpcid mxp rdseed smap
clflushopt xsaveopt xsaved xgetbv

```
From /proc/cpuinfo cache data
  cache size :  2048 KB
```

```
WARNING: a numactl 'node' might or might not correspond to a physical chip.
   available: 1 nodes (0)
   node 0 cpus: 0 1
   node 0 size: 64334 MB
   node 0 free: 63853 MB
   node distances:
     node 0 0: 10
```

```
From /proc/meminfo
  MemTotal: 65878308 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB
```

```
From /etc/*release*/etc/*version*
  SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 3
    # This file is deprecated and will be removed in a future service pack or release.
    # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP3"
    VERSION_ID="12.3"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

```
uname -a:
```

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Supermicro
SuperServer 5019C-WR (X11SCW-F, Intel Celeron G4900T)

SPECrate®2017_int_base = 8.11
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CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Nov-2019
Hardware Availability: Nov-2018
Software Availability: Sep-2018

Platform Notes (Continued)

Linux linux-65nv 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 3 Nov 1 10:28

SPEC is set to: /home/cpu2017

Filesystem    Type  Size  Used  Avail  Use%  Mounted on
/dev/sda3     xfs   145G   14G   131G   10%  /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0b 05/16/2019
Memory:
4x Micron 18ADF2G72AZ-2G6H1R 16 GB 2 rank 2667, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 502.gcc_r(peak)
Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
C       | 502.gcc_r(peak)
(Continued on next page)
<table>
<thead>
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<th>500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak)</th>
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</thead>
</table>

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<table>
<thead>
<tr>
<th>C++</th>
<th>523.xalancbmk_r(peak)</th>
</tr>
</thead>
</table>

Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

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<thead>
<tr>
<th>C++</th>
<th>520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)</th>
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<tr>
<th>Supermicro</th>
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**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro  
**Test Date:** Nov-2019  
**Hardware Availability:** Nov-2018  
**Software Availability:** Sep-2018

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### Compiler Version Notes (Continued)

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**Fortran | 548.exchange2_r(base, peak)**

Intel (R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.0.117 Build 20180804  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

---

### Base Compiler Invocation

**C benchmarks:**

```
icc -m64 -std=c11
```

**C++ benchmarks:**

```
icpc -m64
```

**Fortran benchmarks:**

```
ifort -m64
```

---

### Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc_r: -DSPEC_LP64  
505.mcf_r: -DSPEC_LP64  
520.omnetpp_r: -DSPEC_LP64  
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
525.x264_r: -DSPEC_LP64  
531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64  
548.exchange2_r: -DSPEC_LP64  
557.xz_r: -DSPEC_LP64

---

### Base Optimization Flags

**C benchmarks:**

```
-W1, -z, muldefs -xSSE4.2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```
Supermicro
SuperServer 5019C-WR (X11SCW-F, Intel Celeron G4900T)

SPECrate®2017_int_base = 8.11
SPECrate®2017_int_peak = 8.67

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Nov-2019
Hardware Availability: Nov-2018
Software Availability: Sep-2018

Base Optimization Flags (Continued)

C++ benchmarks:
-Wl,-z,muldefs -xSSE4.2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-Wl,-z,muldefs -xSSE4.2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64 -std=c11

502.gcc_r:icc -m32 -std=c11 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.0.117/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):
icpc -m64

523.xalancbmk_r:icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.0.117/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
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**Supermicro**

SuperServer 5019C-WR (X11SCW-F, Intel Celeron G4900T)

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**CPU2017 License:** 001176  | **Test Date:** Nov-2019
**Test Sponsor:** Supermicro  | **Hardware Availability:** Nov-2018
**Tested by:** Supermicro     | **Software Availability:** Sep-2018

### Peak Optimization Flags

#### C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xSSE4.2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xSSE4.2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xSSE4.2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib
-ljemalloc

525.x264_r: -Wl,-z,muldefs -xSSE4.2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -fno-alias
-L/usr/local/je5.0.1-64/lib -ljemalloc

557.xz_r: basepeak = yes

#### C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xSSE4.2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xSSE4.2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

#### Fortran benchmarks:

-Wl,-z,muldefs -xSSE4.2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

The flags files that were used to format this result can be browsed at

**Supermicro**  
SuperServer 5019C-WR (X11SCW-F, Intel Celeron G4900T)  

**SPECrate®2017_int_base = 8.11**  
**SPECrate®2017_int_peak = 8.67**

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU2017 License</td>
<td>001176</td>
</tr>
<tr>
<td>Test Sponsor</td>
<td>Supermicro</td>
</tr>
<tr>
<td>Tested by</td>
<td>Supermicro</td>
</tr>
<tr>
<td>Test Date</td>
<td>Nov-2019</td>
</tr>
<tr>
<td>Hardware Availability</td>
<td>Nov-2018</td>
</tr>
<tr>
<td>Software Availability</td>
<td>Sep-2018</td>
</tr>
</tbody>
</table>

You can also download the XML flags sources by saving the following links:


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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.0.5 on 2019-10-31 22:33:02-0400.  