Dell Inc.

PowerEdge FC640 (Intel Xeon Gold 5217, 3.00GHz)

SPEC CPU®2017 Integer Rate Result

SPECrate®2017_int_base = 107
SPECrate®2017_int_peak = 111

Dell Inc.

Test Sponsor: Dell Inc.
Test Date: Sep-2019
Hardware Availability: Apr-2019
Software Availability: Sep-2019

Tested by: Dell Inc.

CPU2017 License: 55

500.perlbench_r 32
502.gcc_r 32
505.mcf_r 32
520.omnetpp_r 32
523.xalancbmk_r 32
525.x264_r 32
531.deepsjeng_r 32
541.leela_r 32
548.exchange2_r 32
557.xz_r 32

Hardware

CPU Name: Intel Xeon Gold 5217
Max MHz: 3700
Nominal: 3000
Enabled: 16 cores, 2 chips, 2 threads/core
Orderable: 1.2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 11 MB I+D on chip per chip
Other: None
Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R, running at 2666)
Storage: 1 x 480 GB SATA SSD
Other: None

Software

OS: Ubuntu 18.04.2 LTS
Kernel: 4.15.0-62-generic
Compiler: C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux;
Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux
Parallel: No
Firmware: Version 2.2.11 released Jun-2019
File System: ext4
System State: Run level 5 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: --
Dell Inc.

PowerEdge FC640 (Intel Xeon Gold 5217, 3.00GHz)  

SPEC CPU®2017 Integer Rate Result

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CPU2017 License: 55  
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Test Sponsor: Dell Inc.  
Software Availability: Sep-2019  
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>32</td>
<td>617</td>
<td>82.5</td>
<td>618</td>
<td>82.4</td>
<td>621</td>
<td>82.0</td>
<td>32</td>
<td>540</td>
<td>94.3</td>
<td>541</td>
<td>94.2</td>
<td>541</td>
<td>94.1</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>32</td>
<td>549</td>
<td>82.5</td>
<td>540</td>
<td>84.0</td>
<td>546</td>
<td>83.0</td>
<td>32</td>
<td>472</td>
<td>96.1</td>
<td>473</td>
<td>95.8</td>
<td>474</td>
<td>95.7</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>32</td>
<td>360</td>
<td>144</td>
<td>360</td>
<td>144</td>
<td>359</td>
<td>144</td>
<td>32</td>
<td>362</td>
<td>143</td>
<td>360</td>
<td>144</td>
<td>360</td>
<td>144</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>32</td>
<td>650</td>
<td>64.6</td>
<td>649</td>
<td>64.7</td>
<td>652</td>
<td>64.4</td>
<td>32</td>
<td>648</td>
<td>64.8</td>
<td>650</td>
<td>64.6</td>
<td>651</td>
<td>64.5</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>32</td>
<td>283</td>
<td>119</td>
<td>282</td>
<td>120</td>
<td>279</td>
<td>121</td>
<td>32</td>
<td>263</td>
<td>128</td>
<td>263</td>
<td>128</td>
<td>261</td>
<td>129</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>32</td>
<td>263</td>
<td>213</td>
<td>261</td>
<td>215</td>
<td>260</td>
<td>215</td>
<td>32</td>
<td>251</td>
<td>223</td>
<td>251</td>
<td>223</td>
<td>250</td>
<td>224</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>32</td>
<td>402</td>
<td>91.3</td>
<td>402</td>
<td>91.2</td>
<td>402</td>
<td>91.3</td>
<td>32</td>
<td>402</td>
<td>91.2</td>
<td>402</td>
<td>91.3</td>
<td>403</td>
<td>91.1</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>32</td>
<td>615</td>
<td>86.2</td>
<td>616</td>
<td>86.0</td>
<td>620</td>
<td>85.5</td>
<td>32</td>
<td>620</td>
<td>85.4</td>
<td>615</td>
<td>86.2</td>
<td>625</td>
<td>84.7</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>32</td>
<td>376</td>
<td>223</td>
<td>376</td>
<td>223</td>
<td>376</td>
<td>223</td>
<td>32</td>
<td>376</td>
<td>223</td>
<td>377</td>
<td>223</td>
<td>376</td>
<td>223</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>32</td>
<td>508</td>
<td>68.1</td>
<td>509</td>
<td>67.9</td>
<td>508</td>
<td>68.0</td>
<td>32</td>
<td>507</td>
<td>68.1</td>
<td>507</td>
<td>68.1</td>
<td>507</td>
<td>68.2</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

(Continued on next page)
Dell Inc.

PowerEdge FC640 (Intel Xeon Gold 5217, 3.00GHz)

**CPU2017 License:** 55
**Test Sponsor:** Dell Inc.
**Test Date:** Sep-2019
**Tested by:** Dell Inc.

**SPECrate®2017_int_base = 107**

**SPECrate®2017_int_peak = 111**

**Test Hardware Availability:** Apr-2019
**Software Availability:** Sep-2019

---

**General Notes (Continued)**

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

---

**Platform Notes**

BIOS settings:
- ADDDC setting disabled
- Sub NUMA Cluster enabled
- Virtualization Technology disabled
- DCU Streamer Prefetcher enabled
- System Profile set to Custom
- CPU Performance set to Maximum Performance
- C States set to Autonomous
- C1E disabled
- Uncore Frequency set to Dynamic
- Energy Efficiency Policy set to Performance
- Memory Patrol Scrub disabled
- Logical Processor enabled
- CPU Interconnect Bus Link Power Management disabled
- PCI ASPM L1 Link Power Management disabled
- Sysinfo program /home/cpu2017/bin/sysinfo
- Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
- running on intel-sut Fri Sep 13 21:07:30 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) Gold 5217 CPU @ 3.00GHz
  2 "physical id"s (chips)
  32 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 8
  siblings : 16
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 32
- On-line CPU(s) list: 0-31

(Continued on next page)
### SPEC CPU®2017 Integer Rate Result

**Dell Inc.**

**PowerEdge FC640 (Intel Xeon Gold 5217, 3.00GHz)**

<table>
<thead>
<tr>
<th>CPU2017 License: 55</th>
<th>Test Date: Sep-2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Dell Inc.</td>
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<tr>
<th>SPECrate®2017_int_base = 107</th>
<th>SPECrate®2017_int_peak = 111</th>
</tr>
</thead>
</table>

#### Platform Notes (Continued)

- Thread(s) per core: 2
- Core(s) per socket: 8
- Socket: 2
- NUMA node(s): 2
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Gold 5217 CPU @ 3.00GHz
- Stepping: 6
- CPU MHz: 3343.171
- BogoMIPS: 6000.00
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 1024K
- L3 cache: 11264K
- NUMA node0 CPU(s): 0, 2, 4, 6, 8, 10, 12, 14, 16, 18, 20, 22, 24, 26, 28, 30
- NUMA node1 CPU(s): 1, 3, 5, 7, 9, 11, 13, 15, 17, 19, 21, 23, 25, 27, 29, 31
- Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpellgb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_pplin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vmni flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erva invpcid rtm cqm mxp rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xsaveopt xsave vts vmp-services cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local dthrm ida arat pln pts pku ospe avx512_vnni md_clear flush_lld arch_capabilities

/proc/cpuinfo cache data
cache size : 11264 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

- available: 2 nodes (0-1)
- node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30
- node 0 size: 192836 MB
- node 0 free: 192331 MB
- node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31
- node 1 size: 193532 MB
- node 1 free: 193080 MB

node distances:

- node 0 1
  - 0: 10 21
  - 1: 21 10

(Continued on next page)
Platform Notes (Continued)

From /proc/meminfo
    MemTotal:        395641912 kB
    HugePages_Total:       0
    Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
    Ubuntu 18.04.2 LTS

From /etc/*release* /etc/*version*
    debian_version: buster/sid
    os-release:
        NAME="Ubuntu"
        VERSION="18.04.2 LTS (Bionic Beaver)"
        ID=ubuntu
        ID_LIKE=debian
        PRETTY_NAME="Ubuntu 18.04.2 LTS"
        VERSION_ID="18.04"
        HOME_URL="https://www.ubuntu.com/"
        SUPPORT_URL="https://help.ubuntu.com/"

uname -a:
    Linux intel-sut 4.15.0-62-generic #69-Ubuntu SMP Wed Sep 4 20:55:53 UTC 2019 x86_64
    x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user
    pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB
    filling

run-level 5 Sep 13 21:05

SPEC is set to: /home/cpu2017
    Filesystem Type Size Used Avail Use% Mounted on
    /dev/sda2  ext4  439G  35G  382G   9% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
    BIOS Dell Inc. 2.2.11 06/14/2019
    Memory:
        3x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933, configured at 2666
        6x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933, configured at 2666

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Dell Inc.

PowerEdge FC640 (Intel Xeon Gold 5217, 3.00GHz)

SPECrate®2017_int_base = 107
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CPU2017 License: 55
Test Sponsor: Dell Inc.
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Test Date: Sep-2019
Hardware Availability: Apr-2019
Software Availability: Sep-2019

Platform Notes (Continued)

3x 00AD069D00AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933, configured at 2666
4x Not Specified Not Specified

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 502.gcc_r(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version
19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
       | 525.x264_r(base, peak) 557.xz_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
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==============================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
       | 525.x264_r(base, peak) 557.xz_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C++     | 523.xalancbmk_r(peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version
19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

(Continued on next page)
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Compiler Version Notes (Continued)

Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
     | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
==============================================================================
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
   Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
C++ | 523.xalancbmk_r(peak)
==============================================================================
Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version
   19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
     | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
==============================================================================
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
   Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
Fortran | 548.exchange2_r(base, peak)
==============================================================================
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
   64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
  icc -m64 -std=c11

C++ benchmarks:
  icpc -m64

(Continued on next page)
Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_Linux_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-W1, -z, muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

C++ benchmarks:
-W1, -z, muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-W1, -z, muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64 -std=c11

(Continued on next page)
Dell Inc. PowerEdge FC640 (Intel Xeon Gold 5217, 3.00GHz)

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Tested by: Dell Inc.
Software Availability: Sep-2019

Peak Compiler Invocation (Continued)


C++ benchmarks (except as noted below):
icpc -m64

523.xalancbmk_r: icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64

(Continued on next page)
Peak Optimization Flags (Continued)

505.mcf_r (continued):
-1qkmalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-1qkmalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-1qkmalloc

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-1qkmalloc

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

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