Lenovo Global Technology
ThinkSystem SR950
(2.50 GHz, Intel Xeon Gold 6248)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

<table>
<thead>
<tr>
<th>Threads</th>
<th>603.bwaves_s</th>
<th>607.cactuBSSN_s</th>
<th>619.lbm_s</th>
<th>621.wrf_s</th>
<th>627.cam4_s</th>
<th>628.pop2_s</th>
<th>638.imagick_s</th>
<th>644.nab_s</th>
<th>649.fotonik3d_s</th>
<th>654.roms_s</th>
</tr>
</thead>
<tbody>
<tr>
<td>80</td>
<td>205</td>
<td>214</td>
<td>225</td>
<td>226</td>
<td>227</td>
<td>228</td>
<td>229</td>
<td>230</td>
<td>231</td>
<td>232</td>
</tr>
</tbody>
</table>

**Hardware**

CPU Name: Intel Xeon Gold 6248
Max MHz: 3900
Nominal: 2500
Enabled: 80 cores, 4 chips
Orderable: 2,3,4 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 27.5 MB I+D on chip per chip
Other: None
Memory: 768 GB (48 x 16 GB 2Rx8 PC4-2933Y-R)
Storage: 1 x 800 GB SATA SSD
Other: None

**Software**

OS: SUSE Linux Enterprise Server 15 (x86_64)
Compiler: C/C++: Version 19.0.4.227 of Intel
Compiler for Linux;
Fortran: Version 19.0.4.227 of Intel Fortran
Compiler for Linux
Parallel: Yes
Firmware: Lenovo BIOS Version PSE122R 1.53 released Aug-2019 tested as PSE121R 1.53 Jul-2019
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: None
Power Management: BIOS set to prefer performance at the cost of additional power usage

**SPECspeed®2017_fp_base = 203**

**SPECspeed®2017_fp_peak = Not Run**
## SPEC CPU®2017 Floating Point Speed Result

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**Test Date:** Dec-2019  
**Hardware Availability:** Apr-2019  
**Software Availability:** May-2019

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>80</td>
<td>65.9</td>
<td>895</td>
<td>66.5</td>
<td>887</td>
<td>65.5</td>
<td>901</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>80</td>
<td>81.5</td>
<td>205</td>
<td>80.6</td>
<td>207</td>
<td>81.1</td>
<td>205</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>80</td>
<td>35.0</td>
<td>150</td>
<td>34.9</td>
<td>150</td>
<td>33.7</td>
<td>155</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>80</td>
<td>90.6</td>
<td>146</td>
<td>90.5</td>
<td>146</td>
<td><strong>90.6</strong></td>
<td>146</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>80</td>
<td>57.8</td>
<td>153</td>
<td>58.3</td>
<td>152</td>
<td><strong>58.2</strong></td>
<td>152</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>80</td>
<td>183</td>
<td>64.8</td>
<td>186</td>
<td>64.0</td>
<td><strong>185</strong></td>
<td>64.3</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>80</td>
<td>70.4</td>
<td>205</td>
<td>67.3</td>
<td>214</td>
<td>66.7</td>
<td>216</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>80</td>
<td>43.5</td>
<td>401</td>
<td><strong>43.6</strong></td>
<td>401</td>
<td>43.6</td>
<td>401</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>80</td>
<td>86.0</td>
<td>106</td>
<td>85.6</td>
<td>106</td>
<td><strong>85.8</strong></td>
<td>106</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>80</td>
<td>48.5</td>
<td>325</td>
<td><strong>47.7</strong></td>
<td>330</td>
<td>47.5</td>
<td>331</td>
</tr>
</tbody>
</table>

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

- `KMP_AFFINITY = "granularity=fine,compact"
- `LD_LIBRARY_PATH = "/home/cpu2017-1.1.0-ic19.0u4/lib/intel64"
- `OMP_STACKSIZE = "192M"

### General Notes

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages disabled by default:
- echo never > /sys/kernel/mm/transparent_hugepage/enabled
- echo never > /sys/kernel/mm/transparent_hugepage/defrag

Prior to runcpu invocation:

Filesystem page cache synced and cleared with:
- sync; echo 3> /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a)

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR950
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General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode
CPU P-state Control set to Autonomous
Hyper-Threading set to Disable
Trusted Execution Technology set to Enable
DCU Streamer Prefetcher set to Disable
MONITOR/MWAIT set to Enable

Sysinfo program /home/cpu2017-1.1.0-ic19.0u4/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edbbe6e46a485a0011
running on linux-ulb8 Thu Dec 5 13:05:40 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6248 CPU @ 2.50GHz
 4 "physical id"'s (chips)
 80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 20
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 2: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 3: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 80
On-line CPU(s) list: 0-79
Thread(s) per core: 1
Core(s) per socket: 20
Socket(s): 4
NUMA node(s): 4
Vendor ID: GenuineIntel

(Continued on next page)
## Lenovo Global Technology

**ThinkSystem SR950**
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<table>
<thead>
<tr>
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<th>203</th>
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<tbody>
<tr>
<td>SPECspeed®2017_fp_peak =</td>
<td>Not Run</td>
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<td>Test Date:</td>
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<td>Hardware Availability:</td>
<td>Apr-2019</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>May-2019</td>
</tr>
</tbody>
</table>

### Platform Notes (Continued)

- **CPU family:** 6
- **Model:** 85
- **Model name:** Intel(R) Xeon(R) Gold 6248 CPU @ 2.50GHz
- **Stepping:** 6
- **CPU MHz:** 2500.000
- **CPU max MHz:** 3900.0000
- **CPU min MHz:** 1000.0000
- **BogoMIPS:** 5000.00
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 1024K
- **L3 cache:** 28160K
- **NUMA node0 CPU(s):** 0-19
- **NUMA node1 CPU(s):** 20-39
- **NUMA node2 CPU(s):** 40-59
- **NUMA node3 CPU(s):** 60-79

### Flags:
- fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
- pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
- lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
- aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
- xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
- avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
- invpcid_single ssbd mba ibrs ibpb tpr_shadow vnmi flexpriority ept vpid
- fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f
- avx512dq rdseed adx smap clflushopt clwb intel_pt avx512vd avx512bw avx512vl
- xsavesopt xsaveopt xsave xsetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local
- dtherm ida arat pln pts pku ospke avx512_vnni flush_l1d arch_capabilities

From numactl --hardware

```bash
WARNING: a numactl 'node' might or might not correspond to a physical chip.
```

```
    available: 4 nodes (0-3)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19
    node 0 size: 193116 MB
    node 0 free: 189647 MB
    node 1 cpus: 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39
    node 1 size: 193515 MB
    node 1 free: 192898 MB
    node 2 cpus: 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59
    node 2 size: 193486 MB
    node 2 free: 192476 MB
    node 3 cpus: 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79
    node 3 size: 193512 MB
    node 3 free: 193050 MB
```

(Continued on next page)
Lenovo Global Technology
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SPECspeed®2017_fp_base = 203
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Platform Notes (Continued)

node distances:
node  0  1  2  3
0:  10  21  21  21
1:  21  10  21  21
2:  21  21  10  21
3:  21  21  21  10

From /proc/meminfo
MemTotal:       792197400 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
NAME="SLES"
VERSION="15"
VERSION_ID="15"
PRETTY_NAME="SUSE Linux Enterprise Server 15"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
    x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-3620 (L1 Terminal Fault):        Not affected
Microarchitectural Data Sampling:         No status reported
CVE-2017-5754 (Meltdown):                  Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
                                          via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):        Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):        Mitigation: Indirect Branch Restricted
                                          Speculation, IBPB, IBRS_FW

run-level 3 Dec 5 11:37

SPEC is set to: /home/cpu2017-1.1.0-ic19.0u4

From /sys/devices/virtual/dmi/id
BIOS:    Lenovo -[PSE121R-1.53]- 07/03/2019
Vendor:   Lenovo

(Continued on next page)
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Hardware Availability: Apr-2019
Software Availability: May-2019

Platform Notes (Continued)

Product: ThinkSystem SR950 -[7X12ABC1WW]-
Product Family: ThinkSystem
Serial: none

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
48x NO DIMM NO DIMM
48x Samsung M393A2K43CB2-CVF 16 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C               | 619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

C++, C, Fortran | 607.cactuBSSN_s(base)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Fortran         | 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
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Compiler Version Notes (Continued)

Fortran, C | 621.wrf_s(base) 627.cam4_s(base) 628.pop2_s(base)
-------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
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Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-F.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-F.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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