### SPEC CPU®2017 Integer Speed Result

**Fujitsu**

**PRIMERGY TX1330 M4, Intel Xeon E-2244G, 3.80 GHz**

- **CPU2017 License:** 19
- **Test Sponsor:** Fujitsu
- **Tested by:** Fujitsu
- **Test Date:** Nov-2019
- **Hardware Availability:** Oct-2019
- **Software Availability:** Sep-2019

#### Hardware

<table>
<thead>
<tr>
<th>Function</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon E-2244G</td>
</tr>
<tr>
<td>Max MHz</td>
<td>4800</td>
</tr>
<tr>
<td>Nominal</td>
<td>3800</td>
</tr>
<tr>
<td>Enabled</td>
<td>4 cores, 1 chip, 2 threads/core</td>
</tr>
<tr>
<td>Orderable</td>
<td>1 chip</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2</td>
<td>256 KB I+D on chip per core</td>
</tr>
<tr>
<td>L3</td>
<td>8 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>64 GB (4 x 16 GB 2Rx8 PC4-2666V-E)</td>
</tr>
<tr>
<td>Storage</td>
<td>1 x SATA M.2 SSD, 480 GB</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

#### Software

<table>
<thead>
<tr>
<th>Function</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS</td>
<td>Red Hat Enterprise Linux Server release 7.6</td>
</tr>
<tr>
<td></td>
<td>(Maipo) 3.10.0-957.el7.x86_64</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 19.0.5.281 of C/C++ Compiler for Linux; Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td>Parallel</td>
<td>Yes</td>
</tr>
<tr>
<td>Firmware</td>
<td>Fujitsu BIOS Version V5.0.0.13 R1.12.0 for D3673-A1x. Released Sep-2019</td>
</tr>
<tr>
<td>File System</td>
<td>xfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Other</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management</td>
<td>BIOS set to prefer performance at the cost of additional power usage</td>
</tr>
</tbody>
</table>

### SPECspeed®2017_int_base = 11.3

### SPECspeed®2017_int_peak = 11.6
Fujitsu
PRIMERGY TX1330 M4, Intel Xeon E-2244G, 3.80 GHz

SPECspeed®2017_int_base = 11.3
SPECspeed®2017_int_peak = 11.6

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perbench_s</td>
<td>8</td>
<td>230</td>
<td>7.73</td>
<td>228</td>
<td>7.78</td>
<td>229</td>
<td>7.73</td>
<td></td>
<td></td>
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</tr>
<tr>
<td>602.gcc_s</td>
<td>8</td>
<td>323</td>
<td>12.3</td>
<td>322</td>
<td>12.3</td>
<td>324</td>
<td>12.3</td>
<td>8</td>
<td>314</td>
<td>12.7</td>
<td>313</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>8</td>
<td>285</td>
<td>16.5</td>
<td>287</td>
<td>16.5</td>
<td>288</td>
<td>16.4</td>
<td>8</td>
<td>284</td>
<td>16.6</td>
<td>284</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>8</td>
<td>224</td>
<td>7.27</td>
<td>225</td>
<td>7.25</td>
<td>226</td>
<td>7.21</td>
<td>8</td>
<td>222</td>
<td>7.34</td>
<td>223</td>
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<tr>
<td>623.xalanchmk_s</td>
<td>8</td>
<td>92.0</td>
<td>15.4</td>
<td>91.4</td>
<td>15.5</td>
<td>91.4</td>
<td>15.5</td>
<td>8</td>
<td>92.0</td>
<td>15.4</td>
<td>91.4</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>8</td>
<td>97.4</td>
<td>18.1</td>
<td>97.4</td>
<td>18.1</td>
<td>97.5</td>
<td>18.1</td>
<td>8</td>
<td>97.4</td>
<td>18.1</td>
<td>97.5</td>
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<tr>
<td>631.deepsjeng_s</td>
<td>8</td>
<td>207</td>
<td>6.93</td>
<td>207</td>
<td>6.92</td>
<td>207</td>
<td>6.93</td>
<td>8</td>
<td>207</td>
<td>6.93</td>
<td>207</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>8</td>
<td>294</td>
<td>5.81</td>
<td>294</td>
<td>5.81</td>
<td>295</td>
<td>5.78</td>
<td>8</td>
<td>294</td>
<td>5.81</td>
<td>295</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>8</td>
<td>139</td>
<td>21.1</td>
<td>140</td>
<td>21.1</td>
<td>140</td>
<td>21.1</td>
<td>8</td>
<td>139</td>
<td>21.1</td>
<td>140</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>8</td>
<td>476</td>
<td>13.0</td>
<td>476</td>
<td>13.0</td>
<td>476</td>
<td>13.0</td>
<td>8</td>
<td>463</td>
<td>13.4</td>
<td>463</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Kernel Boot Parameter set with: nohz_full=1-15

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/Benchmark/speccpu2017-1.1.0/lib/intel64:/home/Benchmark/speccpu2017-1.1.0/je5.0.1-64"
OMP_STACKSIZE = "192M"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/Benchmark/speccpu2017-1.1.0/lib/intel64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Xeon E-2288G CPU + 64 GB RAM memory using Redhat Enterprise Linux 7.6
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Files system page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5

(Continued on next page)
General Notes (Continued)

jemalloc: sources available via jemalloc.net
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Adjacent Cache Line Prefetch = Disabled
C-State Pre-Wake = Disabled
DCU Streamer Prefetcher = Disabled
DDR PowerDown and idle counter = PCODE
Energy Efficient Turbo = Disabled
Enhanced C-states = Disabled
Intel Virtualization Technology = Disabled
Native ASPM = Disabled
Package C-State un-demotion = Enabled
REFRESH_2X_MODE = 1-Enabled for WARM or HOT

Sysinfo program /home/Benchmark/speccpu2017-1.1.0/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edhle6e46a485a0011
running on localhost.localdomain Sat Nov 30 07:25:21 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) E-2244G CPU @ 3.80GHz
  1 "physical id"s (chips)
  8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 8
physical 0: cores 0 1 2 3

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 8
On-line CPU(s) list: 0-7

(Continued on next page)
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<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>11.3</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_int_peak</td>
<td>11.6</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 19

**Test Sponsor:** Fujitsu

**Test Date:** Nov-2019

**Hardware Availability:** Oct-2019

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**Software Availability:** Sep-2019

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**Platform Notes (Continued)**

- Thread(s) per core: 2
- Core(s) per socket: 4
- Socket(s): 1
- NUMA node(s): 1
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 158
- Model name: Intel(R) Xeon(R) E-2244G CPU @ 3.80GHz
- Stepping: 10
- CPU MHz: 4502.062
- CPU max MHz: 4800.0000
- CPU min MHz: 800.0000
- BogoMIPS: 7584.00
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 256K
- L3 cache: 8192K
- NUMA node0 CPU(s): 0-7
- Flags: fpu vme de pse mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb intel_pt ssbd ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm rdsese adx smap clflushopt xsaveopt xsavec xgetbv1 dtherm ida arat pln pts hwp hwp_notify hwp_act_window hwp_epp spec_ctrl intel_stibp flush_l1d

/proc/cpuinfo cache data

```
  cache size : 8192 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

From /proc/meminfo

```
  MemTotal: 65725060 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB
```

From /etc/*release* /etc/*version*

```
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.6 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VARIANT="Server"
```

(Continued on next page)
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| SPECspeed®2017_int_base = 11.3 | SPECspeed®2017_int_peak = 11.6 |
---|---|
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Platform Notes (Continued)

```
VARIANT_ID="server"
VERSION_ID="7.6"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.6 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)

uname -a:
Linux localhost.localdomain 3.10.0-957.el7.x86_64 #1 SMP Thu Oct 4 20:48:51 UTC 2018
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-3620 (L1 Terminal Fault): Mitigation: PTE Inversion
Microarchitectural Data Sampling: No status reported
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS (kernel)

run-level 3 Nov 30 07:24
SPEC is set to: /home/Benchmark/speccpu2017-1.1.0
Filesystem            Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   392G   33G  359G   9% /home

From /sys/devices/virtual/dmi/id
BIOS: FUJITSU // American Megatrends Inc. V5.0.0.13 R1.12.0 for D3673-A1x
      09/06/2019
Vendor: FUJITSU
Product: PRIMERGY TX1330 M4
Serial: YMJLXXXXXXXX

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMIOS" standard.
Memory:
  4x SK Hynix HMA82GU7CJR8N-VK 16 GB 2 rank 2667

(End of data from sysinfo program)
```
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SPECspeed\textsuperscript{®}2017\_int\_peak = 11.6

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Compiler Version Notes

C
\begin{tabular}{l|llllllll}
600.perlbench\_s(base, peak) & 602.gcc\_s(base, peak) & 605.mcf\_s(base, peak) & 625.x264\_s(base, peak) & 657.xz\_s(base, peak) \\
\end{tabular}

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.5.281 Build 20190815
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

\begin{tabular}{l|llllllll}
620.omnetpp\_s(base, peak) & 623.xalancbmk\_s(base, peak) & 631.deepsjeng\_s(base, peak) & 641.leela\_s(base, peak) \\
\end{tabular}

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.5.281 Build 20190815
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

\begin{tabular}{l|llllllll}
648.exchange2\_s(base, peak) \\
\end{tabular}

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.5.281 Build 20190815
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
\texttt{icc -m64 -std=c11}

C++ benchmarks:
\texttt{icpc -m64}

Fortran benchmarks:
\texttt{ifort -m64}

Base Portability Flags

\begin{tabular}{ll}
600.perlbench\_s: & -DSPEC\_LP64 -DSPEC\_LINUX\_X64 \\
602.gcc\_s: & -DSPEC\_LP64 \\
605.mcf\_s: & -DSPEC\_LP64 \\
620.omnetpp\_s: & -DSPEC\_LP64 \\
\end{tabular}

(Continued on next page)
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Base Portability Flags (Continued)

623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Peak Portability Flags

Same as Base Portability Flags
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Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -fno-strict-overflow
-stdlib/hw/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP
-stdlib/hw/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O3
-xCORE-AVX2 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP
-stdlib/hw/je5.0.1-64/lib -ljemalloc

625.x264_s: basepeak = yes

657.xz_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -stdlib/hw/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP
-stdlib/hw/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-stdlib/hw/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes
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The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0.2-CFL-RevD.xml](http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0.2-CFL-RevD.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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