# SPEC CPU®2017 Integer Speed Result

**Fujitsu**

PRIMERGY TX1330 M4, Intel Celeron G4930, 3.20 GHz  

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base = 5.81</th>
<th>SPECspeed®2017_int_peak = 6.10</th>
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</table>

**CPU2017 License:** 19  
**Test Sponsor:** Fujitsu  
**Tested by:** Fujitsu  
**Test Date:** Dec-2019  
**Hardware Availability:** Oct-2019  
**Software Availability:** Sep-2019

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## Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Celeron G4930</th>
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<tr>
<td>Max MHz:</td>
<td>3200</td>
</tr>
<tr>
<td>Nominal:</td>
<td>3200</td>
</tr>
<tr>
<td>Enabled:</td>
<td>2 cores, 1 chip</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1 chip</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 32 KB D on chip per core</td>
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<tr>
<td>L2:</td>
<td>256 KB I+D on chip per core</td>
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<td>L3:</td>
<td>2 MB I+D on chip per chip</td>
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<tr>
<td>Other:</td>
<td>None</td>
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<tr>
<td>Memory:</td>
<td>64 GB (4 x 16 GB 2Rx8 PC4-2666V-E, running at 2400)</td>
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<tr>
<td>Storage:</td>
<td>1 x SATA M.2 SSD, 480 GB</td>
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<tr>
<td>Other:</td>
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## Software

<table>
<thead>
<tr>
<th>OS:</th>
<th>Red Hat Enterprise Linux Server release 7.6 (Maipo)</th>
</tr>
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<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 19.0.5.281 of Intel C/C++ Compiler for Linux; Fortran: Version 19.0.5.281 of Intel Fortran Compiler for Linux</td>
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<tr>
<td>Parallel:</td>
<td>Yes</td>
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<tr>
<td>Firmware:</td>
<td>Fujitsu BIOS Version V5.0.0.13 R1.12.0 for D3673-A1x. Released Sep-2019</td>
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<tr>
<td>File System:</td>
<td>xfs</td>
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<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
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<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>32/64-bit</td>
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<tr>
<td>Other:</td>
<td>jemalloc memory allocator V5.0.1</td>
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<tr>
<td>Power Management:</td>
<td>BIOS set to prefer performance at the cost of additional power usage</td>
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</tbody>
</table>
**SPEC CPU®2017 Integer Speed Result**

**Fujitsu**

PRIMERGY TX1330 M4, Intel Celeron G4930, 3.20 GHz

**SPECspeed®2017_int_base = 5.81**

**SPECspeed®2017_int_peak = 6.10**

---

### Results Table

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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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</table>

**SPECspeed®2017_int_base = 5.81**

**SPECspeed®2017_int_peak = 6.10**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Kernel Boot Parameter set with: nohz_full=1-15

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = 
"/home/Benchmark/speccpu2017-1.1.0/lib/ia32;/home/Benchmark/speccpu2017-1.1.0/lib/intel64;/home/Benchmark/speccpu2017-1.1.0/je5.0.1-32;/home/Benchmark/speccpu2017-1.1.0/je5.0.1-64"
OMP_STACKSIZE = "192M"

### General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = 
"/home/Benchmark/speccpu2017-1.1.0/lib/intel64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Xeon E-2288G CPU + 64 GB RAM memory using Redhat Enterprise Linux 7.6
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

(Continued on next page)
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Fujitsu

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SPECspeed®2017_int_peak = 6.10

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

General Notes (Continued)
jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5
jemalloc: sources available via jemalloc.net
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Adjacent Cache Line Prefetch = Disabled
C-State Pre-Wake = Disabled
DCU Streamer Prefetcher = Disabled
DDR PowerDown and idle counter = PCODE
Energy Efficient Turbo = Disabled
Enhanced C-states = Disabled
Intel Virtualization Technology = Disabled
Native ASPM = Disabled
Package C-State un-demotion = Enabled
REFRESH_2X_MODE = 1-Enabled for WARM or HOT

Sysinfo program /home/Benchmark/speccpu2017-1.1.0/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7eddb1e6e46a485a0011
running on localhost.localdomain Mon Dec 16 09:19:44 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Celeron(R) G4930 CPU @ 3.20GHz
  1 "physical id"s (chips)
  2 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 2
siblings : 2
physical 0: cores 0 1

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian

(Continued on next page)
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CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Dec-2019
Hardware Availability: Oct-2019
Software Availability: Sep-2019

Platform Notes (Continued)

- CPU(s): 2
- On-line CPU(s) list: 0,1
- Thread(s) per core: 1
- Core(s) per socket: 2
- Socket(s): 1
- NUMA node(s): 1
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 158
- Model name: Intel(R) Celeron(R) G4930 CPU @ 3.20GHz
- Stepping: 11
- CPU MHz: 3200.000
- CPU max MHz: 3200.0000
- CPU min MHz: 800.0000
- BogoMIPS: 6384.00
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 256K
- L3 cache: 2048K
- NUMA node0 CPU(s): 0,1
- Flags: fpu vme de pse mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
  aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 sdbg xsr x2apic movbe popcnt tsc_deadline_timer aes xsave xsaves lbm_aba 3nowprefetch ecp intel_pt ssbd ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust smep rdsuid smap clflushopt xsaveopt xsavec xgetbv1 dtherm arat pln pts hwp hwp_notify hwp_act_window hwp_epp spec_ctrl intel_stibp flush_l1d

/proc/cpuinfo cache data
  cache size : 2048 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

From /proc/meminfo
  MemTotal: 65726088 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.6 (Maipo)"
    ID="rheil"

(Continued on next page)
Platform Notes (Continued)

ID_LIKE="fedora"
VARIANT="Server"
VARIANT_ID="server"
VERSION_ID="7.6"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.6 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)

uname -a:
Linux localhost.localdomain 3.10.0-957.el7.x86_64 #1 SMP Thu Oct 4 20:48:51 UTC 2018
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault): Mitigation: PTE Inversion
Microarchitectural Data Sampling: No status reported
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS (kernel)

run-level 3 Dec 16 09:18

SPEC is set to: /home/Benchmark/speccpu2017-1.1.0
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 392G 44G 349G 12% /home

From /sys/devices/virtual/dmi/id
BIOS: FUJITSU // American Megatrends Inc. V5.0.0.13 R1.12.0 for D3673-A1x
09/06/2019
Vendor: FUJITSU
Product: PRIMERGY TX1320 M4
Serial: YMJKXXXXXX

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
4x SK Hynix HMA82GU7CJR8N-VK 16 GB 2 rank 2667, configured at 2400

(End of data from sysinfo program)
Fujitsu
PRIMERGY TX1330 M4, Intel Celeron G4930, 3.20 GHz

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Copyright 2017-2020 Standard Performance Evaluation Corporation

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CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Dec-2019
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Compiler Version Notes
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<table>
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<tr>
<th>C</th>
<th>600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)</th>
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<tbody>
<tr>
<td>Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.5.281 Build 20190815</td>
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<tr>
<td>Copyright (C) 1985-2019 Intel Corporation. All rights reserved.</td>
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<th>620.omnetpp_s(base, peak) 623.xalancbmk_s(base) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)</th>
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==============================================================================
<table>
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<tr>
<th>Fortran</th>
<th>648.exchange2_s(base, peak)</th>
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<td>Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)</td>
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Compiler Version Notes (Continued)

64, Version 19.0.5.281 Build 20190815
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Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-W1,-z,muldefs -xSSE4.2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-1/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-W1,-z,muldefs -xSSE4.2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -1/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-W1,-z,muldefs -xSSE4.2 -ipo -O3 -no-prec-div

(Continued on next page)
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Tested by: Fujitsu
Test Date: Dec-2019
Hardware Availability: Oct-2019
Software Availability: Sep-2019

Base Optimization Flags (Continued)

Fortran benchmarks (continued):
- qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
- L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks (except as noted below):
icpc -m64

623.xalancbmk_s: icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.0.117/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xSSE4.2 -qopt-mem-layout-trans=3 -ipo -O3 -no-prec-div
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Fujitsu

PRIMERGY TX1330 M4, Intel Celeron G4930, 3.20 GHz

| SPECspeed®2017_int_base = 5.81 |
| SPECspeed®2017_int_peak = 6.10 |

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Dec-2019
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Peak Optimization Flags (Continued)

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xSSE4.2 -qopt-mem-layout-trans=3 -ipo -O3 -no-prec-div
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xSSE4.2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: basepeak = yes

657.xz_s: Same as 602.gcc_s

C++ benchmarks:

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xSSE4.2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

623.xalancbmk_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xSSE4.2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-32/lib -ljemalloc

631.deepsjeng_s: Same as 620.omnetpp_s

641.leela_s: Same as 620.omnetpp_s

Fortran benchmarks:

-Wl,-z,muldefs -xSSE4.2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0.2-CFL-RevD.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0.2-CFL-RevD.xml
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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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