Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen10
(3.90 GHz, Intel Xeon Gold 6250)

SPECrated®2017_int_base = 139
SPECrated®2017_int_peak = 144

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Feb-2020
Hardware Availability: Feb-2020
Software Availability: Jun-2019

Hardware
CPU Name: Intel Xeon Gold 6250
Max MHz: 4500
Nominal: 3900
Enabled: 16 cores, 2 chips, 2 threads/core
Orderable: 1, 2 chip(s)
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 35.75 MB I+D on chip per chip
Other: None
Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)
Storage: 1 x 400 GB SAS SSD
Other: None

Software
OS: SUSE Linux Enterprise Server 15 SP1 (x86_64)
Kernel 4.12.14-195-default
Compiler: C/C++: Version 19.0.4.227 of Intel C/C++
Compiler Build 20190416 for Linux;
Fortran: Version 19.0.4.227 of Intel Fortran
Compiler Build 20190416 for Linux
Parallel: No
Firmware: HPE BIOS Version U30 2.22 (11/13/2019) released Feb-2020
File System: btrfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of additional power usage
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>32</td>
<td>495</td>
<td>103</td>
<td>492</td>
<td>104</td>
<td>493</td>
<td>103</td>
<td>493</td>
<td>103</td>
<td>492</td>
<td>104</td>
<td>493</td>
<td>103</td>
<td></td>
<td></td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>32</td>
<td>392</td>
<td>116</td>
<td>391</td>
<td>116</td>
<td>393</td>
<td>115</td>
<td>394</td>
<td>115</td>
<td>391</td>
<td>116</td>
<td>393</td>
<td>115</td>
<td></td>
<td></td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>32</td>
<td>282</td>
<td>183</td>
<td>282</td>
<td>183</td>
<td>283</td>
<td>183</td>
<td>283</td>
<td>183</td>
<td>282</td>
<td>183</td>
<td>283</td>
<td>183</td>
<td></td>
<td></td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>32</td>
<td>509</td>
<td>82.6</td>
<td>508</td>
<td>82.6</td>
<td>508</td>
<td>82.7</td>
<td>508</td>
<td>82.7</td>
<td>508</td>
<td>82.6</td>
<td>508</td>
<td>82.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>32</td>
<td>188</td>
<td>180</td>
<td>188</td>
<td>180</td>
<td>188</td>
<td>180</td>
<td>189</td>
<td>181</td>
<td>188</td>
<td>180</td>
<td>189</td>
<td>181</td>
<td></td>
<td></td>
</tr>
<tr>
<td>525.x264_r</td>
<td>32</td>
<td>201</td>
<td>279</td>
<td>201</td>
<td>279</td>
<td>201</td>
<td>279</td>
<td>201</td>
<td>279</td>
<td>201</td>
<td>279</td>
<td>201</td>
<td>279</td>
<td></td>
<td></td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>32</td>
<td>315</td>
<td>116</td>
<td>313</td>
<td>117</td>
<td>314</td>
<td>117</td>
<td>314</td>
<td>117</td>
<td>314</td>
<td>117</td>
<td>314</td>
<td>117</td>
<td></td>
<td></td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>32</td>
<td>305</td>
<td>275</td>
<td>304</td>
<td>276</td>
<td>304</td>
<td>275</td>
<td>304</td>
<td>276</td>
<td>304</td>
<td>276</td>
<td>304</td>
<td>276</td>
<td></td>
<td></td>
</tr>
<tr>
<td>557.xz_r</td>
<td>32</td>
<td>399</td>
<td>86.7</td>
<td>399</td>
<td>86.7</td>
<td>399</td>
<td>86.6</td>
<td>399</td>
<td>86.7</td>
<td>399</td>
<td>86.7</td>
<td>399</td>
<td>86.6</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```bash
sync; echo 3 > /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```bash
numactl --interleave=all runcpu <etc>
```

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/cpu2017/lib/intel64:/cpu2017/lib/ia32:/cpu2017/je5.0.1-32"
```

**General Notes**

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
General Notes (Continued)

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

Platform Notes

BIOS Configuration:
Thermal Configuration set to Maximum Cooling
Memory Patrol Scrubbing set to Disabled
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
Enhanced Processor Performance set to Enabled
Workload Profile set to General Throughput Compute
Workload Profile set to Custom
Minimum Processor Idle Power Core C-State set to C1E State

Sysinfo program /cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edbl6e6e46a485a0011
running on linux-3rlx Sat Feb 1 14:47:01 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6250 CPU @ 3.90GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
  siblings : 16
  physical 0: cores 3 10 13 18 19 24 28 29
  physical 1: cores 1 2 3 6 12 13 18 29

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian

(Continued on next page)
Platform Notes (Continued)

Address sizes: 46 bits physical, 48 bits virtual
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6250 CPU @ 3.90GHz
Stepping: 7
CPU MHz: 3900.000
BogoMIPS: 7800.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0-3,16-19
NUMA node1 CPU(s): 4-7,20-23
NUMA node2 CPU(s): 8-11,24-27
NUMA node3 CPU(s): 12-15,28-31
Flags: fpu vme de pse tm mtrr pae mce sse cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aerpmprefl pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrp pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_p鲲 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vmmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 ets invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsavesopt xsaves qm_xgetbv1 xsaves qm_occup_llc qm_mbb_total
cqm_mbb_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_lld
arch_capabilities

/proc/cpuinfo cache data
cache size: 36608 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 16 17 18 19
node 0 size: 96360 MB
node 0 free: 96112 MB
node 1 cpus: 4 5 6 7 20 21 22 23
node 1 size: 96736 MB

(Continued on next page)
### Platform Notes (Continued)

node 1 free: 96533 MB
node 2 cpus: 8 9 10 11 24 25 26 27
node 2 size: 96766 MB
node 2 free: 96618 MB
node 3 cpus: 12 13 14 15 28 29 30 31
node 3 size: 96765 MB
node 3 free: 96613 MB
node distances:
node 0 1 2 3
  0: 10 21 31 31
  1: 21 10 31 31
  2: 31 31 10 21
  3: 31 31 21 10

From /proc/meminfo
MemTotal: 395908892 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

```
NAME="SLES"
VERSION="15-SP1"
VERSION_ID="15.1"
PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15:sp1"
```

uname -a:
Linux linux-3rlx 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

<table>
<thead>
<tr>
<th>CVE-2018-3620 (L1 Terminal Fault)</th>
<th>Not affected</th>
</tr>
</thead>
<tbody>
<tr>
<td>Microarchitectural Data Sampling:</td>
<td>Not affected</td>
</tr>
<tr>
<td>CVE-2017-5754 (Meltdown):</td>
<td>Not affected</td>
</tr>
<tr>
<td>CVE-2018-3639 (Speculative Store Bypass):</td>
<td>Mitigation: Speculative Store Bypass disabled via prctl and seccomp</td>
</tr>
<tr>
<td>CVE-2017-5753 (Spectre variant 1):</td>
<td>Mitigation: __user pointer sanitization</td>
</tr>
<tr>
<td>CVE-2017-5715 (Spectre variant 2):</td>
<td>Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling</td>
</tr>
</tbody>
</table>

run-level 3 Feb 1 14:41
Platform Notes (Continued)

SPEC is set to: /cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 btrfs 369G 24G 345G 7% /

From /sys/devices/virtual/dmi/id
BIOS: HPE U30 11/13/2019
Vendor: HPE
Product: ProLiant DL380 Gen10
Product Family: ProLiant
Serial: 2M294204YV

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
12x UNKNOWN NOT AVAILABLE
12x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C | 502.gcc_r(peak)
Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
==============================================================================

==============================================================================
C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
| 525.x264_r(base, peak) 557.xz_r(base, peak)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
==============================================================================

==============================================================================
C | 502.gcc_r(peak)
Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
==============================================================================

(Continued on next page)
# SPEC CPU®2017 Integer Rate Result

## Hewlett Packard Enterprise

*Test Sponsor: HPE*

ProLiant DL380 Gen10
(3.90 GHz, Intel Xeon Gold 6250)

<table>
<thead>
<tr>
<th>CPU2017 License: 3</th>
<th>Test Date: Feb-2020</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: HPE</td>
<td>Hardware Availability: Feb-2020</td>
</tr>
<tr>
<td>Tested by: HPE</td>
<td>Software Availability: Jun-2019</td>
</tr>
</tbody>
</table>

### SPECrate®2017_int_base = 139

### SPECrate®2017_int_peak = 144

---

**Compiler Version Notes (Continued)**

<table>
<thead>
<tr>
<th>Language</th>
<th>Benchmark List</th>
</tr>
</thead>
<tbody>
<tr>
<td>C</td>
<td>500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak)</td>
</tr>
<tr>
<td></td>
<td>Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416 Copyright (C) 1985-2019 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>C++</td>
<td>523.xalancbmk_r(peak)</td>
</tr>
<tr>
<td></td>
<td>Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.4.227 Build 20190416 Copyright (C) 1985-2019 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>C++</td>
<td>520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)</td>
</tr>
<tr>
<td></td>
<td>Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416 Copyright (C) 1985-2019 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>C++</td>
<td>523.xalancbmk_r(peak)</td>
</tr>
<tr>
<td></td>
<td>Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.4.227 Build 20190416 Copyright (C) 1985-2019 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>C++</td>
<td>520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)</td>
</tr>
<tr>
<td></td>
<td>Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416 Copyright (C) 1985-2019 Intel Corporation. All rights reserved.</td>
</tr>
</tbody>
</table>

| Fortran  | 548.exchange2_r(base, peak) |

(Continued on next page)
The image contains a page from a SPEC CPU®2017 evaluation report. The page details the performance of Hewlett Packard Enterprise's ProLiant DL380 Gen10 server, which includes the SPEC CPU®2017 Integer Rate Result. The server is configured with a 3.90 GHz, Intel Xeon Gold 6250 processor.

The SPECrate®2017_int_base result is 139, and the SPECrate®2017_int_peak result is 144. The CPU2017 License is 3.

Compiler Version Notes (Continued):

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Base Compiler Invocation:

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags:

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags:

C benchmarks:
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64 -lqkmalloc

C++ benchmarks:
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4

(Continued on next page)
Hewlett Packard Enterprise  
(Test Sponsor: HPE)  
ProLiant DL380 Gen10  
(3.90 GHz, Intel Xeon Gold 6250)  

SPEC CPU®2017 Integer Rate Result  
Copyright 2017-2020 Standard Performance Evaluation Corporation  

SPECrate®2017_int_base = 139  
SPECrate®2017_int_peak = 144  

CPU2017 License: 3  
Test Sponsor: HPE  
Tested by: HPE  

Base Optimization Flags (Continued)  

C++ benchmarks (continued):  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64  
-lqkmalloc  

Fortran benchmarks:  
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64  
-lqkmalloc  

Peak Compiler Invocation  

C benchmarks (except as noted below):  
icc -m64 -std=c11  

502.gcc_r: icc -m32 -std=c11 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/ia32Lin  

C++ benchmarks (except as noted below):  
icpc -m64  

523.xalancbmk_r: icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/ia32Lin  

Fortran benchmarks:  
ifort -m64  

Peak Portability Flags  

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc_r: -D_FILE_OFFSET_BITS=64  
505.mcf_r: -DSPEC_LP64  
520.omnetpp_r: -DSPEC_LP64  
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX  
525.x264_r: -DSPEC_LP64  
531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64  
548.exchange2_r: -DSPEC_LP64  
557.xz_r: -DSPEC_LP64
Hewlett Packard Enterprise  
(Informal Sponsor: HPE)
ProLiant DL380 Gen10  
(3.90 GHz, Intel Xeon Gold 6250)

**SPEC CPU®2017 Integer Rate Result**

**Copyright 2017-2020 Standard Performance Evaluation Corporation**

---

**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)

ProLiant DL380 Gen10  
(3.90 GHz, Intel Xeon Gold 6250)

<table>
<thead>
<tr>
<th>CPU2017 License: 3</th>
<th>Test Date: Feb-2020</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: HPE</td>
<td>Hardware Availability: Feb-2020</td>
</tr>
<tr>
<td>Tested by: HPE</td>
<td>Software Availability: Jun-2019</td>
</tr>
</tbody>
</table>

**SPECrate®2017_int_base = 139**

**SPECrate®2017_int_peak = 144**

---

**Peak Optimization Flags**

C benchmarks:

500.perlbench
- Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
- xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
- fno-strict-overflow
- L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
- lqkmalloc

502.gcc
- Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
- xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
- L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf
- Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=4
- L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
- lqkmalloc

525.x264
- Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=4 -fno-alias
- L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
- lqkmalloc

557.xz
- basepeak = yes

C++ benchmarks:

520.omnetpp
- Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=4
- L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
- lqkmalloc

523.xalancbmk
- Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
- xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
- L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng
- Same as 520.omnetpp

541.leelu
- Same as 520.omnetpp

Fortran benchmarks:

- Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
- L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
- lqkmalloc
<table>
<thead>
<tr>
<th>SPEC CPU®2017 Integer Rate Result</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Hewlett Packard Enterprise</strong></td>
</tr>
<tr>
<td>(Test Sponsor: HPE)</td>
</tr>
<tr>
<td>ProLiant DL380 Gen10</td>
</tr>
<tr>
<td>(3.90 GHz, Intel Xeon Gold 6250)</td>
</tr>
</tbody>
</table>

| CPU2017 License: | 3 |
| Test Sponsor:   | HPE |
| Tested by:      | HPE |
| Test Date:      | Feb-2020 |
| Hardware Availability: | Feb-2020 |
| Software Availability: | Jun-2019 |

The flags files that were used to format this result can be browsed at

- [http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.html](http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.xml](http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.xml)

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-02-01 15:47:01-0500.
Report generated on 2020-03-04 16:40:37 by CPU2017 PDF formatter v6255.
Originally published on 2020-03-03.