**SPEC CPU®2017 Integer Rate Result**

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
DIT400TR-55R/55RL  
(2.10 GHz, Intel Xeon Silver 4216)

**CPU2017 License:** 006042  
**Test Date:** Feb-2020

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Cpu Name:</strong> Intel Xeon Silver 4216</td>
<td><strong>OS:</strong> CentOS Linux release 7.7.1908 (Core) 3.10.0-1062.el7.x86_64</td>
</tr>
<tr>
<td><strong>Max MHz:</strong> 3200</td>
<td><strong>Compiler:</strong> C/C++: Version 19.0.4.243 of Intel C/C++ Compiler Build 20190416 for Linux; Fortran: Version 19.0.4.243 of Intel Fortran Compiler Build 20190416 for Linux</td>
</tr>
<tr>
<td><strong>Nominal:</strong> 2100</td>
<td><strong>Parallel:</strong> No</td>
</tr>
<tr>
<td><strong>Enabled:</strong> 32 cores, 2 chips, 2 threads/core</td>
<td><strong>Firmware:</strong> Version V8.101 released Aug-2019</td>
</tr>
<tr>
<td><strong>Orderable:</strong> 1, 2 (chip)s</td>
<td><strong>File System:</strong> xfs</td>
</tr>
<tr>
<td><strong>Cache L1:</strong> 32 KB I + 32 KB D on chip per core</td>
<td><strong>System State:</strong> Run level 3 (multi-user)</td>
</tr>
<tr>
<td><strong>L2:</strong> 1 MB I+D on chip per core</td>
<td><strong>Base Pointers:</strong> 64-bit</td>
</tr>
<tr>
<td><strong>L3:</strong> 22 MB I+D on chip per chip</td>
<td><strong>Peak Pointers:</strong> 32/64-bit</td>
</tr>
<tr>
<td><strong>Other:</strong> None</td>
<td><strong>Other:</strong> jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td><strong>Memory:</strong> 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R, running at 2400)</td>
<td><strong>Power Management:</strong> Default</td>
</tr>
<tr>
<td><strong>Storage:</strong> 1 x 480 GB SSD</td>
<td></td>
</tr>
<tr>
<td><strong>Other:</strong> None</td>
<td></td>
</tr>
</tbody>
</table>

---

**SPECrater®2017_int_base = 173**  
**SPECrater®2017_int_peak = 180**
SPEC CPU®2017 Integer Rate Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
DIT400TR-55R/55RL
(2.10 GHz, Intel Xeon Silver 4216)

Copyright 2017-2020 Standard Performance Evaluation Corporation

SPECrate®2017_int_base = 173
SPECrate®2017_int_peak = 180

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>64</td>
<td>770</td>
<td>132</td>
<td>768</td>
<td>133</td>
<td>766</td>
<td>133</td>
<td>64</td>
<td>669</td>
<td>152</td>
<td>670</td>
<td>152</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>64</td>
<td>643</td>
<td>141</td>
<td>648</td>
<td>140</td>
<td>644</td>
<td>141</td>
<td>64</td>
<td>559</td>
<td>162</td>
<td>560</td>
<td>162</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>64</td>
<td>455</td>
<td>227</td>
<td>457</td>
<td>226</td>
<td>453</td>
<td>228</td>
<td>64</td>
<td>456</td>
<td>227</td>
<td>456</td>
<td>227</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>64</td>
<td>732</td>
<td>115</td>
<td>734</td>
<td>114</td>
<td>733</td>
<td>115</td>
<td>64</td>
<td>731</td>
<td>115</td>
<td>733</td>
<td>115</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>64</td>
<td>351</td>
<td>193</td>
<td>351</td>
<td>192</td>
<td>351</td>
<td>192</td>
<td>64</td>
<td>321</td>
<td>211</td>
<td>321</td>
<td>211</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>64</td>
<td>337</td>
<td>332</td>
<td>340</td>
<td>338</td>
<td>332</td>
<td></td>
<td>64</td>
<td>323</td>
<td>347</td>
<td>325</td>
<td>345</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>64</td>
<td>514</td>
<td>143</td>
<td>511</td>
<td>144</td>
<td>515</td>
<td>142</td>
<td>64</td>
<td>516</td>
<td>142</td>
<td>516</td>
<td>142</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>64</td>
<td>800</td>
<td>132</td>
<td>798</td>
<td>133</td>
<td>800</td>
<td>132</td>
<td>64</td>
<td>799</td>
<td>133</td>
<td>802</td>
<td>132</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>64</td>
<td>484</td>
<td>347</td>
<td>483</td>
<td>347</td>
<td>480</td>
<td>350</td>
<td>64</td>
<td>481</td>
<td>349</td>
<td>481</td>
<td>348</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>64</td>
<td>599</td>
<td>115</td>
<td>598</td>
<td>116</td>
<td>598</td>
<td>116</td>
<td>64</td>
<td>599</td>
<td>115</td>
<td>598</td>
<td>116</td>
</tr>
</tbody>
</table>

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
"/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
SPEC CPU®2017 Integer Rate Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
DIT400TR-55R/55RL
(2.10 GHz, Intel Xeon Silver 4216)

Copyright 2017-2020 Standard Performance Evaluation Corporation

SPECrate®2017_int_base = 173
SPECrate®2017_int_peak = 180

General Notes (Continued)

numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edbb1e6e46a485a0011
running on NODE6 Tue Oct 8 17:45:18 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Silver 4216 CPU @ 2.10GHz
  2 "physical id"s (chips)
  64 "processors"
core, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Thread(s) per core: 2
Core(s) per socket: 16
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
DIT400TR-55R/55RL
(2.10 GHz, Intel Xeon Silver 4216)

SPECrate®2017_int_base = 173
SPECrate®2017_int_peak = 180

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb
Hardware Availability: Sep-2019
Software Availability: Aug-2019

Platform Notes (Continued)

Model name: Intel(R) Xeon(R) Silver 4216 CPU @ 2.10GHz
Stepping: 7
CPU MHz: 799.932
CPU max MHz: 3200.0000
CPU min MHz: 800.0000
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 22528K
NUMA node0 CPU(s): 0-15,32-47
NUMA node1 CPU(s): 16-31,48-63
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nop Zombie暖室 nonstop_tsc
aperfmprefp eagerfpu pni pclmulqdq dtelｬs4 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx fl64 rdrand lahf_lm abm 3nowprefetch epb cat_13 cdp_13 intel_pinn
intel_pt ssbd mba ibrs ibpb stibp ibrs_effective tpr_shadow vmmi flexpriority ept
vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a
avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt
xsave crtingb1 qcm_llc qcm_occup_llc qcm_mbm_total qcm_mbm_local dtherm ida arat pln
pts hwlp hwlp_act_window hwlp_eph hwlp_pkg_req pku ospke avx512_vnni md_clear spec_ctrl
intel_stibp flush_l1d arch_capabilities

/proc/cpuinfo cache data
  cache size : 22528 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
different chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 32 33 34 35 36 37 38 39 40 41 42 43
  node 0 size: 195228 MB
  node 0 free: 190212 MB
  node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 48 49 50 51 52 53 54 55 56
  node 1 size: 196608 MB
  node 1 free: 191939 MB
  node distances:
    node 0:  10  21
    node 1:  21  10

From /proc/meminfo
  MemTotal:    394671656 KB

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

DIT400TR-55R/55RL
(2.10 GHz,Intel Xeon Silver 4216)

SPECrate®2017_int_base = 173
SPECrate®2017_int_peak = 180

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

Platform Notes (Continued)

HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
centos-release: CentOS Linux release 7.7.1908 (Core)
centos-release-upstream: Derived from Red Hat Enterprise Linux 7.7 (Source)
os-release:
    NAME="CentOS Linux"
    VERSION="7 (Core)"
    ID="centos"
    ID_LIKE="rhel fedora"
    VERSION_ID="7"
    PRETTY_NAME="CentOS Linux 7 (Core)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:centos:centos:7"
redhat-release: CentOS Linux release 7.7.1908 (Core)
system-release: CentOS Linux release 7.7.1908 (Core)
system-release-cpe: cpe:/o:centos:centos:7

uname -a:
Linux NODE6 3.10.0-1062.el7.x86_64 #1 SMP Wed Aug 7 18:08:02 UTC 2019 x86_64 x86_64
x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
    via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer
    sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full retpoline, IBPB

run-level 3 Oct 8 17:25

SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/centos-home xfs 392G 196G 197G 50% /home

From /sys/devices/virtual/dmi/id
BIOS: American Megatrends Inc. V8.101 08/02/2019
Vendor: Tyrone Systems
Product: DIT400TR-55R
Serial: empty

Additional information from dmidecode follows. WARNING: Use caution when you interpret

(Continued on next page)
Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
DIT400TR-55R/55RL
(2.10 GHz, Intel Xeon Silver 4216)

SPEC®2017 int_base = 173
SPEC®2017 int_peak = 180

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

Platform Notes (Continued)
this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 502.gcc_r(peak)
Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.4.243 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
icc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.
==============================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
| 525.x264_r(base, peak) 557.xz_r(base, peak)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.243 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
icc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.
==============================================================================
C       | 502.gcc_r(peak)
Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.4.243 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
icc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.
==============================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
| 525.x264_r(base, peak) 557.xz_r(base, peak)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.243 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
icc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.

(Continued on next page)
## Compiler Version Notes (Continued)

### C++

<table>
<thead>
<tr>
<th>Version</th>
<th>Compiler</th>
</tr>
</thead>
<tbody>
<tr>
<td>523.xalancbmk_r(peak)</td>
<td></td>
</tr>
</tbody>
</table>

---

### C++

<table>
<thead>
<tr>
<th>Version</th>
<th>Compiler</th>
</tr>
</thead>
<tbody>
<tr>
<td>520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)</td>
<td></td>
</tr>
</tbody>
</table>

---

### C++

<table>
<thead>
<tr>
<th>Version</th>
<th>Compiler</th>
</tr>
</thead>
<tbody>
<tr>
<td>523.xalancbmk_r(peak)</td>
<td></td>
</tr>
</tbody>
</table>

---

### C++

<table>
<thead>
<tr>
<th>Version</th>
<th>Compiler</th>
</tr>
</thead>
<tbody>
<tr>
<td>520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)</td>
<td></td>
</tr>
</tbody>
</table>

---

### Fortran

<table>
<thead>
<tr>
<th>Version</th>
<th>Compiler</th>
</tr>
</thead>
<tbody>
<tr>
<td>548.exchange2_r(base, peak)</td>
<td></td>
</tr>
</tbody>
</table>

---

### Notes:

- The evaluation period for this product ends on 2-nov-2019 UTC.
**SPEC CPU®2017 Integer Rate Result**

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
DIT400TR-55R/55RL  
(2.10 GHz, Intel Xeon Silver 4216)  

| SPECrate®2017_int_base = 173 | Test Date: Feb-2020  
|-------------------------------|--------------------------  
| SPECrate®2017_int_peak = 180 | Hardware Availability: Sep-2019  

CPU2017 License: 006042  
Test Sponsor: Netweb Pte Ltd  
Tested by: Netweb

### Base Compiler Invocation

C benchmarks:  
`icc -m64 -std=c11`

C++ benchmarks:  
`icpc -m64`

Fortran benchmarks:  
`ifort -m64`

### Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc_r: -DSPEC_LP64  
505.mcf_r: -DSPEC_LP64  
520.omnetpp_r: -DSPEC_LP64  
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
525.x264_r: -DSPEC_LP64  
531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64  
548.exchange2_r: -DSPEC_LP64  
557.xz_r: -DSPEC_LP64

### Base Optimization Flags

C benchmarks:  
`-W1, -z, multdefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64  
-lqkmalloc`

C++ benchmarks:  
`-W1, -z, multdefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64  
-lqkmalloc`

Fortran benchmarks:  
`-W1, -z, multdefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64  
-lqkmalloc`
Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
DIT400TR-55R/55RL
(2.10 GHz, Intel Xeon Silver 4216)

<table>
<thead>
<tr>
<th>CPU2017 License: 006042</th>
<th>Test Date: Feb-2020</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Netweb Pte Ltd</td>
<td>Hardware Availability: Sep-2019</td>
</tr>
<tr>
<td>Tested by: Netweb</td>
<td>Software Availability: Aug-2019</td>
</tr>
</tbody>
</table>

**SPEC CPU®2017 Integer Rate Result**

**SPECrate®2017_int_base = 173**

**SPECrate®2017_int_peak = 180**

**Peak Compiler Invocation**

C benchmarks (except as noted below):
```bash
icc -m64 -std=c11
```


C++ benchmarks (except as noted below):
```bash
icpc -m64
```

523.xalancbmk_r:icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/ia32_lin

Fortran benchmarks:
```bash
ifort -m64
```

**Peak Portability Flags**

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

**Peak Optimization Flags**

C benchmarks:
```bash
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4 -fno-strict-overflow -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64 -lqkmalloc
```

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4 -L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
DIT400TR-55R/55RL
(2.10 GHz, Intel Xeon Silver 4216)

SPECrate®2017_int_base = 173
SPECrate®2017_int_peak = 180

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

Peak Optimization Flags (Continued)

505.mcf_r (continued):
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64
-lqkmalloc

525.x264_r -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64
-lqkmalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64
-lqkmalloc

523.xalancbmk_r -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64
-lqkmalloc

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/TyroneIT-Platform-Settings-V1-CLX-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/TyroneIT-Platform-Settings-V1-CLX-revA.xml
<table>
<thead>
<tr>
<th>SPEC CPU®2017 Integer Rate Result</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Tyrone Systems</strong></td>
</tr>
<tr>
<td>(Test Sponsor: Netweb Pte Ltd)</td>
</tr>
<tr>
<td><strong>DIT400TR-55R/55RL</strong></td>
</tr>
<tr>
<td>(2.10 GHz, Intel Xeon Silver 4216)</td>
</tr>
<tr>
<td><strong>SPECrate®2017_int_base = 173</strong></td>
</tr>
<tr>
<td><strong>SPECrate®2017_int_peak = 180</strong></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU2017 License: 006042</th>
<th>Test Date: Feb-2020</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Netweb Pte Ltd</td>
<td>Hardware Availability: Sep-2019</td>
</tr>
<tr>
<td>Tested by: Netweb</td>
<td>Software Availability: Aug-2019</td>
</tr>
</tbody>
</table>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2019-10-08 08:15:17-0400.
Report generated on 2020-03-17 16:12:56 by CPU2017 PDF formatter v6255.
Originally published on 2020-03-17.