## SPEC CPU®2017 Integer Rate Result

**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)  
ProLiant DL380 Gen10  
(2.20 GHz, Intel Xeon Gold 6238R)  

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>296</td>
<td>309</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE  
**Test Date:** Feb-2020  
**Hardware Availability:** Feb-2020  
**Software Availability:** Jun-2019

### Hardware

<table>
<thead>
<tr>
<th>Specification</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon Gold 6238R</td>
</tr>
<tr>
<td>Max MHz</td>
<td>4000</td>
</tr>
<tr>
<td>Nominal</td>
<td>2200</td>
</tr>
<tr>
<td>Enabled</td>
<td>56 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable</td>
<td>1, 2 chip(s)</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3</td>
<td>38.5 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)</td>
</tr>
<tr>
<td>Storage</td>
<td>1 x 400 GB SAS SSD</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Specification</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS</td>
<td>SUSE Linux Enterprise Server 15 SP1 (x86_64)</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 19.0.4.227 of Intel C/C++</td>
</tr>
<tr>
<td>Compiler Build</td>
<td>Compiler Build 20190416 for Linux;</td>
</tr>
<tr>
<td>Compiler Build</td>
<td>Fortran: Version 19.0.4.227 of Intel Fortran;</td>
</tr>
<tr>
<td>Compiler Build</td>
<td>Compiler Build 20190416 for Linux</td>
</tr>
<tr>
<td>Parallel</td>
<td>No</td>
</tr>
<tr>
<td>Firmware</td>
<td>HPE BIOS Version U30 2.22 (11/13/2019) released Feb-2020</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management</td>
<td>BIOS set to prefer performance at the cost of additional power usage</td>
</tr>
</tbody>
</table>
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>112</td>
<td>788</td>
<td>226</td>
<td>788</td>
<td>226</td>
<td>787</td>
<td>227</td>
<td>112</td>
<td>685</td>
<td>260</td>
<td>685</td>
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<td>685</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>112</td>
<td>676</td>
<td>235</td>
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<td>235</td>
<td>670</td>
<td>237</td>
<td>112</td>
<td>565</td>
<td>281</td>
<td>566</td>
<td>280</td>
<td>568</td>
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<tr>
<td>505.mcf_r</td>
<td>112</td>
<td>502</td>
<td>361</td>
<td>502</td>
<td>361</td>
<td>504</td>
<td>359</td>
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<td>502</td>
<td>361</td>
<td>503</td>
<td>360</td>
<td>501</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>112</td>
<td>809</td>
<td>182</td>
<td>808</td>
<td>182</td>
<td>807</td>
<td>182</td>
<td>112</td>
<td>809</td>
<td>182</td>
<td>808</td>
<td>182</td>
<td>809</td>
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<tr>
<td>523.xalancbmk_r</td>
<td>112</td>
<td>390</td>
<td>303</td>
<td>389</td>
<td>304</td>
<td>394</td>
<td>300</td>
<td>112</td>
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<td>355</td>
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<td>525.x264_r</td>
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<td>640</td>
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<td>666</td>
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<td>294</td>
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<tr>
<td>531.deepsjeng_r</td>
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<td>254</td>
<td>507</td>
<td>253</td>
<td>506</td>
<td>254</td>
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<td>507</td>
<td>253</td>
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<td>505</td>
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<tr>
<td>541.leela_r</td>
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<td>766</td>
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<td>759</td>
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<td>764</td>
<td>243</td>
<td>759</td>
<td>244</td>
<td>761</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>112</td>
<td>481</td>
<td>610</td>
<td>482</td>
<td>609</td>
<td>480</td>
<td>611</td>
<td>112</td>
<td>481</td>
<td>610</td>
<td>482</td>
<td>608</td>
<td>480</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>112</td>
<td>609</td>
<td>199</td>
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<td>198</td>
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<td>198</td>
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<td>610</td>
<td>198</td>
<td>609</td>
<td>199</td>
<td>610</td>
</tr>
</tbody>
</table>

**SPECrate®2017_int_base = 296**

**SPECrate®2017_int_peak = 309**

**Note:**
- Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:

```
sync; echo 3 > /proc/sys/vm/drop_caches
```
runpcpu command invoked through numactl i.e.:
```
numactl --interleave=all runcpu <etc>
```

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:
```
LD_LIBRARY_PATH = 
"/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"
```
General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.


Platform Notes

BIOS Configuration:
Thermal Configuration set to Maximum Cooling
Memory Patrol Scrubbing set to Disabled
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
Enhanced Processor Performance set to Enabled
Workload Profile set to General Throughput Compute
Workload Profile set to Custom
Minimum Processor Idle Power Core C-State set to C1E State

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edbl6e6e46a485a0011
running on linux-3rlx Mon Feb 24 11:55:34 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6238R CPU @ 2.20GHz
  2 "physical id"s (chips)
112 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 28
siblings : 56
  physical 0: cores 0 1 2 3 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30
  physical 1: cores 0 1 2 3 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29

(Continued on next page)
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SPECrater®2017_int_base = 296
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CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Platform Notes (Continued)

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
Address sizes: 46 bits physical, 48 bits virtual
CPU(s): 112
On-line CPU(s) list: 0-111
Thread(s) per core: 2
Core(s) per socket: 28
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6238R CPU @ 2.20GHz
Stepping: 7
CPU MHz: 2200.000
BogoMIPS: 4400.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 39424K
NUMA node0 CPU(s): 0-13, 56-69
NUMA node1 CPU(s): 14-27, 70-83
NUMA node2 CPU(s): 28-41, 84-97
NUMA node3 CPU(s): 42-55, 98-111
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtransaction pdcm dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abalancedprefetch cpuid_fault epb cat _1 cdpl _3 invpcid_single intel_puin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vni flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cmx mxr rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512v1 xsaveopt xsaveic xsave I sa xsave I sa xsave vcm m_oc u p _ llc m m_b m_total m m_b m_l o c u p _ llc m m b m_total dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_lld arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

(Continued on next page)
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Platform Notes (Continued)

available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 56 57 58 59 60 61 62 63 64 65 66 67 68 69
node 0 size: 96327 MB
node 0 free: 95944 MB
node 1 cpus: 14 15 16 17 18 19 20 21 22 23 24 25 26 27 70 71 72 73 74 75 76 77 78 79 80 81 82 83
node 1 size: 96762 MB
node 1 free: 94314 MB
node 2 cpus: 28 29 30 31 32 33 34 35 36 37 38 39 40 41 84 85 86 87 88 89 90 91 92 93 94 95 96 97
node 2 size: 96762 MB
node 2 free: 96434 MB
node 3 cpus: 42 43 44 45 46 47 48 49 50 51 52 53 54 55 98 99 100 101 102 103 104 105 106 107 108 109 110 111
node 3 size: 96761 MB
node 3 free: 96481 MB
node distances:
node 0 1 2 3
0: 10 21 31 31
1: 21 10 31 31
2: 31 31 10 21
3: 31 31 21 10

From /proc/meminfo
MemTotal: 395892800 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
NAME="SLES"
VERSION="15-SP1"
VERSION_ID="15.1"
PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15:sp1"

uname -a:
Linux linux-3rlx 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

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Test Sponsor: HPE
Tested by: HPE

Test Date: Feb-2020
Hardware Availability: Feb-2020
Software Availability: Jun-2019

Platform Notes (Continued)

CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 Feb 24 11:53

SPEC is set to: /home/cpu2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 btrfs 369G 159G 211G 43% /home

From /sys/devices/virtual/dmi/id
BIOS: HPE U30 11/13/2019
Vendor: HPE
Product: ProLiant DL380 Gen10
Product Family: ProLiant
Serial: 2M294204YV

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
12x UNKNOWN NOT AVAILABLE
12x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 502.gcc_r(peak)
==============================================================================
Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
==============================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak)
==============================================================================
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416

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Compiler Version Notes (Continued)
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
| C       | 502.gcc_r(peak) |
------------------------------------------------------------------------------
| Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.4.227 Build 20190416
| Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

------------------------------------------------------------------------------
| C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak) |
------------------------------------------------------------------------------
| Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416
| Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

------------------------------------------------------------------------------
| C++     | 523.xalancbmk_r(peak) |
------------------------------------------------------------------------------
| Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.4.227 Build 20190416
| Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

------------------------------------------------------------------------------
| C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak) |
------------------------------------------------------------------------------
| Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416
| Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

------------------------------------------------------------------------------
| C++     | 523.xalancbmk_r(peak) |
------------------------------------------------------------------------------
| Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.4.227 Build 20190416
| Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

------------------------------------------------------------------------------
| C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) |
------------------------------------------------------------------------------
| Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.4.227 Build 20190416
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------------------------------------------------------------------------------

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</table>

CPU2017 License: 3  
Test Sponsor: HPE  
Tested by: HPE  

### Compiler Version Notes (Continued)

|  | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak) |

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Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

---

Fortran | 548.exchange2_r(base, peak)  
---

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

### Base Compiler Invocation

**C benchmarks:**  
icc -m64 -std=c11  

**C++ benchmarks:**  
icpc -m64  

**Fortran benchmarks:**  
ifort -m64  

### Base Portability Flags

| 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
| 502.gcc_r: -DSPEC_LP64  
| 505.mcf_r: -DSPEC_LP64  
| 520.omnetpp_r: -DSPEC_LP64  
| 523.xalancmk_r: -DSPEC_LP64 -DSPEC_LINUX  
| 525.x264_r: -DSPEC_LP64  
| 531.deepsjeng_r: -DSPEC_LP64  
| 541.leela_r: -DSPEC_LP64  
| 548.exchange2_r: -DSPEC_LP64  
| 557.xz_r: -DSPEC_LP64 |
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SPECrate®2017_int_peak = 309

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64 -std=c11

502.gcc_r.icc -m32 -std=c11 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):
icpc -m64

523.xalancbmk_r.icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64

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Peak Portability Flags (Continued)

531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -03 -no-prec-div -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -03 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -03 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -03 -no-prec-div
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -03 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -03 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen10
(2.20 GHz, Intel Xeon Gold 6238R)

SPECrate®2017_int_base = 296
SPECrate®2017_int_peak = 309

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE
Test Date: Feb-2020
Hardware Availability: Feb-2020
Software Availability: Jun-2019

Peak Optimization Flags (Continued)

541.leel_r: Same as 520.omnetpp_r

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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