# SPEC CPU®2017 Integer Speed Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL360 Gen10
(3.10 GHz, Intel Xeon Gold 6242R)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>10.4</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_int_peak</td>
<td>10.5</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

---

## Hardware

<table>
<thead>
<tr>
<th>Component</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon Gold 6242R</td>
</tr>
<tr>
<td>Max MHz</td>
<td>4100</td>
</tr>
<tr>
<td>Nominal</td>
<td>3100</td>
</tr>
<tr>
<td>Enabled</td>
<td>40 cores, 2 chips</td>
</tr>
<tr>
<td>Orderable</td>
<td>1, 2 chip(s)</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3</td>
<td>35.75 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>384 GB (24 x 16 GB 2Rx8 PC4-2933Y-R)</td>
</tr>
<tr>
<td>Storage</td>
<td>1 x 400 GB SAS SSD, RAID 0</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

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## Software

<table>
<thead>
<tr>
<th>Component</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS</td>
<td>SUSE Linux Enterprise Server 15 SP1 (x86_64)</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux; Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux</td>
</tr>
<tr>
<td>Parallel</td>
<td>Yes</td>
</tr>
<tr>
<td>Firmware</td>
<td>HPE BIOS Version U32 2.22 (11/13/2019) released Feb-2020</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Other</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management</td>
<td>BIOS set to prefer performance at the cost of additional power usage</td>
</tr>
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<table>
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<tr>
<th>Threads</th>
<th>0</th>
<th>1.00</th>
<th>3.00</th>
<th>5.00</th>
<th>7.00</th>
<th>9.00</th>
<th>11.0</th>
<th>13.0</th>
<th>15.0</th>
<th>17.0</th>
<th>19.0</th>
<th>21.0</th>
<th>23.1</th>
<th>24.0</th>
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<tbody>
<tr>
<td>600.perlbench_s</td>
<td>40</td>
<td>-----</td>
<td>-----</td>
<td>-----</td>
<td>6.78</td>
<td>-----</td>
<td>-----</td>
<td>-----</td>
<td>7.48</td>
<td>-----</td>
<td>-----</td>
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</tr>
<tr>
<td>602.gcc_s</td>
<td>40</td>
<td>-----</td>
<td>-----</td>
<td>-----</td>
<td>9.65</td>
<td>-----</td>
<td>-----</td>
<td>-----</td>
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<td>-----</td>
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<tr>
<td>605.mcf_s</td>
<td>40</td>
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<td>-----</td>
<td>-----</td>
<td>12.5</td>
<td>-----</td>
<td>-----</td>
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<td>-----</td>
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</tr>
<tr>
<td>620.omnetpp_s</td>
<td>40</td>
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<td>-----</td>
<td>-----</td>
<td>9.37</td>
<td>-----</td>
<td>-----</td>
<td>-----</td>
<td>9.31</td>
<td>-----</td>
<td>-----</td>
<td>-----</td>
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<tr>
<td>623.xalancbmk_s</td>
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<td>-----</td>
<td>-----</td>
<td>12.4</td>
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</tr>
<tr>
<td>625.x264_s</td>
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<td>-----</td>
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<td>14.6</td>
<td>-----</td>
<td>-----</td>
<td>-----</td>
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<td>631.deepsjeng_s</td>
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<td>3.58</td>
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</tr>
<tr>
<td>641.leela_s</td>
<td>40</td>
<td>-----</td>
<td>-----</td>
<td>-----</td>
<td>4.80</td>
<td>-----</td>
<td>-----</td>
<td>-----</td>
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</tr>
<tr>
<td>648.exchange2_s</td>
<td>40</td>
<td>-----</td>
<td>-----</td>
<td>-----</td>
<td>16.4</td>
<td>-----</td>
<td>-----</td>
<td>-----</td>
<td>16.4</td>
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<tr>
<td>657.xz_s</td>
<td>40</td>
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<td>-----</td>
<td>-----</td>
<td>23.2</td>
<td>-----</td>
<td>-----</td>
<td>-----</td>
<td>23.1</td>
<td>-----</td>
<td>-----</td>
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</tr>
</tbody>
</table>

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**Test Date:** Feb-2020  
**Hardware Availability:** Feb-2020  
**Software Availability:** Jun-2019
SPEC CPU®2017 Integer Speed Result

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Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL360 Gen10
(3.10 GHz, Intel Xeon Gold 6242R)

SPECspeed®2017_int_base = 10.4
SPECspeed®2017_int_peak = 10.5

Results Table

Benchmark | Threads | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Threads | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio
--- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | ---
600.perlbench_s | 40 | 266 | 6.67 | 262 | 6.78 | 261 | 6.80 | 40 | 227 | 7.80 | 228 | 7.78 | 229 | 7.76
605.mcf_s | 40 | 377 | 12.5 | 374 | 12.6 | 374 | 12.6 | 40 | 376 | 12.5 | 371 | 12.7 | 380 | 12.4
623.xalanchmk_s | 40 | 115 | 12.4 | 113 | 12.5 | 115 | 12.3 | 40 | 113 | 12.5 | 115 | 12.3 | 115 | 12.3
625.x264_s | 40 | 121 | 14.6 | 121 | 14.6 | 121 | 14.6 | 40 | 121 | 14.6 | 121 | 14.6 | 121 | 14.6
631.deepsjeng_s | 40 | 256 | 5.59 | 257 | 5.58 | 257 | 5.57 | 40 | 257 | 5.58 | 257 | 5.57 | 256 | 5.59
641.leela_s | 40 | 356 | 4.80 | 356 | 4.79 | 355 | 4.80 | 40 | 356 | 4.80 | 356 | 4.79 | 355 | 4.80
648.exchange2_s | 40 | 179 | 16.4 | 179 | 16.4 | 179 | 16.4 | 40 | 182 | 16.2 | 179 | 16.4 | 179 | 16.4
657.xz_s | 40 | 266 | 23.2 | 266 | 23.2 | 266 | 23.2 | 40 | 267 | 23.1 | 267 | 23.2 | 267 | 23.1

SPECspeed®2017_int_base = 10.4
SPECspeed®2017_int_peak = 10.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
```
sync; echo 3 > /proc/sys/vm/drop_caches
```

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
```
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/cpu2017/lib/intel64:/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"
```

General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
**SPEC CPU®2017 Integer Speed Result**

Hewlett Packard Enterprise  
(Test Sponsor: HPE)  
ProLiant DL360 Gen10  
(3.10 GHz, Intel Xeon Gold 6242R)  

---

**SPECspeed®2017_int_base = 10.4**  
**SPECspeed®2017_int_peak = 10.5**

---

**Platform Notes**

BIOS Configuration:  
Hyper-Threading set to Disabled  
Thermal Configuration set to Maximum Cooling  
Memory Patrol Scrubbing set to Disabled  
LLC Prefetch set to Enabled  
LLC Dead Line Allocation set to Disabled  
Enhanced Processor Performance set to Enabled  
Workload Profile set to General Peak Frequency Compute  
Energy/Performance Bias set to Balanced Power  
Workload Profile set to Custom  
Numa Group Size Optimization set to Flat  
Intel UPI Link Power Management set to Enabled

Sysinfo program /cpu2017/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7eddb6e46a485a0011  
running on linux-9e6o Wed Feb 19 21:32:09 2020

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 6242R CPU @ 3.10GHz  
  2 "physical id"s (chips)  
  40 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 20  
siblings : 20  
physical 0: cores 1 2 3 5 8 9 10 12 13 16 17 18 19 20 21 26 27 28 29  
physical 1: cores 0 1 2 3 5 6 8 9 10 12 13 16 18 19 20 21 26 27 28 29

From lscpu:  
Architecture: x86_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
Address sizes: 46 bits physical, 48 bits virtual  
CPU(s): 40  
On-line CPU(s) list: 0-39  
Thread(s) per core: 1  
Core(s) per socket: 20  
Socket(s): 2  
NUMA node(s): 2  
Vendor ID: GenuineIntel  
CPU family: 6  
Model: 85  
Model name: Intel(R) Xeon(R) Gold 6242R CPU @ 3.10GHz

(Continued on next page)
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CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Feb-2020
Hardware Availability: Feb-2020
Software Availability: Jun-2019

Specifications:
- Stepping: 7
- CPU MHz: 3100.000
- BogoMIPS: 6200.00
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 1024K
- L3 cache: 36608K
- NUMA node0 CPU(s): 0-19
- NUMA node1 CPU(s): 20-39
- Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
- Other: pcmulqdq ds longd cop13 tm[-mks] cmov[+ms] popcnt [l2apic]
- Other: clflush size 64
- Other: dtes64 mru clamp[8237] mlb
data

Platform Notes (Continued)

/platform/cpuid capability

/proc/cpuinfo cache data
 cache size: 36608 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.

node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19
node 0 size: 193097 MB
node 0 free: 190649 MB
node 1 cpus: 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39
node 1 size: 193531 MB
node 1 free: 193246 MB
node distances:
node 0 1
0: 10 21
1: 21 10

From /proc/meminfo
MemTotal: 395907208 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

(Continued on next page)
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SPECspeed®2017_int_base = 10.4
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CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE
Test Date: Feb-2020
Hardware Availability: Feb-2020
Software Availability: Jun-2019

Platform Notes (Continued)

NAME="SLES"
VERSION="15-SP1"
VERSION_ID="15.1"
PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15:sp1"

uname -a:
Linux linux-9e6o 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 Feb 19 21:29

SPEC is set to: /cpu2017
Filesystem    Type  Size  Used Avail Use% Mounted on
/dev/sda2     btrfs  369G  116G  254G  32% /

From /sys/devices/virtual/dmi/id
BIOS: HPE U32 11/13/2019
Vendor: HPE
Product: ProLiant DL360 Gen10
Product Family: ProLiant
Serial: MXQ94204PS

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
24x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2933

(End of data from sysinfo program)
Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL360 Gen10
(3.10 GHz, Intel Xeon Gold 6242R)

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Copyright 2017-2020 Standard Performance Evaluation Corporation

SPECspeed®2017_int_base = 10.4
SPECspeed®2017_int_peak = 10.5

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Compiler Version Notes
==============================================================================
<table>
<thead>
<tr>
<th></th>
<th>600.perlbennch_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>C</td>
<td>Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416</td>
</tr>
<tr>
<td></td>
<td>Copyright (C) 1985-2019 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>----</td>
<td>---------------------------------------------------------------------------------</td>
</tr>
<tr>
<td>C++</td>
<td>620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)</td>
</tr>
<tr>
<td></td>
<td>Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416</td>
</tr>
<tr>
<td></td>
<td>Copyright (C) 1985-2019 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>----</td>
<td>---------------------------------------------------------------------------------</td>
</tr>
<tr>
<td>Fortran</td>
<td>648.exchange2_s(base, peak)</td>
</tr>
<tr>
<td></td>
<td>Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416</td>
</tr>
<tr>
<td></td>
<td>Copyright (C) 1985-2019 Intel Corporation. All rights reserved.</td>
</tr>
</tbody>
</table>

Base Compiler Invocation
C benchmarks:
```
icc -m64 -std=c11
```

C++ benchmarks:
```
icpc -m64
```

Fortran benchmarks:
```
ifort -m64
```

Base Portability Flags
```
600.perlbennch_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
```

(Continued on next page)
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<th>CPU2017 License: 3</th>
<th>Test Date: Feb-2020</th>
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</thead>
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<tr>
<td>Test Sponsor: HPE</td>
<td>Hardware Availability: Feb-2020</td>
</tr>
<tr>
<td>Tested by: HPE</td>
<td>Software Availability: Jun-2019</td>
</tr>
</tbody>
</table>

**Base Portability Flags (Continued)**

623.xalanbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs

**Peak Compiler Invocation**

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

**Peak Portability Flags**

Same as Base Portability Flags
Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,muldefs -CORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

623.xalancbk_s: -Wl,-z,muldefs -CORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

631.deepsjeng_s: Same as 623.xalancbk_s

641.leela_s: Same as 623.xalancbk_s

Fortran benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4

(Continued on next page)
Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL360 Gen10
(3.10 GHz, Intel Xeon Gold 6242R)

| SPECspeed®2017_int_base = 10.4 |
| SPECspeed®2017_int_peak = 10.5 |

CPU2017 License: 3
Test Sponsor: HPE
Test Date: Feb-2020
Tested by: HPE
Hardware Availability: Feb-2020
Software Availability: Jun-2019

Peak Optimization Flags (Continued)

Fortran benchmarks (continued):
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.xml

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