## SPEC CPU®2017 Integer Speed Result

**Dell Inc.**

PowerEdge T440 (Intel Xeon Silver 4216, 2.10 GHz)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base = 8.22</th>
<th>SPECspeed®2017_int_peak = 8.36</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU2017 License: 55</td>
<td>Test Date: Mar-2020</td>
</tr>
<tr>
<td>Test Sponsor: Dell Inc.</td>
<td>Hardware Availability: Dec-2019</td>
</tr>
<tr>
<td>Tested by: Dell Inc.</td>
<td>Software Availability: Jun-2019</td>
</tr>
</tbody>
</table>

### Threads

<table>
<thead>
<tr>
<th>Test</th>
<th>Threads</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s 32</td>
<td></td>
<td>5.43</td>
<td>6.21</td>
</tr>
<tr>
<td>602.gcc_s 32</td>
<td></td>
<td>8.37</td>
<td>10.1</td>
</tr>
<tr>
<td>605.mcf_s 32</td>
<td></td>
<td>6.64</td>
<td>7.72</td>
</tr>
<tr>
<td>620.omnetpp_s 32</td>
<td></td>
<td>9.99</td>
<td>9.98</td>
</tr>
<tr>
<td>623.xalanchmk_s 32</td>
<td></td>
<td>11.4</td>
<td>11.4</td>
</tr>
<tr>
<td>625.x264_s 32</td>
<td></td>
<td>4.48</td>
<td>4.48</td>
</tr>
<tr>
<td>631.deepsjeng_s 32</td>
<td></td>
<td>3.72</td>
<td>3.72</td>
</tr>
<tr>
<td>641.leela_s 32</td>
<td></td>
<td>13.0</td>
<td>13.0</td>
</tr>
<tr>
<td>648.exchange2_s 32</td>
<td></td>
<td>18.6</td>
<td>19.0</td>
</tr>
<tr>
<td>657.xz_s 32</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### SPECspeed®2017 Int Base = 8.22

### SPECspeed®2017 Int Peak = 8.36

### Hardware

- **CPU Name:** Intel Xeon Silver 4216
- **Max MHz:** 3200
- **Nominal:** 2100
- **Enabled:** 32 cores, 2 chips, 2 threads/core
- **Orderable:** 1.2 chip
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 22 MB I+D on chip per chip
- **Other:** None
- **Memory:** 384 GB (12 x 32 GB 2Rx8 PC4-2666V-R)
- **Storage:** 1 x 960 GB SATA SSD
- **Other:** None

### Software

- **OS:** Suse Linux Enterprise Server 15 SP1
- **Compiler:** C/C++; Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux;
  Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux
- **Parallel:** Yes
- **Firmware:** Version 2.5.4 released Jan-2020
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>32</td>
<td>329</td>
<td>5.40</td>
<td>327</td>
<td>5.44</td>
<td>327</td>
<td>5.43</td>
<td>32</td>
<td>286</td>
<td>6.21</td>
<td>286</td>
<td>6.21</td>
<td>287</td>
<td>6.19</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>32</td>
<td>475</td>
<td>8.38</td>
<td>475</td>
<td>8.37</td>
<td>482</td>
<td>8.26</td>
<td>32</td>
<td>480</td>
<td>8.29</td>
<td>484</td>
<td>8.22</td>
<td>486</td>
<td>8.20</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>32</td>
<td>467</td>
<td>10.1</td>
<td>465</td>
<td>10.2</td>
<td>466</td>
<td>10.1</td>
<td>32</td>
<td>458</td>
<td>10.3</td>
<td>460</td>
<td>10.3</td>
<td>456</td>
<td>10.4</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>32</td>
<td>247</td>
<td>6.59</td>
<td>244</td>
<td>6.68</td>
<td>246</td>
<td>6.64</td>
<td>32</td>
<td>243</td>
<td>6.72</td>
<td>243</td>
<td>6.72</td>
<td>243</td>
<td>6.71</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>32</td>
<td>155</td>
<td>11.4</td>
<td>155</td>
<td>11.4</td>
<td>155</td>
<td>11.4</td>
<td>32</td>
<td>155</td>
<td>11.4</td>
<td>155</td>
<td>11.4</td>
<td>155</td>
<td>11.4</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>32</td>
<td>320</td>
<td>4.48</td>
<td>320</td>
<td>4.48</td>
<td>319</td>
<td>4.49</td>
<td>32</td>
<td>320</td>
<td>4.48</td>
<td>320</td>
<td>4.48</td>
<td>319</td>
<td>4.49</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>32</td>
<td>458</td>
<td>3.72</td>
<td>458</td>
<td>3.72</td>
<td>459</td>
<td>3.72</td>
<td>32</td>
<td>459</td>
<td>3.72</td>
<td>459</td>
<td>3.72</td>
<td>459</td>
<td>3.72</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>32</td>
<td>227</td>
<td>13.0</td>
<td>226</td>
<td>13.0</td>
<td>226</td>
<td>13.0</td>
<td>32</td>
<td>226</td>
<td>13.0</td>
<td>225</td>
<td>13.1</td>
<td>226</td>
<td>13.0</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>32</td>
<td>332</td>
<td>18.6</td>
<td>333</td>
<td>18.6</td>
<td>331</td>
<td>18.7</td>
<td>32</td>
<td>325</td>
<td>19.0</td>
<td>323</td>
<td>19.2</td>
<td>325</td>
<td>19.0</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)
## Dell Inc.

**PowerEdge T440 (Intel Xeon Silver 4216, 2.10 GHz)**

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base = 8.22</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_int_peak = 8.36</td>
</tr>
</tbody>
</table>

### CPU2017 License: 55

### Test Sponsor: Dell Inc.

### Tested by: Dell Inc.

### Test Date: Mar-2020

### Hardware Availability: Dec-2019

### Software Availability: Jun-2019

---

### General Notes (Continued)

Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
`sync; echo 3 > /proc/sys/vm/drop_caches`

runcpu command invoked through numactl i.e.:  
`numactl --interleave=all runcpu <etc>`

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  

---

### Platform Notes

BIOS settings:  
Sub NUMA Cluster enabled  
Virtualization Technology disabled  
DCU Streamer Prefetcher disabled  
System Profile set to Custom  
CPU Performance set to Maximum Performance  
C States set to Autonomous  
C1E disabled  
Uncore Frequency set to Dynamic  
Energy Efficiency Policy set to Performance  
Memory Patrol Scrub disabled  
Logical Processor enabled  
CPU Interconnect Bus Link Power Management enabled  
PCI ASPM L1 Link Power Management enabled

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7ed1e6e46a485a0011  
running on linux-g3ob Thu Mar 12 07:49:47 2020

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Silver 4216 CPU @ 2.10GHz
  2 "physical id"s (chips)
  64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 32
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
```

(Continued on next page)
Dell Inc.

PowerEdge T440 (Intel Xeon Silver 4216, 2.10 GHz)

SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge T440 (Intel Xeon Silver 4216, 2.10 GHz)

SPECspeed®2017_int_base = 8.22

SPECspeed®2017_int_peak = 8.36

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>8.22</td>
<td>8.36</td>
</tr>
</tbody>
</table>

Test Date: Mar-2020
Hardware Availability: Dec-2019
Software Availability: Jun-2019

Platform Notes (Continued)

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
Address sizes: 46 bits physical, 48 bits virtual
CPU(s): 64
On-line CPU(s) list: 0-63
Thread(s) per core: 2
Core(s) per socket: 16
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4216 CPU @ 2.10GHz
Stepping: 6
CPU MHz: 2100.000
BogomIPS: 4200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 22528K
NUMA node0 CPU(s): 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62
NUMA node1 CPU(s): 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59,61,63
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrm pdcm pcl dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abtm 3nowprefetch cpuid_fault epb cat _l3 cdp _l3 invpcid_single intel_pcin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnni flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 2ms invvpid rtm cqm mpx rd_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsaves xsavec xgetbv1 xsaveas cqm _l1c cqm _occup _l1c cqm _mbm _total cqm _mbm _local dtherm ida arat pbn pts pku ospke avx512_vnni md_clear flush _lld arch_capabilities

/procpuinfo cache data
cache size : 22528 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

(Continued on next page)
Dell Inc.

PowerEdge T440 (Intel Xeon Silver 4216, 2.10 GHz)

| SPECspeed®2017_int_base = 8.22 |
| SPECspeed®2017_int_peak = 8.36 |

### Platform Notes (Continued)

available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62
node 0 size: 192071 MB
node 0 free: 190500 MB
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47 49 51 53 55 57 59 61 63
node 1 size: 193499 MB
node 1 free: 192512 MB
node distances:
  node 0 1
node 0: 10 21
node 1: 21 10

From /proc/meminfo
  MemTotal: 394825140 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
oS-release:
  NAME="SLES"
  VERSION="15-SP1"
  VERSION_ID="15.1"
  PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
  ID="sles"
  ID_LIKE="suse"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:15:sp1"

uname -a:
  Linux linux-g3ob 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 Mar 11 05:50 last=5

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Dell Inc.

PowerEdge T440 (Intel Xeon Silver 4216, 2.10 GHz) SPECspeed®2017_int_base = 8.22

SPECspeed®2017_int_peak = 8.36

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Platform Notes (Continued)

 SPEC is set to: /home/cpu2017
 Filesystem   Type  Size  Used Avail Use% Mounted on
 /dev/sda2    xfs   440G   46G  395G  11% /

From /sys/devices/virtual/dmi/id
BIOS:    Dell Inc. 2.5.4  01/14/2020
Vendor:  Dell Inc.
Product: PowerEdge T440
Product Family: PowerEdge
Serial:  FBLH613

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
4x 00AD00B300AD HMA84GR7CJR4N-WM  32 GB 2 rank 2933
8x 00AD063200AD HMA84GR7CJR4N-WM  32 GB 2 rank 2933
4x Not Specified Not Specified

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
 C       | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)  
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
==============================================================================

==============================================================================
 C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)  
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
==============================================================================

==============================================================================
 Fortran | 648.exchange2_s(base, peak)  
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
(Continued on next page)
Dell Inc. PowerEdge T440 (Intel Xeon Silver 4216, 2.10 GHz)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>8.22</td>
<td>8.36</td>
</tr>
</tbody>
</table>

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Compiler Version Notes (Continued)

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-1qkmalloC

(Continued on next page)
**SPEC CPU®2017 Integer Speed Result**

**Dell Inc.**

PowerEdge T440 (Intel Xeon Silver 4216, 2.10 GHz)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>8.22</td>
<td>8.36</td>
</tr>
</tbody>
</table>

**Test Date:** Mar-2020  
**Hardware Availability:** Dec-2019  
**Software Availability:** Jun-2019

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

---

**Base Optimization Flags (Continued)**

Fortran benchmarks:
- xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
- nostandard-realloc-lhs

---

**Peak Compiler Invocation**

C benchmarks:
- icc -m64 -std=c11

C++ benchmarks:
- icpc -m64

Fortran benchmarks:
- ifort -m64

---

**Peak Portability Flags**

Same as Base Portability Flags

---

**Peak Optimization Flags**

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2  
-xCORE-AVX2 -qopt-mem-layout-trans=4 -ipo -O3  
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp  
-DSPEC_OPENMP -fno-strict-overflow  
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2  
-xCORE-AVX2 -qopt-mem-layout-trans=4 -ipo -O3  
-no-prec-div -DSPEC_SUPPRESS_OPENMP  
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=4  
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP  
-L/usr/local/je5.0.1-64/lib -ljemalloc

(Continued on next page)
Dell Inc.
PowerEdge T440 (Intel Xeon Silver 4216, 2.10 GHz)

**SPEC CPU®2017 Integer Speed Result**

<table>
<thead>
<tr>
<th>CPU2017 License: 55</th>
<th>Test Date: Mar-2020</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Dell Inc.</td>
<td>Hardware Availability: Dec-2019</td>
</tr>
<tr>
<td>Tested by: Dell Inc.</td>
<td>Software Availability: Jun-2019</td>
</tr>
</tbody>
</table>

**SPECspeed®2017_int_base = 8.22**

**SPECspeed®2017_int_peak = 8.36**

---

### Peak Optimization Flags (Continued)

625.x264_s: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4 -gopenmp -DSPEC_OPENMP -L/usr/local/jed5.0.1-64/lib -ljemalloc

657.xz_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-mem-layout-trans=4 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP -L/usr/local/jed5.0.1-64/lib -ljemalloc

### C++ benchmarks:

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64 -lqkmalloc


631.deepsjeng_s: Same as 623.xalancbmk_s

641.leela_s: Same as 623.xalancbmk_s

### Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4 -nostandard-realloc-lhs

---

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


---

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-03-12 08:49:46-0400.
Originally published on 2020-03-31.