## SPEC CPU®2017 Integer Rate Result

**Fujitsu**

PRIMERGY RX2540 M5, Intel Xeon Gold 6240R, 2.40 GHz

### CPU2017 Details
- **License:** 19
- **Test Sponsor:** Fujitsu
- **Tested by:** Fujitsu
- **Test Date:** Mar-2020
- **Hardware Availability:** Feb-2020
- **Software Availability:** May-2019
- **Tested by:** Fujitsu

### SPECrate®2017 Results

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base =</th>
<th>274</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_int_peak   =</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>Name</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon Gold 6240R</td>
</tr>
<tr>
<td>Max MHz</td>
<td>4000</td>
</tr>
<tr>
<td>Nominal</td>
<td>2400</td>
</tr>
<tr>
<td>Enabled</td>
<td>48 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable</td>
<td>1.2 chips</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Cache L2</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>Cache L3</td>
<td>35.75 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Memory</td>
<td>768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R)</td>
</tr>
<tr>
<td>Storage</td>
<td>1 x SATA M.2 SSD, 240 GB</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Name</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS</td>
<td>SUSE Linux Enterprise Server 15</td>
</tr>
<tr>
<td></td>
<td>4.12.14-25.28-default</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 19.0.4.227 of Intel C/C++</td>
</tr>
<tr>
<td></td>
<td>Compiler Build 20190416 for Linux;</td>
</tr>
<tr>
<td></td>
<td>Fortran: Version 19.0.4.227 of Intel Fortran</td>
</tr>
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<td>Compiler Build 20190416 for Linux</td>
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<tr>
<td>Parallel</td>
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<td>Firmware</td>
<td>Fujitsu BIOS Version V5.0.0.14 R1.18.0 for D3384-B1x</td>
</tr>
<tr>
<td></td>
<td>released Feb-2020</td>
</tr>
<tr>
<td>File System</td>
<td>xfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>Not Applicable</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Power Management</td>
<td>BIOS set to prefer performance at the cost of additional power usage</td>
</tr>
</tbody>
</table>

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<table>
<thead>
<tr>
<th>Name</th>
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<tr>
<td>500.perlbench_r</td>
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</tr>
<tr>
<td>502.gcc_r</td>
<td>96</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>96</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>96</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>96</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>96</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>96</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>96</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>96</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>96</td>
</tr>
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</table>

### SPECrate®2017 Int Base (274)
SPEC CPU®2017 Integer Rate Result

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Fujitsu
PRIMERGY RX2540 M5, Intel Xeon Gold 6240R, 2.40 GHz

SPECrate®2017_int_base = 274

SPECrate®2017_int_peak = Not Run

Results Table

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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<td>96</td>
<td>753</td>
<td>203</td>
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<td>203</td>
<td>754</td>
<td>203</td>
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<tr>
<td>502.gcc_r</td>
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<td>618</td>
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<td>621</td>
<td>219</td>
<td>620</td>
<td>219</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>96</td>
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<td>339</td>
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<td>457</td>
<td>340</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>96</td>
<td>686</td>
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<td>687</td>
<td>183</td>
</tr>
<tr>
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<td>344</td>
<td>295</td>
<td>345</td>
<td>294</td>
<td>346</td>
<td>293</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>96</td>
<td>289</td>
<td>581</td>
<td>285</td>
<td>589</td>
<td>290</td>
<td>581</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
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<td>472</td>
<td>233</td>
<td>472</td>
<td>233</td>
<td>472</td>
<td>233</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>96</td>
<td>721</td>
<td>220</td>
<td>714</td>
<td>223</td>
<td>724</td>
<td>220</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>96</td>
<td>461</td>
<td>546</td>
<td>459</td>
<td>548</td>
<td>461</td>
<td>546</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>96</td>
<td>569</td>
<td>182</td>
<td>568</td>
<td>182</td>
<td>570</td>
<td>182</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Kernel Boot Parameter set with : nohz_full=1-95

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/Benchmark/speccpu2017-1.1.0/lib/intel64:/home/Benchmark/speccpu2017-1.1.0/lib/ia32:/home/Benchmark/speccpu2017-1.1.0/je5.0.1-32"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/Benchmark/SPECCPU2017-1.1.0/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default

(Continued on next page)
Fujitsu
PRIMERGY RX2540 M5, Intel Xeon Gold 6240R, 2.40 GHz

SPECrate®2017_int_base = 274
SPECrate®2017_int_peak = Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

General Notes (Continued)

Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
umactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Patrol Scrub = Disabled
WR CRC feature Control = Disabled
Fan Control = Full

Sysinfo program /home/Benchmark/speccpu2017-1.1.0/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7ed1e6e46a485a0011
running on RX2540M5_CLXR Tue Mar 3 10:18:05 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6240R CPU @ 2.40GHz
  2 "physical id"s (chips)
    96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    cpu cores : 24
    siblings : 48
    physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
    physical 1: cores 0 1 2 3 4 5 6 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95
Thread(s) per core: 2
Core(s) per socket: 24

(Continued on next page)
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CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Mar-2020
Hardware Availability: Feb-2020
Software Availability: May-2019

Platform Notes (Continued)

Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6240R CPU @ 2.40GHz
Stepping: 7
CPU MHz: 2400.000
CPU max MHz: 4000.0000
CPU min MHz: 1000.0000
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0-3,7-9,13-15,19,20,48-51,55-57,61-63,67,68
NUMA node1 CPU(s): 4-6,10-12,16-18,21-23,52-54,58-60,64-66,69-71
NUMA node2 CPU(s): 24-27,31,32,36-38,42-44,72-75,79,80-86,90-92
NUMA node3 CPU(s): 28-30,33-35,39-41,45-47,76-78,81-83,87-89,93-95
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abml cmov
From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
	available: 4 nodes (0-3)
		node 0 cpus: 0 1 2 3 7 8 9 13 14 15 19 20 48 49 50 51 55 56 57 61 62 63 67 68
		node 0 size: 191974 MB
		node 0 free: 191567 MB
		node 1 cpus: 4 5 6 10 11 12 16 17 18 21 22 23 52 53 54 58 59 60 64 65 66 69 70 71
		node 1 size: 193531 MB
		node 1 free: 193249 MB
		node 2 cpus: 24 25 26 27 31 32 36 37 38 42 43 44 72 73 74 75 79 80 84 85 86 90 91 92
		node 2 size: 193531 MB

(Continued on next page)
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<tr>
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<td>Fujitsu</td>
</tr>
<tr>
<td>Tested by</td>
<td>Fujitsu</td>
</tr>
</tbody>
</table>

**Platform Notes (Continued)**

- node 2 free: 193204 MB
- node 3 cpus: 28 29 30 33 34 35 39 40 41 45 46 47 76 77 78 81 82 83 87 88 89 93 94 95
- node 3 size: 193289 MB
- node 3 free: 192936 MB
- node distances:
  - node 0: 10 11 21 21
  - node 1: 11 10 21 21
  - node 2: 21 21 10 11
  - node 3: 21 21 11 10

From `/proc/meminfo`
- MemTotal: 790862828 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From `/etc/*release* /etc/*version*`
- os-release:
  - NAME="SLES"
  - VERSION="15"
  - VERSION_ID="15"
  - PRETTY_NAME="SUSE Linux Enterprise Server 15"
  - ID="sles"
  - ID_LIKE="suse"
  - ANSI_COLOR="0;32"
  - CPE_NAME="cpe:/o:suse:sles:15"

```
uname -a:
Linux RX2540M5_CLXR 4.12.14-25.28-default #1 SMP Wed Jan 16 20:00:47 UTC 2019
(dd6077c) x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

- CVE-2018-3620 (L1 Terminal Fault): Not affected
- Microarchitectural Data Sampling: No status reported
- CVE-2017-5754 (Meltdown): Not affected
- CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
- CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 Mar 3 10:16

SPEC is set to: /home/Benchmark/speccpu2017-1.1.0

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda5</td>
<td>xfs</td>
<td>191G</td>
<td>113G</td>
<td>79G</td>
<td>60%</td>
<td>/home</td>
</tr>
</tbody>
</table>

(Continued on next page)
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PRIMERGY RX2540 M5, Intel Xeon Gold 6240R, 2.40 GHz

SPECrate®2017_int_base = 274
SPECrate®2017_int_peak = Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Mar-2020
Hardware Availability: Feb-2020
Software Availability: May-2019

Platform Notes (Continued)

From /sys/devices/virtual/dmi/id
BIOS: FUJITSU // American Megatrends Inc. V5.0.0.14 R1.18.0 for D3384-B1x
02/10/2020
Vendor: FUJITSU
Product: PRIMERGY RX2540 M5
Product Family: SERVER
Serial: YMSQXXXXX

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
24x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933, configured at 2934

(End of data from sysinfo program)

Compiler Version Notes

C
500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base)
525.x264_r(base) 557.xz_r(base)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

C++
520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
541.leela_r(base)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Fortran
548.exchange2_r(base)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
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Fujitsu
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Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

C++ benchmarks:
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc
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</thead>
<tbody>
<tr>
<td>19</td>
<td>Fujitsu</td>
<td>Fujitsu</td>
<td>Mar-2020</td>
<td>Feb-2020</td>
<td>May-2019</td>
</tr>
</tbody>
</table>

SPECrate®2017_int_base = 274
SPECrate®2017_int_peak = Not Run

The flags files that were used to format this result can be browsed at:

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-CSL-RevE.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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