## Dell Inc. PowerEdge R7515 (AMD EPYC 7552, 2.20 GHz)

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>48</td>
<td>8.43</td>
<td>8.76</td>
</tr>
</tbody>
</table>

### CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Jan-2020
Hardware Availability: Apr-2020
Software Availability: Aug-2019

### Hardware
- **CPU Name:** AMD EPYC 7552
- **Max MHz:** 3300
- **Nominal:** 2200
- **Enabled:** 48 cores, 1 chip, 2 threads/core
- **Orderable:** 1 chip
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 512 KB I+D on chip per core
- **L3:** 192 MB I+D on chip per chip, 16 MB shared / 4 cores
- **Other:** None
- **Memory:** 256 GB (8 x 32 GB 2Rx4 PC4-3200AA-R)
- **Storage:** 1 x 960 GB SATA SSD
- **Other:** None

### Software
- **OS:** SUSE Linux Enterprise Server 15 SP1
- **Compiler:** C/C++/Fortran: Version 2.0.0 of AOCC
- **Parallel:** Yes
- **Firmware:** Version 1.3.0 released Jan-2020
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 32/64-bit
- **Other:** jemalloc: jemalloc memory allocator library v5.1.0
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage.
SPEC CPU®2017 Integer Speed Result

Dell Inc.
PowerEdge R7515 (AMD EPYC 7552, 2.20 GHz)

SPECspeed®2017_int_base = 8.43
SPECspeed®2017_int_peak = 8.76

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<tr>
<td>600.perlbench_s</td>
<td>48</td>
<td>397</td>
<td>4.47</td>
<td>395</td>
<td>4.49</td>
<td>396</td>
<td>4.48</td>
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<td>602.gcc_s</td>
<td>48</td>
<td>441</td>
<td>9.02</td>
<td>435</td>
<td>9.15</td>
<td>431</td>
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<td>605.mcfs</td>
<td>48</td>
<td>334</td>
<td>14.2</td>
<td>335</td>
<td>14.1</td>
<td>332</td>
<td>14.2</td>
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<tr>
<td>620.omnetpp_s</td>
<td>48</td>
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<td>9.15</td>
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<td>9.24</td>
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<td>625.x264_s</td>
<td>48</td>
<td>144</td>
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<td>149</td>
<td>11.8</td>
<td>148</td>
<td>12.0</td>
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<tr>
<td>631.deepsjeng_s</td>
<td>48</td>
<td>309</td>
<td>4.64</td>
<td>304</td>
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<td>641.leela_s</td>
<td>48</td>
<td>410</td>
<td>4.16</td>
<td>412</td>
<td>4.14</td>
<td>410</td>
<td>4.16</td>
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<td>648.exchange2_s</td>
<td>48</td>
<td>189</td>
<td>15.6</td>
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<td>15.6</td>
<td>188</td>
<td>15.6</td>
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<tr>
<td>657.xz_s</td>
<td>48</td>
<td>315</td>
<td>19.7</td>
<td>314</td>
<td>19.7</td>
<td>313</td>
<td>19.7</td>
</tr>
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at http://developer.amd.com/amd-aocc/

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory sync then drop_caches=3 to reset caches before invoking runcpu

dirty_ratio, swappiness, zone_reclaim_mode and drop_caches were all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages set to 'always' for this run (OS default)
## General Notes

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26.

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
Dell Inc.
PowerEdge R7515 (AMD EPYC 7552, 2.20 GHz)

**General Notes (Continued)**

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flto
jemalloc 5.1.0 is available here:
https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2

**Platform Notes**

BIOS settings:
- NUMA Nodes Per Socket set to 2
- CCX as NUMA Domain set to Enabled
- System Profile set to Custom
- CPU Power Management set to Maximum Performance
- Memory Frequency set to Maximum Performance
- Turbo Boost Enabled
- Cstates set to Enabled
- Memory Patrol Scrub Disabled
- Memory Refresh Rate set to 1x
- PCI ASPM L1 Link Power Management Disabled
- Determinism Slider set to Power Determinism
- Efficiency Optimized Mode Disabled
- Memory Interleaving set to Disabled
- Memory Freq set to 3200
- Fan Speed = Maximum

Sysinfo program /root/cpu2017-1.1.0/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011
running on linux-g3ob Fri Jan 24 04:59:17 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : AMD EPYC 7552 48-Core Processor
  - 1 "physical id"s (chips)
  - 96 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 48
  - siblings : 96
  - physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24

(Continued on next page)
**SPEC CPU®2017 Integer Speed Result**

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**Dell Inc.**

PowerEdge R7515 (AMD EPYC 7552, 2.20 GHz)

**SPECspeed®2017_int_base = 8.43**

**SPECspeed®2017_int_peak = 8.76**

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**Test Date:** Jan-2020  
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---

**Platform Notes (Continued)**

```
From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
Address sizes: 43 bits physical, 48 bits virtual
CPU(s): 96
On-line CPU(s) list: 0-95
Thread(s) per core: 2
Core(s) per socket: 48
Socket(s): 1
NUMA node(s): 12
Vendor ID: AuthenticAMD
CPU family: 23
Model: 49
Model name: AMD EPYC 7552 48-Core Processor
Stepping: 0
CPU MHz: 2195.691
BogoMIPS: 4391.38
Virtualization: AMD-V
L1d cache: 32K
L1i cache: 32K
L2 cache: 512K
L3 cache: 16384K
NUMA node0 CPU(s): 0-3,48-51
NUMA node1 CPU(s): 4-7,52-55
NUMA node2 CPU(s): 8-11,56-59
NUMA node3 CPU(s): 12-15,60-63
NUMA node4 CPU(s): 16-19,64-67
NUMA node5 CPU(s): 20-23,68-71
NUMA node6 CPU(s): 24-27,72-75
NUMA node7 CPU(s): 28-31,76-79
NUMA node8 CPU(s): 32-35,80-83
NUMA node9 CPU(s): 36-39,84-87
NUMA node10 CPU(s): 40-43,88-91
NUMA node11 CPU(s): 44-47,92-95
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl xtopology nonstop_tsc cpuid extd_apicid aperf perf
pi pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx
f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse
3dnowprefetch osvw lws kinit wdt tce topoext perfctr_core perfctr_nb bpxext
perfctr_l2 mwaitx cpb cat_l3 cdp_l3 hw_pstate sme ssbd dev ibrs ibpb stibp vmmcall
fsgsbase bmi1 avx2 amxa bmi2 cqm rdt_a rdseed adx smap clflushopt clwb sha ni
xsaveopt xsave vgetbv1 xsavec cq _llc cqm_occup_llc cqm mbm_total cqm mbm_local
czero irperf xsaveopt arat npt lbrv svm_lock nrip save tsc_scale vmbc_clean
```

(Continued on next page)
## Platform Notes (Continued)

flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif umip
rdpid overflow_recov succor smca

/proc/cpuinfo cache data
  cache size : 512 KB

From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 12 nodes (0-11)
  node 0 cpus:  0 1 2 3 48 49 50 51
  node 0 size:  20923 MB
  node 0 free:  20677 MB
  node 1 cpus:  4 5 6 7 52 53 54 55
  node 1 size:  21472 MB
  node 1 free:  21343 MB
  node 2 cpus:  8 9 10 11 56 57 58 59
  node 2 size:  21502 MB
  node 2 free:  21402 MB
  node 3 cpus:  12 13 14 15 60 61 62 63
  node 3 size:  21501 MB
  node 3 free:  21376 MB
  node 4 cpus:  16 17 18 19 64 65 66 67
  node 4 size:  21501 MB
  node 4 free:  21369 MB
  node 5 cpus:  20 21 22 23 68 69 70 71
  node 5 size:  21503 MB
  node 5 free:  21406 MB
  node 6 cpus:  24 25 26 27 72 73 74 75
  node 6 size:  21501 MB
  node 6 free:  21396 MB
  node 7 cpus:  28 29 30 31 76 77 78 79
  node 7 size:  21501 MB
  node 7 free:  21409 MB
  node 8 cpus:  32 33 34 35 80 81 82 83
  node 8 size:  21503 MB
  node 8 free:  21408 MB
  node 9 cpus:  36 37 38 39 84 85 86 87
  node 9 size:  21501 MB
  node 9 free:  21319 MB
  node 10 cpus: 40 41 42 43 88 89 90 91
  node 10 size: 21501 MB
  node 10 free: 21409 MB
  node 11 cpus: 44 45 46 47 92 93 94 95
  node 11 size: 21489 MB
  node 11 free: 21398 MB
  node distances:
 node 0 1 2 3 4 5 6 7 8 9 10 11

(Continued on next page)
Dell Inc.  
PowerEdge R7515 (AMD EPYC 7552, 2.20 GHz)  

**SPEC®2017_int_base = 8.43**  
**SPEC®2017_int_peak = 8.76**

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**Test Sponsor:** Dell Inc.  
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**Test Date:** Jan-2020  
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**Platform Notes (Continued)**

```
0: 10 11 11 11 11 11 12 12 12 12 12 12
1: 11 10 11 11 11 11 12 12 12 12 12 12
2: 11 11 10 11 11 11 12 12 12 12 12 12
3: 11 11 11 11 11 11 12 12 12 12 12 12
4: 11 11 11 11 11 11 12 12 12 12 12 12
5: 11 11 11 11 11 11 12 12 12 12 12 12
6: 12 12 12 12 12 12 12 12 12 12 12 12
7: 12 12 12 12 12 12 12 12 12 12 12 12
8: 12 12 12 12 12 12 12 12 12 12 12 12
9: 12 12 12 12 12 12 12 12 12 12 12 12
10: 12 12 12 12 12 12 12 12 12 12 12 12
11: 12 12 12 12 12 12 12 12 12 12 12 12
```

```
From /proc/meminfo  
MemTotal: 263581788 kB  
HugePages_Total: 0  
Hugepagesize: 2048 kB
```

```
From /etc/*release* /etc/*version*  
  os-release:  
    NAME="SLES"  
    VERSION="15-SP1"  
    VERSION_ID="15.1"  
    PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"  
    ID="sles"  
    ID_LIKE="suse"  
    ANSI_COLOR="0;32"  
    CPE_NAME="cpe:/o:suse:sles:15:sp1"
```

```
uname -a:  
Linux linux-g3ob 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)  
x86_64 x86_64 x86_64 GNU/Linux
```

**Kernel self-reported vulnerability status:**

- **CVE-2018-3620 (L1 Terminal Fault):** Not affected
- **Microarchitectural Data Sampling:** Not affected
- **CVE-2017-5754 (Meltdown):** Not affected
- **CVE-2018-3639 (Speculative Store Bypass):** Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- **CVE-2017-5753 (Spectre variant 1):** Mitigation: __user pointer sanitization
- **CVE-2017-5715 (Spectre variant 2):** Mitigation: Full AMD retpoline, IBPB: conditional, IBRS_FW, STIBP: conditional, RSB filling

```
run-level 3 Jan 23 08:17 last=5
```

(Continued on next page)
Dell Inc.

PowerEdge R7515 (AMD EPYC 7552, 2.20 GHz)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
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CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Platform Notes (Continued)

SPEC is set to: /root/cpu2017-1.1.0
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 440G 36G 405G 9% /

From /sys/devices/virtual/dmi/id
BIOS: Dell Inc. 1.3.0 01/14/2020
Vendor: Dell Inc.
Product: PowerEdge R7515
Product Family: PowerEdge
Serial: 5MGPH13

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
8x 80AD80B380AD HMA84GR7CJR4N-XN 32 GB 2 rank 3200
8x Not Specified Not Specified

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
==============================================================================
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
==============================================================================
C++     | 623.xalancbmk_s(peak)
==============================================================================
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
==============================================================================
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base)
(Continued on next page)
Compiler Version Notes (Continued)

<table>
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<tr>
<th>631.deepsjeng_s(base, peak) 641.leela_s(base, peak)</th>
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C++ | 623.xalancbmk_s(peak)
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AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins |
Target: i386-unknown-linux-gnu |
Thread model: posix |
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

==============================================================================
C++ | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base) |
| 631.deepsjeng_s(base, peak) 641.leela_s(base, peak) |
-----------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins |
Target: x86_64-unknown-linux-gnu |
Thread model: posix |
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

==============================================================================
Fortran | 648.exchange2_s(base, peak)
-----------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins |
Target: x86_64-unknown-linux-gnu |
Thread model: posix |
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

Base Compiler Invocation

C benchmarks:
clang

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

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Base Compiler Invocation (Continued)

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Base Portability Flags

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xmlbench_s: -DSPEC_LINUX -DSPEC_LP64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-ffast-math
-march=znver2
-mllvm -unroll-threshold=100
-flv-function-specialization -z muldefs -DSPEC_OPENMP -fopenmp
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc
-lflang

C++ benchmarks:
-ffast-math
-march=znver2
-mllvm -unroll-threshold=100
-flv-function-specialization -z muldefs -DSPEC_OPENMP -fopenmp
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc

(Continued on next page)
## Base Optimization Flags (Continued)

### C++ benchmarks (continued):
- `-lflang`

### Fortran benchmarks:
- `-flto -Wl,-mlllvm -Wl,-function-specialize`
- `-Wl,-mlllvm -Wl,-region-vectorize -Wl,-mlllvm -Wl,-vector-library=LIBMVEC`
- `-Wl,-mlllvm -Wl,-reduce-array-computations=3 -ffast-math`
- `-Wl,-mlllvm -Wl,-inline-recursion=4 -Wl,-mlllvm -Wl,-lsr-in-nested-loop`
- `-Wl,-mlllvm -Wl,-enable-iv-split -O3 -march=znver2 -funroll-loops`
- `-Mrecursive -mlllvm -vector-library=LIBMVEC -z muldefs`
- `-mlllvm -disable-indvar-simplify -mlllvm -unroll-aggressive`
- `-mlllvm -unroll-threshold=150 -DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamilibm -ljemalloc -lflang`

## Base Other Flags

### C benchmarks:
- `-Wno-return-type`

### C++ benchmarks:
- `-Wno-return-type`

### Fortran benchmarks:
- `-Wno-return-type`

## Peak Compiler Invocation

### C benchmarks:
- `clang`

### C++ benchmarks:
- `clang++`

### Fortran benchmarks:
- `flang`

## Peak Portability Flags

600.perlbench_s: `-DSPEC_LINUX_X64 -DSPEC_LP64`

(Continued on next page)
### Dell Inc.

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</table>

### Peak Portability Flags (Continued)

- 602.gcc_s: -DSPEC_LP64
- 605.mcf_s: -DSPEC_LP64
- 620.omnetpp_s: -DSPEC_LP64
- 623.xalancbmk_s: -DSPEC_LINUX -D_FILE_OFFSET_BITS=64
- 625.x264_s: -DSPEC_LP64
- 631.deepsjeng_s: -DSPEC_LP64
- 641.leela_s: -DSPEC_LP64
- 648.exchange2_s: -DSPEC_LP64
- 657.xz_s: -DSPEC_LP64

### Peak Optimization Flags

**C benchmarks:**

- 600.perlbench_s: -flto -Wl, -mllvm -Wl, -function-specialize
- -Wl, -mllvm -Wl, -region-vectorize
- -Wl, -mllvm -Wl, -vector-library=LIBMVEC
- -Wl, -mllvm -Wl, -reduce-array-computations=3
- -fprofile-instr-generate(pass 1)
- -fprofile-instr-use(pass 2) -Ofast -march=znver2
- -mno-sse4a -fstruct-layout=5
- -mllvm -vectorize-memory-aggressively
- -mllvm -function-specialize -mllvm -enable-gvn-hoist
- -mllvm -unroll-threshold=50 -fremap-arrays
- -mllvm -vector-library=LIBMVEC
- -mllvm -reduce-array-computations=3
- -mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
- -flv-function-specialization -DSPEC_OPENMP -fopenmp
- -lmvec -landlibm -fopenmp=libomp -lomp -lpthread -ldl
- -ljemalloc -lflang

(Continued on next page)
Dell Inc.

PowerEdge R7515 (AMD EPYC 7552, 2.20 GHz)

SPECspeed®2017_int_base = 8.43
SPECspeed®2017_int_peak = 8.76

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Jan-2020
Hardware Availability: Apr-2020
Software Availability: Aug-2019

Peak Optimization Flags (Continued)

602.gcc_s (continued):
-ldl -ljemalloc

605.mcf_s: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -DSPEC_OPENMP -fopenmp
-lmvec -lamdlibm -fopenmp=libomp -lomp -lpthread -ldl
-ljemalloc -lflang

625.x264_s: Same as 600.perlbench_s

657.xz_s: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -DSPEC_OPENMP -fopenmp
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm
-ljemalloc -lflang

C++ benchmarks:

620.omnetpp_s: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -flv-function-specialization
-mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000
-mllvm -vector-library=LIBMVEC

(Continued on next page)
Dell Inc. PowerEdge R7515 (AMD EPYC 7552, 2.20 GHz)

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Peak Optimization Flags (Continued)

620.omnetpp_s (continued):
-mlvm -inline-threshold=1000 -DSPEC_OPENMP -fopenmp
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm
-ljemalloc -lflang

623.xalancbmk_s: -m32 -flto -Wl,-mlvm -Wl,-function-specialize
-Wl,-mlvm -Wl,-region-vectorize
-Wl,-mlvm -Wl,-vector-library=LIBMVEC
-Wl,-mlvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -flv-function-specialization
-mlvm -unroll-threshold=100
-mlvm -enable-partial-unswitch
-mlvm -loop-unswitch-threshold=200000
-mlvm -vector-library=LIBMVEC
-mlvm -inline-threshold=1000 -DSPEC_OPENMP -fopenmp
-fopenmp=libomp -lomp -lpthread -ldl -ljemalloc

631.deepsjeng_s: Same as 620.omnetpp_s

641.leela_s: basepeak = yes

Fortran benchmarks:
-flto -Wl,-mlvm -Wl,-function-specialize
-Wl,-mlvm -Wl,-region-vectorize -Wl,-mlvm -Wl,-vector-library=LIBMVEC
-Wl,-mlvm -Wl,-reduce-array-computations=3 -ffast-math
-Wl,-mlvm -Wl,-inline-recursion=4 -Wl,-mlvm -Wl,-lsr-in-nested-loop
-Wl,-mlvm -Wl,-enable-iv-split -O3 -march=znver2 -funroll-loops
-Mrecursive -mlvm -vector-library=LIBMVEC
-mlvm -disable-indvar-simplify -mlvm -unroll-aggressive
-mlvm -unroll-threshold=150 -DSPEC_OPENMP -fopenmp -fopenmp=libomp
-lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc -lflang

Peak Other Flags

C benchmarks:
-\!-Wno-return-type

C++ benchmarks (except as noted below):
-\!-Wno-return-type

623.xalancbmk_s: -Wno-return-type
-L/sppo/dev/cpu2017/v110/amd_speed_aocc200_rome_C_lib/32

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Dell Inc.

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Peak Other Flags (Continued)

Fortran benchmarks:
-Wno-return-type

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

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