# SPEC CPU®2017 Integer Rate Result

**Lenovo Global Technology**

**ThinkSystem SN550**

(2.40 GHz, Intel Xeon Gold 6240R)

**Copyright 2017-2020 Standard Performance Evaluation Corporation**

**SPECrater®2017_int_base = 280**

**SPECrater®2017_int_peak = Not Run**

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<table>
<thead>
<tr>
<th>Test Date:</th>
<th>Apr-2020</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Mar-2020</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Nov-2019</td>
</tr>
</tbody>
</table>

---

## Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon Gold 6240R</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz:</td>
<td>4000</td>
</tr>
<tr>
<td>Nominal:</td>
<td>2400</td>
</tr>
<tr>
<td>Enabled:</td>
<td>48 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1.2 chips</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3:</td>
<td>35.75 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>384 GB (24 x 16 GB 2Rx8 PC4-2933Y-R)</td>
</tr>
<tr>
<td>Storage:</td>
<td>1 x 960 GB SATA SSD</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
</tbody>
</table>

## Software

<table>
<thead>
<tr>
<th>OS:</th>
<th>Red Hat Enterprise Linux 8.1 (Ootpa)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 19.0.5.281 of Intel</td>
</tr>
<tr>
<td>Compiler for Linux:</td>
<td>Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td>Parallel:</td>
<td>No</td>
</tr>
<tr>
<td>Firmware:</td>
<td>Lenovo BIOS Version IVE152L 2.51 released Feb-2020 tested as IVE151L 2.51 Jan-2020</td>
</tr>
<tr>
<td>File System:</td>
<td>xfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>Not Applicable</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>Power Management:</td>
<td>BIOS set to prefer performance at the cost of additional power usage</td>
</tr>
</tbody>
</table>
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**CPU2017 License:** 9017  
**Test Date:** Apr-2020

**Test Sponsor:** Lenovo Global Technology  
**Hardware Availability:** Mar-2020

**Tested by:** Lenovo Global Technology  
**Software Availability:** Nov-2019

---

**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>96</td>
<td>766</td>
<td>200</td>
<td>769</td>
<td>199</td>
<td>769</td>
<td>199</td>
<td></td>
<td></td>
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</tr>
<tr>
<td>502.gcc_r</td>
<td>96</td>
<td>588</td>
<td>231</td>
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<td>230</td>
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<tr>
<td>505.mcf_r</td>
<td>96</td>
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</tr>
<tr>
<td>520.omnetpp_r</td>
<td>96</td>
<td>649</td>
<td>194</td>
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<td>194</td>
<td>649</td>
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<tr>
<td>523.xalancbmk_r</td>
<td>96</td>
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<td>313</td>
<td>325</td>
<td>312</td>
<td>325</td>
<td>312</td>
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<tr>
<td>525.x264_r</td>
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<td>594</td>
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<td>593</td>
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</tr>
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<td>531.deepsjeng_r</td>
<td>96</td>
<td>463</td>
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</tr>
<tr>
<td>541.leela_r</td>
<td>96</td>
<td>717</td>
<td>222</td>
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<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>96</td>
<td>456</td>
<td>552</td>
<td>456</td>
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<td>456</td>
<td>552</td>
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</tr>
<tr>
<td>557.xz_r</td>
<td>96</td>
<td>581</td>
<td>179</td>
<td>580</td>
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<td>580</td>
<td>179</td>
<td></td>
<td></td>
<td></td>
<td></td>
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</tr>
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</table>

**SPECrate®2017_int_base = 280**  
**SPECrate®2017_int_peak = Not Run**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

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**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

---

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

---

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH =  
"/home/cpu2017-1.1.0-ic19.0u5-2/lib/intel64:/home/cpu2017-1.1.0-ic19.0u5-2/lib/ia32:/home/cpu2017-1.1.0-ic19.0u5-2/je5.0.1-32"

MALLOC_CONF = "retain:true"

---

**General Notes**

Binaries compiled on a system with 1x Intel Core i9-9900K CPU + 64GB RAM  
memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop_caches

(Continued on next page)
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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Apr-2020
Hardware Availability: Mar-2020
Software Availability: Nov-2019

General Notes (Continued)

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode
CPU P-state Control set to Cooperative
MONITOR/MWAIT set to Enable
Trusted Execution Technology set to Enable
SNC set to Enable
Stale AtoS set to Enable
Patrol Scrub set to Disable

Sysinfo program /home/cpu2017-1.1.0-ic19.0u5-2/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edbi6e646a485a0011
running on localhost.localdomain Thu Apr 9 23:11:20 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6240R CPU @ 2.40GHz
  2 "physical id"s (chips)
  96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 24
  siblings : 48
  physical 0: cores 0 1 2 3 4 5 6 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
  physical 1: cores 0 1 2 3 4 5 6 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit

(Continued on next page)
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Platform Notes (Continued)

Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95
Thread(s) per core: 2
Core(s) per socket: 24
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6240R CPU @ 2.40GHz
Stepping: 7
CPU MHz: 1490.939
CPU max MHz: 4000.0000
CPU min MHz: 1000.0000
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0-3, 7, 8, 12-14, 18-20, 48-51, 55, 56, 60-62, 66-68
NUMA node1 CPU(s): 4-6, 9-11, 15-17, 21-23, 52-54, 57-59, 63-65, 69-71
NUMA node2 CPU(s): 24-27, 31, 32, 36-38, 42-44, 72-75, 79, 80, 84-86, 90-92
NUMA node3 CPU(s): 28-30, 33-35, 39-41, 45-47, 76-78, 81-83, 87-89, 93-95
Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe poprdop tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_pppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase tsfc_adjust bmi1 hle avx2 smep bmi2 erts invpcid rm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaves xsaveopt xsavevc xsaves cqm_llc cqm_occucc_llc cqm_mbb_total cqm_mbb_local dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req pku ospke avx512_vnni md_clear flush_l1d arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 7 8 12 13 14 18 19 20 48 49 50 51 55 56 60 61 62 66 67 68
node 0 size: 96356 MB
node 0 free: 91601 MB

(Continued on next page)
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Platform Notes (Continued)

node 1 cpus: 4 5 6 9 10 11 15 16 17 21 22 23 52 53 54 57 58 59 63 64 65 69 70 71
node 1 size: 96763 MB
node 1 free: 96405 MB
node 2 cpus: 24 25 26 27 31 32 36 37 38 42 43 44 72 73 74 75 79 80 84 85 86 90 91 92
node 2 size: 96763 MB
node 2 free: 91911 MB
node 3 cpus: 28 29 30 33 34 35 39 40 41 45 46 47 76 77 78 81 82 83 87 88 89 93 94 95
node 3 size: 96762 MB
node 3 free: 95727 MB
node distances:
node 0 1 2 3
0: 10 11 21 21
1: 11 10 21 21
2: 21 21 10 11
3: 21 21 11 10

From /proc/meminfo
MemTotal: 395925240 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.1 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.1"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.1 (Ootpa)"
ANSI_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.1:ga

uname -a:
Linux localhost.localdomain 4.18.0-147.el8.x86_64 #1 SMP Thu Sep 26 15:52:44 UTC 2019
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user

(Continued on next page)
Platform Notes (Continued)

pointer sanitization

CVE-2017-5715 (Spectre variant 2):
Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 Apr 9 20:15

SPEC is set to: /home/cpu2017-1.1.0-ic19.0u5-2
Filesystem Type Size Used Avail Use% Mounted on
/dev/sdb2 xfs 839G 19G 820G 3% /home

From /sys/devices/virtual/dmi/id
BIOS: Lenovo -[IVE151L-2.51]- 01/14/2020
Vendor: Lenovo
Product: ThinkSystem SN550 -[7X16CT00WW]-
Product Family: ThinkSystem
Serial: 1234567890

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
24x Samsung M393A2K43CB2-CVF 16 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base)
          | 525.x264_r(base) 557.xz_r(base)
==============================================================================
Intel(R) C Compiler for applications running on Intel(R) 64, Version 19.0.5
NextGen Technology Build 20190729
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
==============================================================================

C++     | 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
          | 541.leela_r(base)
==============================================================================
Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 19.0.5
NextGen Technology Build 20190729
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

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### Compiler Version Notes (Continued)

<table>
<thead>
<tr>
<th>Fortran</th>
<th>548.exchange2_r(base)</th>
</tr>
</thead>
</table>

Intel (R) Fortran Intel (R) 64 Compiler for applications running on Intel (R) 64, Version 19.0.5.281 Build 20190815  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

### Base Compiler Invocation

- **C benchmarks:**  
  icc

- **C++ benchmarks:**  
  icpc

- **Fortran benchmarks:**  
  ifort

### Base Portability Flags

- 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
- 502.gcc_r: -DSPEC_LP64  
- 505.mcf_r: -DSPEC_LP64  
- 520.omnetpp_r: -DSPEC_LP64  
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
- 525.x264_r: -DSPEC_LP64  
- 531.deepsjeng_r: -DSPEC_LP64  
- 541.leela_r: -DSPEC_LP64  
- 548.exchange2_r: -DSPEC_LP64  
- 557.xz_r: -DSPEC_LP64

### Base Optimization Flags

- **C benchmarks:**  
  -m64 -std=c11 -W1,-z,muldefs -xCORE-AVX2 -Ofast -flto -mfpmath=sse  
  -funroll-loops -qnextgen -fuse-ld=gold -qopt-mem-layout-trans=4  
  -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin  
  -lqkmalloc

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Base Optimization Flags (Continued)

C++ benchmarks:
-m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -flto -mfpmath=sse
-funroll-loops -qnextgen -fuse-ld=gold -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin
-1qkmalloc

Fortran benchmarks:
-m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ipo -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin
-1qkmalloc

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-G.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-G.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-04-09 11:11:19-0400.