# SPEC CPU®2017 Integer Speed Result

## Dell Inc.

PowerEdge MX740 (Intel Xeon Gold 6242R, 3.10 GHz)

<table>
<thead>
<tr>
<th>Test Sponsor:</th>
<th>Dell Inc.</th>
<th>Test Date:</th>
<th>Apr-2020</th>
</tr>
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<tr>
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<td>Dell Inc.</td>
<td>Hardware Availability:</td>
<td>Feb-2020</td>
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### SPECspeed®2017_int_base = 9.97

### SPECspeed®2017_int_peak = 10.1

### Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
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<td>6.77</td>
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<td>12.8</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>80</td>
<td>6.70</td>
<td>11.0</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>80</td>
<td>14.2</td>
<td>16.1</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>80</td>
<td>5.63</td>
<td>24.3</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>80</td>
<td>4.81</td>
<td>24.3</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>80</td>
<td></td>
<td></td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>80</td>
<td></td>
<td></td>
</tr>
<tr>
<td>657.xz_s</td>
<td>80</td>
<td></td>
<td></td>
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### CPU Name: Intel Xeon Gold 6242R

- Max MHz: 4100
- Nominal: 3100
- Enabled: 40 cores, 2 chips, 2 threads/core
- Orderable: 1.2 chips
- Cache L1: 32 KB I + 32 KB D on chip per core
- L2: 1 MB I+D on chip per core
- L3: 35.75 MB I+D on chip per chip
- Other: None
- Memory: 768 GB (24 x 32 GB 2Rx8 PC4-2933V-R, running at 2933)
- Storage: 1 x 480 GB SATA SSD

### OS: Red Hat Enterprise Linux 8.1

- kernel 4.18.0-147.el8.x86_64

### Compiler: C/C++: Version 19.0.5.281 of Intel C/C++ Compiler for Linux;

### Fortran: Version 19.0.5.281 of Intel Fortran Compiler for Linux

### Parallel: Yes

### Firmware: Version 2.7.1 released Feb-2020

### File System: tmpfs

### System State: Run level 3 (multi-user)

### Base Pointers: 64-bit

### Peak Pointers: 64-bit

### Other: jemalloc memory allocator V5.0.1

### Power Management: BIOS set to prefer performance at the cost of additional power usage
Dell Inc.  
PowerEdge MX740 (Intel Xeon Gold 6242R, 3.10 GHz)  

Results Table

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<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
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<tr>
<td>600.perlbench_s</td>
<td>80</td>
<td>262</td>
<td>6.77</td>
<td>262</td>
<td>6.78</td>
<td>262</td>
<td>6.77</td>
<td>80</td>
<td>234</td>
<td>7.58</td>
<td>233</td>
<td>7.63</td>
<td>233</td>
<td>7.63</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>80</td>
<td>396</td>
<td>10.0</td>
<td>396</td>
<td>10.1</td>
<td>395</td>
<td>10.1</td>
<td>80</td>
<td>385</td>
<td>10.3</td>
<td>387</td>
<td>10.3</td>
<td>387</td>
<td>10.3</td>
</tr>
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<td>80</td>
<td>372</td>
<td>12.7</td>
<td>373</td>
<td>12.7</td>
<td>372</td>
<td>12.7</td>
<td>80</td>
<td>368</td>
<td>12.8</td>
<td>370</td>
<td>12.8</td>
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<td>80</td>
<td>239</td>
<td>6.82</td>
<td>238</td>
<td>6.86</td>
<td>240</td>
<td>6.80</td>
<td>80</td>
<td>243</td>
<td>6.70</td>
<td>243</td>
<td>6.71</td>
<td>246</td>
<td>6.64</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>80</td>
<td>128</td>
<td>11.1</td>
<td>130</td>
<td>10.9</td>
<td>128</td>
<td>11.0</td>
<td>80</td>
<td>128</td>
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<td>80</td>
<td>256</td>
<td>5.60</td>
<td>255</td>
<td>5.63</td>
<td>254</td>
<td>5.63</td>
<td>80</td>
<td>256</td>
<td>5.60</td>
<td>255</td>
<td>5.63</td>
<td>254</td>
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<td>4.81</td>
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<td>80</td>
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<td>183</td>
<td>16.1</td>
<td>183</td>
<td>16.1</td>
<td>183</td>
<td>16.0</td>
<td>80</td>
<td>183</td>
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<td>254</td>
<td>24.3</td>
<td>80</td>
<td>254</td>
<td>24.3</td>
<td>254</td>
<td>24.3</td>
<td>255</td>
<td>24.2</td>
</tr>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/dev/shm/cpu2017/lib/intel64:/dev/shm/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)
Dell Inc. PowerEdge MX740 (Intel Xeon Gold 6242R, 3.10 GHz)

**SPECspeed®2017_int_base = 9.97**

**SPECspeed®2017_int_peak = 10.1**

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**General Notes (Continued)**

Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
- `sync; echo 3 > /proc/sys/vm/drop_caches`
- `runcpu` command invoked through `numactl` i.e.:
  - `numactl --interleave=all runcpu <etc>`
- jemalloc, a general purpose malloc implementation
  built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

---

**Platform Notes**

BIOS settings:
- Sub NUMA Cluster enabled
- Virtualization Technology disabled
- System Profile set to Custom
- CPU Performance set to Maximum Performance
- C States set to Autonomous
- C1E disabled
- Uncore Frequency set to Dynamic
- Energy Efficiency Policy set to Performance
- Memory Patrol Scrub set to standard
- Logical Processor enabled
- CPU Interconnect Bus Link Power Management disabled
- PCI ASPM L1 Link Power Management disabled

Sysinfo program /dev/shm/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edbd1e6e46a485a0011
running on localhost.localdomain Sun May 10 18:16:59 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) Gold 6242R CPU @ 3.10GHz
-  2 "physical id"s (chips)
-  80 "processors"
-  cores, siblings (Caution: counting these is hw and system dependent. The following
  excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 20
  - siblings : 40
  - physical 0: cores 0 1 3 5 6 8 10 12 13 16 17 18 19 20 21 25 26 27 28 29
  - physical 1: cores 0 1 2 4 5 6 8 9 10 11 12 13 16 17 18 19 21 26 28 29

From lscpu:

(Continued on next page)
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CPU2017 License: 55
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Test Date: Apr-2020
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Platform Notes (Continued)

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 80
On-line CPU(s) list: 0-79
Thread(s) per core: 2
Core(s) per socket: 20
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6242R CPU @ 3.10GHz
Stepping: 7
CPU MHz: 3640.632
CPU max MHz: 4100.0000
CPU min MHz: 1200.0000
BogoMIPS: 6200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0, 4, 8, 12, 16, 20, 24, 28, 32, 36, 40, 44, 48, 52, 56, 60, 64, 68, 72, 76
NUMA node1 CPU(s): 1, 5, 9, 13, 17, 21, 25, 29, 33, 37, 41, 45, 49, 53, 57, 61, 65, 69, 73, 77
NUMA node2 CPU(s): 2, 6, 10, 14, 18, 22, 26, 30, 34, 38, 42, 46, 50, 54, 58, 62, 66, 70, 74, 78
NUMA node3 CPU(s): 3, 7, 11, 15, 19, 23, 27, 31, 35, 39, 43, 47, 51, 55, 59, 63, 67, 71, 75, 79
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
apts36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm
cache data constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_pmm ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occap_llc cqm_mbb_total
cqm_mbb_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_l1d
arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60 64 68 72 76

(Continued on next page)
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SPECspeed®2017_int_base = 9.97
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CPU2017 License: 55
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Tested by: Dell Inc.

Platform Notes (Continued)

node 0 size: 192071 MB
node 0 free: 159466 MB
node 1 cpus: 1 5 9 13 17 21 25 29 33 37 41 45 49 53 57 61 65 69 73 77
node 1 size: 193531 MB
node 1 free: 181914 MB
node 2 cpus: 2 6 10 14 18 22 26 30 34 38 42 46 50 54 58 62 66 70 74 78
node 2 size: 193506 MB
node 2 free: 181789 MB
node 3 cpus: 3 7 11 15 19 23 27 31 35 39 43 47 51 55 59 63 67 71 75 79
node 3 size: 193531 MB
node 3 free: 182394 MB
node distances:
node 0 1 2 3
0: 10 21 11 21
1: 21 10 21 11
2: 11 21 10 21
3: 21 11 21 10

From /proc/meminfo
MemTotal: 791184568 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.1 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.1"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.1 (Ootpa)"
ANSI_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.1:ga

uname -a:
Linux localhost.localdomain 4.18.0-147.el8.x86_64 #1 SMP Thu Sep 26 15:52:44 UTC 2019
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled

(Continued on next page)
Platform Notes (Continued)

CVE-2017-5753 (Spectre variant 1): mitctl and seccomp
Mitigation: usercopy/swapgs barriers and __user pointer sanitation

CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 May 8 12:41

SPEC is set to: /dev/shm/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 378G 55G 323G 15% /dev/shm

From /sys/devices/virtual/dmi/id
BIOS: Dell Inc. 2.7.1 02/14/2020
Vendor: Dell Inc.
Product: PowerEdge MX740c
Product Family: PowerEdge
Serial: 1234567

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
   21x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
   1x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
   2x 00AD069D00AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C    | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
==============================================================================

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.5.281 Build 20190815
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
C++  | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
==============================================================================

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
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### Compiler Version Notes (Continued)

Version 19.0.5.281 Build 20190815  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Compiler Invocation:

- **C benchmarks:**  
  - icc

- **C++ benchmarks:**  
  - icpc

- **Fortran benchmarks:**  
  - ifort

### Base Portability Flags

- `600.perlbench_s`: `-DSPEC_LP64` `-DSPEC_LINUX_X64`
- `602.gcc_s`: `-DSPEC_LP64`
- `605.mcf_s`: `-DSPEC_LP64`
- `620.omnetpp_s`: `-DSPEC_LP64`
- `623.xalancbmk_s`: `-DSPEC_LP64` `-DSPEC_LINUX`
- `625.x264_s`: `-DSPEC_LP64`
- `631.deepsjeng_s`: `-DSPEC_LP64`
- `641.leela_s`: `-DSPEC_LP64`
- `648.exchange2_s`: `-DSPEC_LP64`
- `657.xz_s`: `-DSPEC_LP64`

### Base Optimization Flags

- **C benchmarks:**  
  - `-m64` `-std=c11` `-Wl,-z,muldefs` `-xCORE-AVX512` `-ipo -O3 -no-prec-div` `-qopt-mem-layout-trans=4` `-qopenmp` `-DSPEC_OPENMP`
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Copyright 2017-2020 Standard Performance Evaluation Corporation

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Base Optimization Flags (Continued)

C benchmarks (continued):
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-m64 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-m64 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
600.perlbench_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)
-prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-mem-layout-trans=4 -ipo -O3 -no-prec-div
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

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**CPU2017 License:** 55

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**Test Sponsor:** Dell Inc.

**Tested by:** Dell Inc.

**Tested by:** Dell Inc.

### Peak Optimization Flags (Continued)

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)
-prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-mem-layout-trans=4 -ipo -O3 -no-prec-div
-DSPEC_SUPPRESS_OPENMP -L/usr/local/je5.0.1-64/lib
-ljemalloc

605.mcf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)
-prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div
-qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: basepeak = yes

657.xz_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)
-ipo -xCORE-AVX512 -O3 -no-prec-div
-qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

**C++ benchmarks:**

620.omnetpp_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-ipo -xCORE-AVX512 -O3 -no-prec-div
-qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-ipo -xCORE-AVX512 -O3 -no-prec-div
-qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-DSPEC_OPENMP -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64 LIN
-lqkmalloc

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

**Fortran benchmarks:**

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:


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Standard Performance Evaluation Corporation (info@spec.org) https://www.spec.org/
# SPEC CPU®2017 Integer Speed Result

Dell Inc.  
PowerEdge MX740 (Intel Xeon Gold 6242R, 3.10 GHz)  

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base = 9.97</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_int_peak = 10.1</td>
</tr>
</tbody>
</table>

| CPU2017 License: | 55 |
| Test Sponsor: | Dell Inc. |
| Tested by: | Dell Inc. |
| Test Date: | Apr-2020 |
| Hardware Availability: | Feb-2020 |
| Software Availability: | Nov-2019 |

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-05-10 18:16:59-0400.  