## Dell Inc. PowerEdge MX740C (Intel Xeon Gold 5218R, 2.10 GHz)

**CPU2017 License:** 55
**Test Sponsor:** Dell Inc.
**Tested by:** Dell Inc.
**Test Date:** May-2020
**Hardware Availability:** Feb-2020
**Software Availability:** Nov-2019

### SPEC CPU 2017 Integer Rate Result

<table>
<thead>
<tr>
<th>Test</th>
<th>Copies</th>
<th>SPECrate 2017_int_base</th>
<th>SPECrate 2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>80</td>
<td>181</td>
<td>185</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>80</td>
<td>215</td>
<td></td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>80</td>
<td>160</td>
<td></td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>80</td>
<td>261</td>
<td></td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>80</td>
<td>266</td>
<td></td>
</tr>
<tr>
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<td>463</td>
<td></td>
</tr>
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<td>80</td>
<td>182</td>
<td></td>
</tr>
<tr>
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<td>80</td>
<td>173</td>
<td></td>
</tr>
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<td>420</td>
<td></td>
</tr>
<tr>
<td>557.xz_r</td>
<td>80</td>
<td>139</td>
<td></td>
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</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 5218R
- **Max MHz:** 4000
- **Nominal:** 2100
- **Enabled:** 40 cores, 2 chips, 2 threads/core
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 27.5 MB I+D on chip per chip
- **Other:** None
- **Memory:** 768 GB (24 x 32 GB 2Rx8 PC4-2933V-R, running at 2666)
- **Storage:** 1 x 960 GB SATA SSD
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux 8.1
  - kernel 4.18.0-147.el8.x86_64
- **Compiler:** C/C++: Version 19.0.5.281 of Intel C/C++ Compiler for Linux;
  - Fortran: Version 19.0.5.281 of Intel Fortran Compiler for Linux
- **Parallel:** No
- **Firmware:** Version 2.7.1 released Feb-2020
- **File System:** tmpfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 32/64-bit
- **Other:** Jemalloc memory allocator V5.0.1
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage
SPEC CPU®2017 Integer Rate Result

Dell Inc.

PowerEdge MX740C (Intel Xeon Gold 5218R, 2.10 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

SPECrate®2017_int_base = 221
SPECrate®2017_int_peak = 229

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Seconds</th>
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</tr>
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<td>80</td>
<td>803</td>
<td>159</td>
<td>798</td>
<td>160</td>
<td>798</td>
<td>160</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>80</td>
<td>614</td>
<td>184</td>
<td>611</td>
<td>185</td>
<td>609</td>
<td>186</td>
</tr>
<tr>
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<td>80</td>
<td>485</td>
<td>266</td>
<td>486</td>
<td>266</td>
<td>484</td>
<td>267</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>80</td>
<td>657</td>
<td>160</td>
<td>657</td>
<td>160</td>
<td>656</td>
<td>160</td>
</tr>
<tr>
<td>523.xalanckmk_r</td>
<td>80</td>
<td>324</td>
<td>260</td>
<td>324</td>
<td>261</td>
<td>324</td>
<td>261</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>80</td>
<td>302</td>
<td>463</td>
<td>302</td>
<td>463</td>
<td>300</td>
<td>466</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>80</td>
<td>499</td>
<td>184</td>
<td>508</td>
<td>180</td>
<td>502</td>
<td>182</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>80</td>
<td>766</td>
<td>173</td>
<td>767</td>
<td>173</td>
<td>769</td>
<td>172</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>80</td>
<td>499</td>
<td>139</td>
<td>620</td>
<td>139</td>
<td>619</td>
<td>140</td>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = 
"/dev/shm/cpu2017/lib/intel64:/dev/shm/cpu2017/lib/ia32:/dev/shm/cpu2017 
/jes5.0.1-32"

MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 1x Intel Core i9-9900K CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

(Continued on next page)
General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
Benchmark run from a 225 GB ramdisk created with the cmd; "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"
jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS settings:
Sub NUMA Cluster enabled
Virtualization Technology disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub set to standard
Logical Processor enabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
UPI Prefetch enabled
LLC Prefetch disabled
Dead Line LLC Alloc enabled
Directory AtoS disabled

Sysinfo program /dev/shm/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7ed6b1e6e46a465a0011
running on localhost.localdomain Tue May 12 14:47:55 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 5218R CPU @ 2.10GHz
    2 "physical id"s (chips)
    80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following

(Continued on next page)
## SPEC CPU®2017 Integer Rate Result

**Dell Inc.**

PowerEdge MX740C (Intel Xeon Gold 5218R, 2.10 GHz)

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**Test Date:** May-2020
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### Platform Notes (Continued)

```
(Continued on next page)
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Dell Inc.

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SPECrate®2017_int_base = 221
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Platform Notes (Continued)

/proc/cpuinfo cache data
  cache size : 28160 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 4 nodes (0-3)
  node 0 cpus: 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60 64 68 72 76
  node 0 size: 192071 MB
  node 0 free: 191738 MB
  node 1 cpus: 1 5 9 13 17 21 25 29 33 37 41 45 49 53 57 61 65 69 73 77
  node 1 size: 193531 MB
  node 1 free: 192881 MB
  node 2 cpus: 2 6 10 14 18 22 26 30 34 38 42 46 50 54 58 62 66 70 74 78
  node 2 size: 193531 MB
  node 2 free: 193201 MB
  node 3 cpus: 3 7 11 15 19 23 27 31 35 39 43 47 51 55 59 63 67 71 75 79
  node 3 size: 193505 MB
  node 3 free: 176877 MB
  node distances:
    node 0 1 2 3
    0: 10 21 11 21
    1: 21 10 21 11
    2: 11 21 10 21
    3: 21 11 21 10

From /proc/meminfo
  MemTotal: 791184568 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.1 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.1"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.1 (Ootpa)"
    ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
  system-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:8.1:ga

uname -a:
  Linux localhost.localdomain 4.18.0-147.el8.x86_64 #1 SMP Thu Sep 26 15:52:44 UTC 2019
  x86_64 x86_64 x86_64 GNU/Linux

(Continued on next page)
Platform Notes (Continued)

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 May 12 12:59

SPEC is set to: /dev/shm/cpu2017
   Filesystem     Type Size  Used Avail Use% Mounted on
tmpfs           tmpfs  378G  7.6G  370G   2%   /dev/shm

From /sys/devices/virtual/dmi/id
   BIOS: Dell Inc. 2.7.1 02/14/2020
   Vendor: Dell Inc.
   Product: PowerEdge MX740c
   Product Family: PowerEdge
   Serial: 1234567

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
   21x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
   1x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
   2x 00AD069D00AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

===============================================================================================
C | 502.gcc_r(peak)
===============================================================================================
Intel(R) C Compiler for applications running on IA-32, Version 19.0.5 NextGen Technology Build 20190729
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

(Continued on next page)
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<td></td>
</tr>
</tbody>
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**Compiler Version Notes (Continued)**

```
Technology Build 20190729
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
```

```
C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
| 525.x264_r(base, peak) 557.xz_r(base)
```

```
Intel(R) C Compiler for applications running on Intel(R) 64, Version 19.0.5
NextGen Technology Build 20190729
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
```

```
C       | 500.perlbench_r(peak) 557.xz_r(peak)
```

```
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.5.281 Build 20190815
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
```

```
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak)
| 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
```

```
Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 19.0.5
NextGen Technology Build 20190729
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
```

```
Fortran | 548.exchange2_r(base, peak)
```

```
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.5.281 Build 20190815
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
```

Base Compiler Invocation

**C benchmarks:**
- icc

**C++ benchmarks:**
- icpc

(Continued on next page)
### Dell Inc.

**PowerEdge MX740C (Intel Xeon Gold 5218R, 2.10 GHz)**

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**Base Compiler Invocation (Continued)**

Fortran benchmarks:
ifort

---

### Base Portability Flags

- 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
- 502gcc_r: -DSPEC_LP64  
- 505.mcf_r: -DSPEC_LP64  
- 520.omnetpp_r: -DSPEC_LP64  
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
- 525.x264_r: -DSPEC_LP64  
- 531.deepsjeng_r: -DSPEC_LP64  
- 541.leela_r: -DSPEC_LP64  
- 548.exchange2_r: -DSPEC_LP64  
- 557.xz_r: -DSPEC_LP64

---

### Base Optimization Flags

**C benchmarks:**
- -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -flto  
- -mfpmath=sse -funroll-loops -qnextgen -fuse-ld=gold  
- -qopt-mem-layout-trans=4  
- -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin  
- -lqkmalloc

**C++ benchmarks:**
- -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -flto -mfpmath=sse  
- -funroll-loops -qnextgen -fuse-ld=gold -qopt-mem-layout-trans=4  
- -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin  
- -lqkmalloc

**Fortran benchmarks:**
- -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div  
- -qopt-mem-layout-trans=4 -nostandard-realloc-lhs  
- -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin  
- -lqkmalloc
## SPEC CPU®2017 Integer Rate Result

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**SPECrate®2017_int_base = 221**

**SPECrate®2017_int_peak = 229**

### Peak Compiler Invocation

**C benchmarks:**

- icc

**C++ benchmarks:**

- icpc

**Fortran benchmarks:**

- ifort

### Peak Portability Flags

- `500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64`
- `502.gcc_r: -D_FILE_OFFSET_BITS=64`
- `505.mcf_r: -DSPEC_LP64`
- `520.omnetpp_r: -DSPEC_LP64`
- `523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX`
- `525.x264_r: -DSPEC_LP64`
- `531.deepsjeng_r: -DSPEC_LP64`
- `541.leela_r: -DSPEC_LP64`
- `548.exchange2_r: -DSPEC_LP64`
- `557.xz_r: -DSPEC_LP64`

### Peak Optimization Flags

**C benchmarks:**

- `500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4 -fno-strict-overflow -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin -lqkmalloc`


- `505.mcf_r: basepeak = yes`
Dell Inc. PowerEdge MX740C (Intel Xeon Gold 5218R, 2.10 GHz)

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**Peak Optimization Flags (Continued)**

525.x264_r: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -flto -03
-ffast-math -qnextgen -fuse-ld=gold
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -03 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: -m64 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -03 -ffast-math -qnextgen -fuse-ld=gold
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin
-lqkmalloc

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.0u5-official-linux64_rev0.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.