Lenovo Global Technology
ThinkSystem SR630
(3.10 GHz, Intel Xeon Gold 6242R)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

SPECrate®2017_fp_base = 259
SPECrate®2017_fp_peak = Not Run

SPECrates

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate®2017_fp_base</th>
<th>SPECrate®2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>80</td>
<td>720</td>
<td>Not Run</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>80</td>
<td>352</td>
<td>Not Run</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>80</td>
<td>284</td>
<td>Not Run</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>80</td>
<td>304</td>
<td>Not Run</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>80</td>
<td>304</td>
<td>Not Run</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>80</td>
<td>304</td>
<td>Not Run</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>80</td>
<td>304</td>
<td>Not Run</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>80</td>
<td>304</td>
<td>Not Run</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>80</td>
<td>304</td>
<td>Not Run</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>80</td>
<td>304</td>
<td>Not Run</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>80</td>
<td>304</td>
<td>Not Run</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>80</td>
<td>304</td>
<td>Not Run</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>80</td>
<td>304</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

Hardware

CPU Name: Intel Xeon Gold 6242R
Max MHz: 4100
Nominal: 3100
Enabled: 40 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 35.75 MB I+D on chip per chip
Other: None
Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R)
Storage: 1 x 800 GB SATA SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP1 (x86_64)
Kernel 4.12.14-195-default
Compiler: C/C++: Version 19.1.1.217 of Intel
C/C++ Compiler for Linux;
Fortran: Version 19.1.1.217 of Intel Fortran
Compiler for Linux
Parallel: No
Firmware: Lenovo BIOS Version IVE155L 2.61 released May-2020
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of additional power usage
## SPEC CPU®2017 Floating Point Rate Result

**Lenovo Global Technology**

ThinkSystem SR630  
(3.10 GHz, Intel Xeon Gold 6242R)

**SPEC CPU®2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>80</td>
<td>1509</td>
<td>532</td>
<td>1506</td>
<td>533</td>
<td>1511</td>
<td>531</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>80</td>
<td>288</td>
<td>351</td>
<td>287</td>
<td>352</td>
<td>285</td>
<td>355</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>80</td>
<td>365</td>
<td>208</td>
<td>365</td>
<td>208</td>
<td>366</td>
<td>208</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>510.parest_r</td>
<td>80</td>
<td>1438</td>
<td>146</td>
<td>1434</td>
<td>146</td>
<td>1435</td>
<td>146</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>80</td>
<td>613</td>
<td>305</td>
<td>616</td>
<td>303</td>
<td>615</td>
<td>304</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>80</td>
<td>686</td>
<td>123</td>
<td>685</td>
<td>123</td>
<td>685</td>
<td>123</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>80</td>
<td>750</td>
<td>239</td>
<td>741</td>
<td>242</td>
<td>747</td>
<td>240</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>526.blender_r</td>
<td>80</td>
<td>454</td>
<td>269</td>
<td>455</td>
<td>268</td>
<td>453</td>
<td>269</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>80</td>
<td>491</td>
<td>285</td>
<td>494</td>
<td>283</td>
<td>493</td>
<td>284</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>80</td>
<td>279</td>
<td>714</td>
<td>280</td>
<td>710</td>
<td>277</td>
<td>719</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>80</td>
<td>291</td>
<td>463</td>
<td>290</td>
<td>464</td>
<td>289</td>
<td>466</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>80</td>
<td>1918</td>
<td>163</td>
<td>1913</td>
<td>163</td>
<td>1910</td>
<td>163</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>80</td>
<td>1135</td>
<td>112</td>
<td>1134</td>
<td>112</td>
<td>1135</td>
<td>112</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**SPECrate®2017_fp_base =** 259  
**SPECrate®2017_fp_peak =** Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler. The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux. The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = 
"/home/cpu2017-1.1.0-ic19.1.1/lib/intel64:/home/cpu2017-1.1.0-ic19.1.1/j 
 e5.0.1-64"
MALLOC_CONF = "retain:true"
```
Lenovo Global Technology
ThinkSystem SR630
(3.10 GHz, Intel Xeon Gold 6242R)

SPECrate®2017_fp_base = 259
SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

CPU2017 License: 9017
Test Date: May-2020
Hardware Availability: Mar-2020
Software Availability: Apr-2020

General Notes
Binaries compiled on a system with 1x Intel Core i9–7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes
BIOS configuration:
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode
C-States set to Legacy
SNC set to Enable
DCU Streamer Prefetcher set to Disable
Trusted Execution Technology set to Enable
Stale AtoS set to Enable
LLC dead line alloc set to Disable
Patrol Scrub set to Disable
Sysinfo program /home/cpu2017-1.1.0-ic19.1.1/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edbbe6e46a485a0011
running on linux-thtl Thu May 28 10:14:41 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 6242R CPU @ 3.10GHz
    2 "physical id"s (chips)
    80 "processors"

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR630
(3.10 GHz, Intel Xeon Gold 6242R)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Hardware Availability: Mar-2020
Tested by: Lenovo Global Technology
Software Availability: Apr-2020

SPECrate®2017_fp_base = 259
SPECrate®2017_fp_peak = Not Run

Platform Notes (Continued)
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 20
siblings : 40
physical 0: cores 0 2 3 5 6 8 9 10 11 12 13 16 17 18 19 21 26 27 28 29
physical 1: cores 0 1 2 3 5 6 10 11 12 13 16 17 18 19 21 24 26 27 28 29

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
Address sizes: 46 bits physical, 48 bits virtual
CPU(s): 80
On-line CPU(s) list: 0-79
Thread(s) per core: 2
Core(s) per socket: 20
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6242R CPU @ 3.10GHz
Stepping: 7
CPU MHz: 3100.000
CPU max MHz: 4100.0000
CPU min MHz: 1200.0000
BogoMIPS: 6200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0-2, 5-7, 11-13, 16-40, 42-45, 47-51, 53-56
NUMA node1 CPU(s): 3, 4, 8-10, 14, 15, 17-19, 43, 44, 48-50, 54, 55, 57-59
NUMA node2 CPU(s): 20-23, 26, 30-32, 35, 36, 60-63, 66, 70-72, 75, 76
NUMA node3 CPU(s): 24, 25, 27-29, 33, 34, 37-39, 64, 65, 67-69, 73, 74, 77-79
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xapic nd sse2 tsc参与到多核 cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_apic vmx smx est tm2 ssse3 sdbg fma cx16
xtrr pdcm pcid dca sse4_1 dca4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_pni ssbd mbb ibrs ibpb stibp ibrs_enhanced tpr_shadow vmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_l1d

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR630
(3.10 GHz, Intel Xeon Gold 6242R)

SPEC CPU®2017 Floating Point Rate Result
Copyright 2017-2020 Standard Performance Evaluation Corporation

SPECrate®2017_fp_base = 259
SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: May-2020
Hardware Availability: Mar-2020
Tested by: Lenovo Global Technology
Software Availability: Apr-2020

Platform Notes (Continued)

```
arch_capabilities

/proc/cpuinfo cache data
  cache size : 36608 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 4 nodes (0-3)
  node 0 cpus: 0 1 2 5 6 7 11 12 13 16 40 41 42 45 46 47 51 52 53 56
  node 0 size: 193150 MB
  node 0 free: 192707 MB
  node 1 cpus: 3 4 8 9 10 14 15 17 18 19 43 44 48 49 50 54 55 57 58 59
  node 1 size: 193502 MB
  node 1 free: 193211 MB
  node 2 cpus: 20 21 22 23 26 30 31 32 35 36 60 61 62 63 66 70 71 72 75 76
  node 2 size: 193532 MB
  node 2 free: 193290 MB
  node 3 cpus: 24 25 27 28 29 33 34 37 38 39 64 65 67 68 69 73 74 77 78 79
  node 3 size: 193530 MB
  node 3 free: 193281 MB
  node distances:
    node   0   1   2   3
    0:  10  11  21  21
    1:  11  10  21  21
    2:  21  21  10  11
    3:  21  21  11  10

From /proc/meminfo
  MemTotal:       792285448 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
  SUSE Linux Enterprise Server 15 SP1

From /etc/*release* /etc/*version*
  os-release:
    NAME="SLES"
    VERSION="15-SP1"
    VERSION_ID="15.1"
    PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
    ID="sles"
    ID_LIKE="suse"
    ANSI_COLOR="0;32"
    CPE_NAME=cpe:/o:suse:sles:15:sp1"

  uname -a:
```

(Continued on next page)
### Lenovo Global Technology

ThinkSystem SR630  
(3.10 GHz, Intel Xeon Gold 6242R)

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>Lenovo Global Technology</th>
<th>Test Date:</th>
<th>May-2020</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Lenovo Global Technology</td>
<td>Hardware Availability:</td>
<td>Mar-2020</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Lenovo Global Technology</td>
<td>Software Availability:</td>
<td>Apr-2020</td>
</tr>
</tbody>
</table>

**Platform Notes (Continued)**

Linux linux-thtl 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)  
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

- **CVE-2018-3620 (L1 Terminal Fault):** Not affected
- **Microarchitectural Data Sampling:** Not affected
- **CVE-2017-5754 (Meltdown):** Not affected
- **CVE-2018-3639 (Speculative Store Bypass):** Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- **CVE-2017-5753 (Spectre variant 1):** Mitigation: __user pointer sanitization
- **CVE-2017-5715 (Spectre variant 2):** Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 May 28 10:13

SPEC is set to: /home/cpu2017-1.1.0-ic19.1.1

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sdb2</td>
<td>xfs</td>
<td>744G</td>
<td>49G</td>
<td>695G</td>
<td>7%</td>
<td>/</td>
</tr>
</tbody>
</table>

From /sys/devices/virtual/dmi/id

- **BIOS:** Lenovo -[IVE155L-2.61]- 05/20/2020
- **Vendor:** Lenovo
- **Product:** ThinkSystem SR630 -[7X01RCZ000]-
- **Product Family:** ThinkSystem
- **Serial:** 1234567890

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

**Memory:**

- 24x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)

### Compiler Version Notes

```
C | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
```

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1

NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

(Continued on next page)
# Lenovo Global Technology

**ThinkSystem SR630**  
(3.10 GHz, Intel Xeon Gold 6242R)

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>9017</th>
<th>Test Date:</th>
<th>May-2020</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tested Sponsor:</td>
<td>Lenovo Global Technology</td>
<td>Hardware Availability:</td>
<td>Mar-2020</td>
</tr>
<tr>
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<td>Lenovo Global Technology</td>
<td>Software Availability:</td>
<td>Apr-2020</td>
</tr>
</tbody>
</table>

| SPECrate®2017_fp_base = | 259                        |            |          |
| SPECrate®2017_fp_peak = | Not Run                    |            |          |

---

**Compiler Version Notes (Continued)**

```
C++          | 508.namd_r(base) 510.parest_r(base)
```

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

```
C++, C      | 511.povray_r(base) 526.blender_r(base)
```

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

```
C++, C, Fortran | 507.cactuBSSN_r(base)
```

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

```
Fortran      | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
```

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

```
Fortran, C  | 521.wrf_r(base) 527.cam4_r(base)
```

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR630
(3.10 GHz, Intel Xeon Gold 6242R)

SPECrate®2017_fp_base = 259
SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Compiler Version Notes (Continued)

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
Base Optimization Flags

C benchmarks:
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-fuse-ld=gold -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-auto -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo -no-prec-div
-qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles -nostandard-realloc-lhs
-align array32byte -auto -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both C and C++:
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo -no-prec-div
-qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles -nostandard-realloc-lhs
-align array32byte -auto -mbranches-within-32B-boundaries

(Continued on next page)
# SPEC CPU®2017 Floating Point Rate Result

**Lenovo Global Technology**

ThinkSystem SR630  
(3.10 GHz, Intel Xeon Gold 6242R)  

**SPECrate®2017_fp_base = 259**  
**SPECrate®2017_fp_peak = Not Run**

<table>
<thead>
<tr>
<th>CPU2017 License: 9017</th>
<th>Test Date: May-2020</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Lenovo Global Technology</td>
<td>Hardware Availability: Mar-2020</td>
</tr>
<tr>
<td>Tested by: Lenovo Global Technology</td>
<td>Software Availability: Apr-2020</td>
</tr>
</tbody>
</table>

## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):  
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-H.xml](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-H.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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