### Hardware

- **CPU Name:** Intel Xeon Gold 6142
- **Max MHz:** 3700
- **Nominal:** 2600
- **Enabled:** 32 cores, 2 chips
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 22 MB I+D on chip per chip
- **Other:** None
- **Memory:** 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2666)
- **Storage:** 2 x 480 GB SATA SSD
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 12 SP4 4.12.14-94.41-default
- **Compiler:** C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux;
  Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux
- **Parallel:** Yes
- **Firmware:** Version 2.00.41 released Jun-2020 BIOS
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** None
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage

### SPEC CPU 2017 Results

**SPECspeed®2017_fp_base = 119**

**SPECspeed®2017_fp_peak = 120**

<table>
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<tr>
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<tr>
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New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 6142)

SPECspeed®2017_fp_base = 119  
SPECspeed®2017_fp_peak = 120

Results Table

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<td>114</td>
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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:

- KMP_AFFINITY = "granularity=fine,compact"
- LD_LIBRARY_PATH = "/home/spec/cpu/lib/intel64"
- OMP_STACKSIZE = "192M"

**General Notes**

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3 > /proc/sys/vm/drop_caches
```
**Platform Notes**

BIOS settings:
Set Hyper-Threading to Disabled
Set SNC to Disabled
Set IMC Interleaving to 2-way Interleave
Set Patrol Scrub to Disabled

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7ed1e6e46a485a001
running on linux-rsx1 Mon Jun 22 21:01:50 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 6142 CPU @ 2.60GHz
  2 "physical id"s (chips)
  32 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 16
  siblings : 16
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian
  CPU(s): 32
  On-line CPU(s) list: 0-31
  Thread(s) per core: 1
  Core(s) per socket: 16
  Socket(s): 2
  NUMA node(s): 2
  Vendor ID: GenuineIntel
  CPU family: 6
  Model: 85
  Model name: Intel(R) Xeon(R) Gold 6142 CPU @ 2.60GHz
  Stepping: 4
  CPU MHz: 2600.000
  CPU max MHz: 3700.0000
  CPU min MHz: 1000.0000
  BogoMIPS: 5200.00
  Virtualization: VT-x
  L1d cache: 32K
  L1i cache: 32K

(Continued on next page)
### SPEC CPU®2017 Floating Point Speed Result

**New H3C Technologies Co., Ltd.**

**H3C UniServer R4900 G3 (Intel Xeon Gold 6142)**

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base</th>
<th>119</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_fp_peak</td>
<td>120</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.  
**Test Date:** Jun-2020  
**Hardware Availability:** Mar-2019  
**Software Availability:** May-2019

### Platform Notes (Continued)

| L2 cache: | 1024K |
| L3 cache: | 22528K |
| NUMA node0 CPU(s): | 0-15 |
| NUMA node1 CPU(s): | 16-31 |

---

From `numactl --hardware`

<table>
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<tr>
<th>node</th>
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<th>size</th>
<th>free</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0-15</td>
<td>95248 MB</td>
<td>92006 MB</td>
</tr>
<tr>
<td>1</td>
<td>16-31</td>
<td>96526 MB</td>
<td>91463 MB</td>
</tr>
</tbody>
</table>

From `/proc/meminfo`

| MemTotal: | 196377644 kB |
| HugePages_Total: | 0 |
| Hugepagesize: | 2048 kB |

/usr/bin/lsb_release -d

SUSE Linux Enterprise Server 12 SP4

From `/etc/*release*`  
**SuSE-release:**

| SUSE Linux Enterprise Server 12 (x86_64)  
| VERSION = 12  
| PATCHLEVEL = 4 |

# This file is deprecated and will be removed in a future service pack or release.

(Continued on next page)
### New H3C Technologies Co., Ltd.

**H3C UniServer R4900 G3 (Intel Xeon Gold 6142)**

<table>
<thead>
<tr>
<th><strong>SPEC</strong></th>
<th><strong>Value</strong></th>
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<tr>
<td>SPECspeed®2017_fp_base</td>
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**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Test Date:** Jun-2020  
**Hardware Availability:** Mar-2019  
**Tested by:** New H3C Technologies Co., Ltd.  
**Software Availability:** May-2019

---

### Platform Notes (Continued)

```bash
# Please check /etc/os-release for details about this release.

os-release:
  NAME="SLES"
  VERSION="12-SP4"
  VERSION_ID="12.4"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP4"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp4"

uname -a:
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

- **CVE-2018-3620 (L1 Terminal Fault):** Mitigation: PTE Inversion; VMX: conditional cache flushes, SMT disabled
- **Microarchitectural Data Sampling:** No status reported
- **CVE-2017-5754 (Meltdown):** Mitigation: PTI
- **CVE-2018-3639 (Speculative Store Bypass):** Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- **CVE-2017-5753 (Spectre variant 1):** Mitigation: __user pointer sanitization
- **CVE-2017-5715 (Spectre variant 2):** Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 Jun 22 16:01

SPEC is set to: /home/speccpu

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<tr>
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<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/md126p4</td>
<td>xfs</td>
<td>383G</td>
<td>13G</td>
<td>370G</td>
<td>4%</td>
<td>/home</td>
</tr>
</tbody>
</table>

From /sys/devices/virtual/dmi/id

| BIOS: American Megatrends Inc. 2.00.41 06/09/2020 |
| Vendor: New H3C Technologies Co., Ltd. |
| Product: UniServer R4900 G3 |
| Product Family: Rack |
| Serial: 210200A00QH18C001552 |

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

**Memory:**

- 12x Hynix HMA82GR7AFR8N-VK 16 GB 2 rank 2666
- 12x NO DIMM NO DIMM

(Continued on next page)
### Platform Notes (Continued)

(End of data from sysinfo program)

### Compiler Version Notes

<table>
<thead>
<tr>
<th>Platform</th>
<th>Compiler Version Notes</th>
</tr>
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<tbody>
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<td>C</td>
<td>Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416</td>
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<td>Copyright (C) 1985-2019 Intel Corporation. All rights reserved.</td>
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<td>C++, C, Fortran</td>
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<tr>
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H3C UniServer R4900 G3 (Intel Xeon Gold 6142) | SPECspeed®2017_fp_peak = 120

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.
Test Date: Jun-2020
Hardware Availability: Mar-2019
Software Availability: May-2019

Compiler Version Notes (Continued)

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-nostandard-realloc-lhs

(Continued on next page)
New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 6142)  

SPECspeed\textsuperscript{2017\_fp\_base} = 119  
SPECspeed\textsuperscript{2017\_fp\_peak} = 120

**CPU2017 License**: 9066  
**Test Date**: Jun-2020  
**Test Sponsor**: New H3C Technologies Co., Ltd.  
**Tested by**: New H3C Technologies Co., Ltd.  
**Hardware Availability**: Mar-2019  
**Software Availability**: May-2019

### Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC\_OPENMP  
-nostandard-realloc-lhs

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC\_OPENMP  
-nostandard-realloc-lhs

### Peak Compiler Invocation

C benchmarks:  
icc -m64 -std=c11

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:  
icpc -m64 icc -m64 -std=c11 ifort -m64

### Peak Portability Flags

Same as Base Portability Flags

### Peak Optimization Flags

C benchmarks:  
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC\_OPENMP

Fortran benchmarks:  
603.bwaves_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC\_SUPPRESS\_OPENMP  
-DSPEC\_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3  
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=4  
-qopenmp -nostandard-realloc-lhs

(Continued on next page)
New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 6142)

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<th>Test Date:</th>
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<td>New H3C Technologies Co., Ltd.</td>
<td>Hardware Availability:</td>
<td>Mar-2019</td>
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<td>Tested by:</td>
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<td>Software Availability:</td>
<td>May-2019</td>
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**Peak Optimization Flags (Continued)**

649.fotonik3d_s: Same as 603.bwaves_s

654.roms_s: `-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs`

Benchmarks using both Fortran and C:

621.wrf_s: `-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs`

627.cam4_s: `-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs`

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:

`-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs`

The flags files that were used to format this result can be browsed at


http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.3-SKL-RevE.html

You can also download the XML flags sources by saving the following links:


http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.3-SKL-RevE.xml

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