### SPEC CPU®2017 Integer Rate Result

**Inspur Corporation**

**Inspur NF5280M5 (Intel Xeon Silver 4216)**

**SPECrate®2017_int_base = 188**

**SPECrate®2017_int_peak = Not Run**

<table>
<thead>
<tr>
<th>Copies</th>
<th>500.perlbench_r</th>
<th>502.gcc_r</th>
<th>505.mcf_r</th>
<th>520.omnetpp_r</th>
<th>523.xalancbmk_r</th>
<th>525.x264_r</th>
<th>531.deepsjeng_r</th>
<th>541.leela_r</th>
<th>548.exchange2_r</th>
<th>557.xz_r</th>
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</tbody>
</table>

#### Hardware

**CPU Name:** Intel Xeon Silver 4216  
**Max MHz:** 3200  
**Nominal:** 2100  
**Enabled:** 32 cores, 2 chips, 2 threads/core  
**Orderable:** 1.2 chips  
**Cache L1:** 32 KB I+ 32 KB D on chip per core  
**L2:** 1 MB I+D on chip per core  
**L3:** 22 MB I+D on chip per chip  
**Other:** None  
**Memory:** 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R, running at 2400)  
**Storage:** 1 x 2 TB NVME SSD  
**Other:** None  

#### Software

**OS:** Red Hat Enterprise Linux release 8.0 (Ootpa)  
**Compiler:** C/C++: Version 19.1.1.217 of Intel C/C++ Compiler Build 20200306 for Linux; Fortran: Version 19.1.1.217 of Intel Fortran Compiler Build 20200306 for Linux  
**Parallel:** No  
**Firmware:** Version 4.1.7 released Apr-2019  
**File System:** xfs  
**System State:** Run level 5 (multi-user)  
**Base Pointers:** 64-bit  
**Peak Pointers:** Not Applicable  
**Other:** None  

**Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
Inspur Corporation

Inspur NF5280M5 (Intel Xeon Silver 4216)

SPECrater®2017_int_base = 188
SPECrater®2017_int_peak = Not Run

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Test Date: Jul-2020
Hardware Availability: Apr-2019
Software Availability: Apr-2020

Results Table

Benchmark | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio
---|---|---|---|---|---|---|---|---|---|---|---|---|---|---
500.perlbench_r | 64 | 795 | 128 | 801 | 127 | 794 | 128 | | | | | | | |
502.gcc_r | 64 | 624 | 145 | 621 | 146 | 622 | 146 | | | | | | | |
505.mcf_r | 64 | 323 | 320 | 318 | 325 | 320 | 324 | | | | | | | |
520.omnetpp_r | 64 | 683 | 123 | 688 | 122 | 684 | 123 | | | | | | | |
523.xalancbmk_r | 64 | 269 | 251 | 270 | 251 | 269 | 251 | | | | | | | |
525.x264_r | 64 | 298 | 376 | 294 | 381 | 297 | 378 | | | | | | | |
531.deepsjeng_r | 64 | 496 | 148 | 495 | 148 | 495 | 148 | | | | | | | |
541.leela_r | 64 | 778 | 136 | 778 | 136 | 773 | 137 | | | | | | | |
548.exchange2_r | 64 | 476 | 352 | 476 | 352 | 476 | 352 | | | | | | | |
557.xz_r | 64 | 630 | 110 | 632 | 109 | 630 | 110 | | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler. The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux. The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
SCALING_GOVERNOR set to Performance

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = 
"/home/CPU2017/lib/intel64:/home/CPU2017/lib/ia32:/home/CPU2017/je5.0.1-32"
MALLOC_CONF = "retain:true"
## SPEC CPU®2017 Integer Rate Result

### Inspur Corporation

**Inspur NF5280M5 (Intel Xeon Silver 4216)**

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
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<td>Inspur Corporation</td>
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<td>Tested by:</td>
<td>Inspur Corporation</td>
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<tbody>
<tr>
<td>SPECrater®2017_int_peak =</td>
<td>Not Run</td>
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</table>

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
    sync; echo 3>/proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
    numactl --interleave=all runcpu <etc>
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

### Platform Notes

BIOS configuration:

ENERGY_PERF_BIAS_CFG mode set to Performance

Hardware Prefetch set to Disable

VT Support set to Disable

C1E Support set to Disable

IMC (Integrated memory controller) Interleaving set to 1-way

Sub NUMA Cluster (SNC) set to Enable

Sysinfo program /home/CPU2017/bin/sysinfo

Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011

running on localhost.localdomain Fri Jun 22 21:45:56 2018

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo:

```
    model name : Intel(R) Xeon(R) Silver 4216 CPU @ 2.10GHz
    2 "physical id"$s (chips)
    64 "processors"

    cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    cpu cores : 16
    siblings : 32

    physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
    physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
```

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Insapur Corporation

**SPEC CPU® 2017 Integer Rate Result**

**Inspur Corporation**

**Inspur NF5280M5 (Intel Xeon Silver 4216)**

**CPU2017 License:** 3358  
**Test Sponsor:** Inspur Corporation  
**Tested by:** Inspur Corporation

**Test Date:** Jul-2020  
**Hardware Availability:** Apr-2019  
**Software Availability:** Apr-2020

**SPECRate® 2017_int_base =** 188

**SPECRate® 2017_int_peak =** Not Run

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**Platform Notes (Continued)**

From `lscpu`:
- **Architecture:** x86_64
- **CPU op-mode(s):** 32-bit, 64-bit
- **Byte Order:** Little Endian
- **CPU(s):** 64
- **On-line CPU(s) list:** 0-63
- **Thread(s) per core:** 2
- **Core(s) per socket:** 16
- **Socket(s):** 2
- **NUMA node(s):** 2
- **Vendor ID:** GenuineIntel
- **CPU family:** 6
- **Model:** 85
- **Model name:** Intel(R) Xeon(R) Silver 4216 CPU @ 2.10GHz
- **Stepping:** 7
- **CPU MHz:** 2700.021
- **CPU max MHz:** 3200.0000
- **CPU min MHz:** 800.0000
- **BogoMIPS:** 4200.00
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 1024K
- **L3 cache:** 22528K
- **NUMA node0 CPU(s):** 0-15,32-47
- **NUMA node1 CPU(s):** 16-31,48-63
- **Flags:** fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bni hle avx2 smep bmi2 erms invpcid rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsavesopt xsaveopt xsave xgetbv1 xsavec cmqm_llc cmqm_occuc_llc cmqm_mbm_total cmqm_mbm_local dtherm ida arat pln pts kpu ospke avx512_vnni flush_l1d arch_capabilities

```bash
From /proc/cpuinfo cache data
cache size : 22528 KB
```

From `numactl --hardware` WARNING: a numactl 'node' might or might not correspond to a physical chip.

```bash
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47
node 0 size: 385355 MB
```

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Inspar Corporation

Inspur NF5280M5 (Intel Xeon Silver 4216)

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

SPECrate®2017_int_base = 188
SPECrate®2017_int_peak = Not Run

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Test Date: Jul-2020
Hardware Availability: Apr-2019
Software Availability: Apr-2020

Platform Notes (Continued)

node 0 free: 374075 MB
node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 48 49 50 51 52 53 54 55 56
node 1 cpus: 57 58 59 60 61 62 63
node 1 size: 387065 MB
node 1 free: 377449 MB
node distances:
node 0 1
0: 10 21
1: 21 10

From /proc/meminfo
MemTotal: 790958672 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.0 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.0"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.0 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.0 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.0 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.0:ga

uname -a:
Linux localhost.localdomain 4.18.0-80.el8.x86_64 #1 SMP Wed Mar 13 12:02:46 UTC 2019
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: No status reported
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 5 Jun 22 07:11
SPEC is set to: /home/CPU2017

(Continued on next page)
**SPEC CPU®2017 Integer Rate Result**

**Inspur Corporation**

**Inspur NF5280M5 (Intel Xeon Silver 4216)**

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**CPU2017 License:** 3358  
**Test Sponsor:** Inspur Corporation  
**Tested by:** Inspur Corporation  
**Test Date:** Jul-2020  
**Hardware Availability:** Apr-2019  
**Software Availability:** Apr-2020

### Platform Notes (Continued)

Filesystem            Type  Size  Used  Avail  Use%  Mounted on  
/dev/mapper/rhel-home  xfs   1.8T   39G  1.8T  3%   /home

From /sys/devices/virtual/dmi/id  
BIOS: American Megatrends Inc. 4.1.7 04/19/2019  
Vendor: Inspur  
Product: NF5280M5  
Serial: 217453240

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
24x Hynix HMAA4GR7AJR8N-WM 32 GB 2 rank 2933

This system support 12 DIMMs per processor, total 24 DIMMs. 24 DIMM slots installed with 32 GB DIMM for this run, and running at 2400 due to CPU limitation.

### Compiler Version Notes

C

| 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base)  
| 525.x264_r(base) 557.xz_r(base) |

---

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

C++

| 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)  
| 541.leela_r(base) |

---

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

Fortran  
| 548.exchange2_r(base) |

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Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306

(Continued on next page)
## SPEC CPU®2017 Integer Rate Result

**Inspur Corporation**

Inspur NF5280M5 (Intel Xeon Silver 4216)

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base</th>
<th>188</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_int_peak</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 3358  
**Test Sponsor:** Inspur Corporation  
**Tested by:** Inspur Corporation

**Test Date:** Jul-2020  
**Hardware Availability:** Apr-2019  
**Software Availability:** Apr-2020

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### Compiler Version Notes (Continued)

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### Base Compiler Invocation

**C benchmarks:**  
- icc

**C++ benchmarks:**  
- icpc

**Fortran benchmarks:**  
- ifort

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### Base Portability Flags

- 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
- 502.gcc_r: -DSPEC_LP64  
- 505.mcf_r: -DSPEC_LP64  
- 520.omnetpp_r: -DSPEC_LP64  
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
- 525.x264_r: -DSPEC_LP64  
- 531.deepsjeng_r: -DSPEC_LP64  
- 541.leela_r: -DSPEC_LP64  
- 548.exchange2_r: -DSPEC_LP64  
- 557.xz_r: -DSPEC_LP64

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### Base Optimization Flags

**C benchmarks:**  
- -m64 -qnextgen -std=c11  
- -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs  
- -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops  
- -fuse=ld.gold -qopt-mem-layout-trans=4  
- -L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin  
- -lqkmalloc

**C++ benchmarks:**  
- -m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries  
- -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse  
- -funroll-loops -fuse=ld.gold -qopt-mem-layout-trans=4

(Continued on next page)
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SPECrater®2017_int_base = 188
SPECrater®2017_int_peak = Not Run

CPU2017 License: 3358
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Base Optimization Flags (Continued)

C++ benchmarks (continued):
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V1.9.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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