## Lenovo Global Technology

**ThinkSystem SR630**

(2.10 GHz, Intel Xeon Gold 5218R)

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>80</td>
<td>Not Run</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>80</td>
<td>189</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>80</td>
<td>165</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>80</td>
<td>165</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>80</td>
<td>316</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>80</td>
<td>488</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>80</td>
<td>184</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>80</td>
<td>180</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>80</td>
<td>453</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>80</td>
<td>146</td>
</tr>
</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon Gold 5218R
- **Max MHz:** 4000
- **Nominal:** 2100
- **Enabled:** 40 cores, 2 chips, 2 threads/core
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 27.5 MB I+D on chip per chip
- **Memory:** 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R, running at 2666)
- **Storage:** 1 x 800 GB SATA SSD
- **Other:** None

**Software**

- **OS:** SUSE Linux Enterprise Server 15 SP1 (x86_64) Kernel 4.12.14-195-default
- **Compiler:** C/C++: Version 19.1.1.217 of Intel C/C++ Compiler for Linux; Fortran: Version 19.1.1.217 of Intel Fortran Compiler for Linux
- **Parallel:** No
- **Firmware:** Lenovo BIOS Version IVE155L 2.61 released May-2020
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** Not Applicable
- **Other:** None

**Power Management:** BIOS set to prefer performance at the cost of additional power usage
Lenovo Global Technology

ThinkSystem SR630
(2.10 GHz, Intel Xeon Gold 5218R)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

SPECratenos

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>80</td>
<td>793</td>
<td>161</td>
<td>795</td>
<td>160</td>
<td>793</td>
<td>161</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>80</td>
<td>615</td>
<td>184</td>
<td>599</td>
<td>189</td>
<td>597</td>
<td>190</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>80</td>
<td>321</td>
<td>402</td>
<td>322</td>
<td>402</td>
<td>321</td>
<td>403</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>80</td>
<td>634</td>
<td>166</td>
<td>636</td>
<td>165</td>
<td>636</td>
<td>165</td>
</tr>
<tr>
<td>523.xalanbmkr</td>
<td>80</td>
<td>267</td>
<td>316</td>
<td>267</td>
<td>316</td>
<td>267</td>
<td>316</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>80</td>
<td>288</td>
<td>487</td>
<td>287</td>
<td>488</td>
<td>286</td>
<td>490</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>80</td>
<td>499</td>
<td>184</td>
<td>498</td>
<td>184</td>
<td>498</td>
<td>184</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>80</td>
<td>735</td>
<td>180</td>
<td>735</td>
<td>180</td>
<td>735</td>
<td>180</td>
</tr>
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<td>80</td>
<td>462</td>
<td>453</td>
<td>462</td>
<td>454</td>
<td>463</td>
<td>453</td>
</tr>
<tr>
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<td>80</td>
<td>593</td>
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<td>146</td>
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</table>

SPECratenos

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler.
The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux
The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = 
"/home/cpu2017-1.1.0-ic19.1.1/lib/intel64:/home/cpu2017-1.1.0-ic19.1.1/lib/ia32:/home/cpu2017-1.1.0-ic19.1.1/je5.0.1-32"
MALLOC_CONF = "retain:true"
```
Lenovo Global Technology
ThinkSystem SR630
(2.10 GHz, Intel Xeon Gold 5218R)

SPECrate®2017_int_base = 242
SPECrate®2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jun-2020
Hardware Availability: Mar-2020
Software Availability: Apr-2020

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
    sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
    numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode
MONITOR/MWAIT set to Enable
SNC set to Enable
DCU Streamer Prefetcher set to Disable
LLC dead line alloc set to Disable

Sysinfo program /home/cpu2017-1.1.0-ic19.1.1/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7ed8b1e6e46a485a0011
running on linux-thtl Tue Jun 23 16:33:56 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 5218R CPU @ 2.10GHz
    2 "physical id"s (chips)
    80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    cpu cores : 20
    siblings : 40
    physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
    physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

From lscpu:
    Architecture: x86_64
    CPU op-mode(s): 32-bit, 64-bit

(Continued on next page)
**Platform Notes (Continued)**

Byte Order: Little Endian
Address sizes: 46 bits physical, 48 bits virtual
CPU(s): 80
On-line CPU(s) list: 0-79
Thread(s) per core: 2
Core(s) per socket: 20
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5218R CPU @ 2.10GHz
Stepping: 7
CPU MHz: 2100.000
CPU max MHz: 4000.0000
CPU min MHz: 800.0000
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 28160K
NUMA node0 CPU(s): 0-2, 5, 6, 10-12, 15, 16, 40-41, 45, 46, 50-52, 55, 56
NUMA node1 CPU(s): 3, 4, 7-9, 13, 14, 17-19, 20-22, 25, 26, 30-32, 35, 36, 60-62, 65, 66, 70-72, 75, 76
NUMA node2 CPU(s): 23, 24, 27-29, 33, 34, 37-39, 63, 64, 67-69, 73, 74, 77-79
NUMA node3 CPU(s): 0-2, 5, 6, 10-12, 15, 16, 40-41, 45, 46, 50-52, 55, 56

```
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pse syscall nx pdflush dts
 Cá nhaio i tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpubi
aperf_mprof pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xptr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abmah 3nowprefetch cpuid fault epb cat_x3 cdp_x3
invpcid_single intel_pinn ssbd mba ibp ibp att ibrs enhanced tpr_shapen vmni flexpriority
xpt vpid fsqbase tsc_adjust bni hle avx2 smep bmi2 erms invpcid rtm
cmq mpx rdt_a avx512f avx512dq rdsd adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsaves xsavec xgetbv1 xsaves cqm_llc cqm_occuillc cqm_mbb_total
cmq_mbb_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_l1d
arch_capabilities
/process/cinfo cache data
cache size: 28160 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 5 6 10 11 12 15 16 40 41 42 45 46 50 51 52 55 56
node 0 size: 193150 MB

(Continued on next page)
**Platform Notes (Continued)**

node 0 free: 192502 MB
node 1 cpus: 3 4 7 8 9 13 14 17 18 19 43 44 47 48 49 53 54 57 58 59
node 1 size: 193502 MB
node 1 free: 192951 MB
node 2 cpus: 20 21 22 25 26 30 31 32 35 36 60 61 62 65 66 70 71 72 75 76
node 2 size: 193532 MB
node 2 free: 193071 MB
node 3 cpus: 23 24 27 28 29 33 34 37 38 39 63 64 67 68 69 73 74 77 78 79
node 3 size: 193530 MB
node 3 free: 193074 MB
node distances:

<table>
<thead>
<tr>
<th>node</th>
<th>0</th>
<th>1</th>
<th>2</th>
<th>3</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>10 11 21 21</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>11 10 21 21</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>21 21 10 11</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>21 21 11 10</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

From /proc/meminfo
- MemTotal: 792285448 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /usr/bin/lsb_release -d
- SUSE Linux Enterprise Server 15 SP1

From /etc/*release* /etc/*version*
- os-release:
  - NAME="SLES"
  - VERSION="15-SP1"
  - VERSION_ID="15.1"
  - PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
  - ID="sles"
  - ID_LIKE="suse"
  - ANSI_COLOR="0;32"
  - CPE_NAME="cpe:/o:suse:sles:15:sp1"

uname -a:
- x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
- CVE-2018-3620 (L1 Terminal Fault): Not affected
- Microarchitectural Data Sampling: Not affected
- CVE-2017-5754 (Meltdown): Not affected
- CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp

(Continued on next page)
**SPEC CPU®2017 Integer Rate Result**

**Lenovo Global Technology**

**ThinkSystem SR630**

**(2.10 GHz, Intel Xeon Gold 5218R)**

<table>
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</tr>
</tbody>
</table>

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Jun-2020  
**Hardware Availability:** Mar-2020  
**Software Availability:** Apr-2020

### Platform Notes (Continued)

- **CVE-2017-5753 (Spectre variant 1):** Mitigation: __user pointer sanitization
- **CVE-2017-5715 (Spectre variant 2):** Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

- **run-level 3 Jun 23 05:55**

- **SPEC is set to:** /home/cpu2017-1.1.0-ic19.1.1
- **Filesystem**  
<table>
<thead>
<tr>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>xfs</td>
<td>744G</td>
<td>47G</td>
<td>698G</td>
<td>7%</td>
<td>/</td>
</tr>
</tbody>
</table>

- **From /sys/devices/virtual/dmi/id**
  - **BIOS:** Lenovo -[IVE155L-2.61]- 05/20/2020
  - **Vendor:** Lenovo
  - **Product:** ThinkSystem SR630 -[7X01RCZ000]-
  - **Product Family:** ThinkSystem
  - **Serial:** 1234567890

- **Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.**
  - **Memory:**
    - 24x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

- **(End of data from sysinfo program)**

This system support 12 DIMMs per processor, total 24 DIMMs.  
24 DIMM slots installed with 32 GB DIMM for this run, and running at 2666 due to CPU limitation.

### Compiler Version Notes

```
C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base)  
| 525.x264_r(base) 557.xz_r(base)  

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++     | 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)  
| 541.leela_r(base)  

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
```

(Continued on next page)
**Lenovo Global Technology**  
ThinkSystem SR630  
(2.10 GHz, Intel Xeon Gold 5218R)  

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<thead>
<tr>
<th>CPU2017 License:</th>
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</tr>
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<td>Test Sponsor:</td>
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</tr>
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**SPEC CPU®2017 Integer Rate Result**  

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**Compiler Version Notes (Continued)**

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

-------------------------------------------------------------------

Fortran  | 548.exchange2_r(base)
-------------------------------------------------------------------

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

**Base Compiler Invocation**

C benchmarks:
icc
C++ benchmarks:
icpc
Fortran benchmarks:
ifort

**Base Portability Flags**

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc_r: -DSPEC_LP64  
505.mcf_r: -DSPEC_LP64  
520.omnetpp_r: -DSPEC_LP64  
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
525.x264_r: -DSPEC_LP64  
531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64  
548.exchange2_r: -DSPEC_LP64  
557.xz_r: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:
-m64 -qnextgen -std=c11  
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs  
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops

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(Continued on next page)
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**Base Optimization Flags (Continued)**

C benchmarks (continued):
- `fuse-ld=gold`  
- `-qopt-mem-layout-trans=4`  
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin`  
- `lqkmalloc`

C++ benchmarks:
- `-m64`  
- `-qnextgen`  
- `-Wl,-plugin-opt=-x86-branches-within-32B-boundaries`  
- `-Wl,-z,muldefs`  
- `xCORE-AVX512`  
- `-O3`  
- `-ffast-math`  
- `flto`  
- `mfpmath=sse`  
- `-funroll-loops`  
- `fuse-ld=gold`  
- `-qopt-mem-layout-trans=4`  
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin`  
- `lqkmalloc`

Fortran benchmarks:
- `-m64`  
- `-Wl,-plugin-opt=-x86-branches-within-32B-boundaries`  
- `-Wl,-z,muldefs`  
- `xCORE-AVX512`  
- `-O3`  
- `-ipo`  
- `-no-prec-div`  
- `-qopt-mem-layout-trans=4`  
- `-nostandard-realloc-lhs`  
- `align array32byte`  
- `-auto`  
- `-mbranches-within-32B-boundaries`  
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin`  
- `lqkmalloc`

The flags files that were used to format this result can be browsed at:
- [http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-I.html](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-I.html)

You can also download the XML flags sources by saving the following links:
- [http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-I.xml](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-I.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-06-23 04:33:55-0400.
Originally published on 2020-07-21.

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**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Jun-2020  
**Hardware Availability:** Mar-2020  
**Software Availability:** Apr-2020