## NEC Corporation

**Express5800/R120h-1M (Intel Xeon Gold 5218R)**

<table>
<thead>
<tr>
<th>Spec CPU®2017 Floating Point Rate Result</th>
<th>SPECrate®2017_fp_base = 197</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Test Date:</strong></td>
<td>Jul-2020</td>
</tr>
<tr>
<td><strong>Hardware Availability:</strong></td>
<td>May-2020</td>
</tr>
<tr>
<td><strong>Software Availability:</strong></td>
<td>Sep-2019</td>
</tr>
<tr>
<td><strong>CPU2017 License:</strong></td>
<td>9006</td>
</tr>
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<td>NEC Corporation</td>
</tr>
<tr>
<td><strong>Tested by:</strong></td>
<td>NEC Corporation</td>
</tr>
<tr>
<td><strong>SPECrate®2017_fp_peak:</strong></td>
<td>211</td>
</tr>
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<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate®2017_fp_base (197)</th>
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</tr>
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<tr>
<td>503.bwaves_r</td>
<td>80</td>
<td>172</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>80</td>
<td>151</td>
</tr>
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<td>508.namd_r</td>
<td>80</td>
<td>107</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>80</td>
<td>226</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>80</td>
<td>111</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>80</td>
<td>199</td>
</tr>
<tr>
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<td>80</td>
<td>208</td>
</tr>
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<td>80</td>
<td>208</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>80</td>
<td>254</td>
</tr>
<tr>
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<td>80</td>
<td>254</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>80</td>
<td>358</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>80</td>
<td>154</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>80</td>
<td>842</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 5218R
- **Max MHz:** 4000
- **Nominal:** 2100
- **Enabled:** 40 cores, 2 chips, 2 threads/core
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 27.5 MB I+D on chip per chip
- **Other:** None
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2933Y-R, running at 2666)
- **Storage:** 1 x 1 TB SATA, 7200 RPM, RAID 0
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux Server release 7.7 (Maipo)
- **Compiler:** C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux;
  Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux
- **Parallel:** No
- **Firmware:** NEC BIOS Version U32 v2.32 03/09/2020 released Jun-2020
- **File System:** ext4
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** None
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage.
SPEC CPU®2017 Floating Point Rate Result

NEC Corporation

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CPU2017 License: 9006
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SPECrate®2017_fp_base = 197
SPECrate®2017_fp_peak = 211

Results Table

<table>
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<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>80</td>
<td>1739</td>
<td>461</td>
<td>1738</td>
<td>462</td>
<td>1738</td>
<td>462</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>80</td>
<td>587</td>
<td>172</td>
<td>587</td>
<td>172</td>
<td>588</td>
<td>172</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>80</td>
<td>504</td>
<td>151</td>
<td>501</td>
<td>152</td>
<td>502</td>
<td>151</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>80</td>
<td>1936</td>
<td>108</td>
<td>1967</td>
<td>106</td>
<td>1951</td>
<td>107</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>80</td>
<td>826</td>
<td>226</td>
<td>825</td>
<td>226</td>
<td>825</td>
<td>226</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>80</td>
<td>762</td>
<td>111</td>
<td>763</td>
<td>110</td>
<td>762</td>
<td>111</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>80</td>
<td>899</td>
<td>199</td>
<td>893</td>
<td>201</td>
<td>905</td>
<td>198</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>80</td>
<td>584</td>
<td>208</td>
<td>586</td>
<td>208</td>
<td>585</td>
<td>208</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>80</td>
<td>611</td>
<td>229</td>
<td>623</td>
<td>225</td>
<td>621</td>
<td>225</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>80</td>
<td>405</td>
<td>491</td>
<td>416</td>
<td>478</td>
<td>405</td>
<td>491</td>
</tr>
<tr>
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<td>80</td>
<td>376</td>
<td>358</td>
<td>377</td>
<td>357</td>
<td>379</td>
<td>355</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>80</td>
<td>2026</td>
<td>154</td>
<td>2028</td>
<td>154</td>
<td>2023</td>
<td>154</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>80</td>
<td>1519</td>
<td>83.7</td>
<td>1510</td>
<td>84.2</td>
<td>1506</td>
<td>84.4</td>
</tr>
</tbody>
</table>

PECrate2017_fp_base = 197
PECrate2017_fp_peak = 211

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches

(Continued on next page)
General Notes (Continued)

runcpu command invoked through numactl i.e.: 
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) 
is mitigated in the system as tested and documented. 
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) 
is mitigated in the system as tested and documented. 
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) 
is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
  Thermal Configuration: Maximum Cooling
  Workload Profile: General Throughput Compute
  Memory Patrol Scrubbing: Disabled
  LLC Dead Line Allocation: Disabled
  LLC Prefetch: Enabled
  Enhanced Processor Performance: Enabled
  Workload Profile: Custom
  Advanced Memory Protection: Advanced ECC Support

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edeb1e6e46a485a0011
running on r120h1m Tue Jul 14 19:29:13 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 5218R CPU @ 2.10GHz
  2 "physical id"s (chips)
  80 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following
  excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    cpu cores : 20
    siblings : 40
    physical 0: cores 0 1 2 3 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
    physical 1: cores 0 1 2 3 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

From lscpu:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian

(Continued on next page)
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CPU2017 License: 9006
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Hardware Availability: May-2020
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Platform Notes (Continued)

CPU(s): 80
On-line CPU(s) list: 0-79
Thread(s) per core: 2
Core(s) per socket: 20
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5218R CPU @ 2.10GHz
Stepping: 7
CPU MHz: 2100.000
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 28160K
NUMA node0 CPU(s): 0-9,40-49
NUMA node1 CPU(s): 10-19,50-59
NUMA node2 CPU(s): 20-29,60-69
NUMA node3 CPU(s): 30-39,70-79
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref eagerfpu pni pclmulqdq dtses4 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtrp pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx fl64 rdrand lahf_lm abm 3dnowprefetch ebp cat_13 cdp_13 invpcid_single
intel_pmm intel_pt ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 ibrms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb avx512bw avx512vl
xsaveopt xsavec xgetbv1 cqm_llc cqm_occ_interp cqm_llc cqm_mbb_total cqm_mbb_local
dtherm ida arat pti pku ospke avx512_vnni md_clear spec_ctrl intel_stibp
flush_lid arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 40 41 42 43 44 45 46 47 48 49
node 0 size: 97960 MB
node 0 free: 95538 MB
node 1 cpus: 10 11 12 13 14 15 16 17 18 19 50 51 52 53 54 55 56 57 58 59
node 1 size: 98304 MB
node 1 free: 95986 MB

(Continued on next page)
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Platform Notes (Continued)

node 2 cpus: 20 21 22 23 24 25 26 27 28 29 60 61 62 63 64 65 66 67 68 69
node 2 size: 98304 MB
node 2 free: 95993 MB
node 3 cpus: 30 31 32 33 34 35 36 37 38 39 70 71 72 73 74 75 76 77 78 79
node 3 size: 98303 MB
node 3 free: 95960 MB
node distances:
node 0 1 2 3
0: 10 21 21 21
1: 21 10 21 21
2: 21 21 10 21
3: 21 21 21 10

From /proc/meminfo
MemTotal: 395916300 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.7 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VARIANT="Server"
    VARIANT_ID="server"
    VERSION_ID="7.7"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.7 (Maipo)"
  redhat-release: Red Hat Enterprise Linux Server release 7.7 (Maipo)
  system-release: Red Hat Enterprise Linux Server release 7.7 (Maipo)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:7.7:ga:server

uname -a:
Linux r120h1m 3.10.0-1062.1.1.el7.x86_64 #1 SMP Tue Aug 13 18:39:59 UTC 2019 x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, usercopy/swapps barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full retpoline, IBPB

(Continued on next page)
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Platform Notes (Continued)

run-level 3 Jul 14 19:23

SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 ext4 908G 179G 683G 21% /

From /sys/devices/virtual/dmi/id
BIOS: NEC U32 03/09/2020
Vendor: NEC
Product: Express5800/R120h-1M
Serial: JPN0084094

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
24x HPE P03050-091 16 GB 2 rank 2933

Regarding the sysinfo display about the memory speed, the correct configured memory speed is 2400 MT/s. The dmidecode description should be as follows:
24x HPE P03050-091 16 GB 2 rank 2933, configured at 2666

Compiler Version Notes

==============================================================================
C               | 519.lbm_r(base, peak) 538.imagick_r(base, peak)
| 544.nab_r(base, peak)
==============================================================================

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
C++             | 508.namd_r(base, peak) 510.parest_r(base, peak)
==============================================================================

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
==============================================================================

(Continued on next page)
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- SPECrates\(^\text{®}\)\text{2017}\_fp\_base = 197
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Compiler Version Notes (Continued)

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Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
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Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

(Continued on next page)
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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.//m_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

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Base Optimization Flags (Continued)

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64
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Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

544.nab_r: Same as 538.imagick_r

C++ benchmarks:
508.namd_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

510.parest_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

Fortran benchmarks:
503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -auto
-nostandard-realloc-lhs -align array32byte

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

(Continued on next page)
Peak Optimization Flags (Continued)

Benchmarks using both C and C++:

511.povray_r -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

526.blender_r -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120h-RevE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120h-RevE.xml