# SPEC CPU®2017 Integer Rate Result

**Hitachi Vantara**

**Advanced Server DS220**  
(Intel Xeon Gold 6258R, 2.70 GHz)

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**SPECrates**

- **SPECrates**
  - `2017_int_base = 339`
  - `2017_int_peak = 353`

---

**CPU 2017 License:** 35  
**Test Sponsor:** Hitachi Vantara  
**Tested by:** Hitachi Vantara  
**Test Date:** Aug-2020  
**Hardware Availability:** Mar-2020  
**Software Availability:** Apr-2020

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**Copies**  
<table>
<thead>
<tr>
<th>Benchmark</th>
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<tbody>
<tr>
<td>500.perlbench_r</td>
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</tr>
<tr>
<td>557.xz_r</td>
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</tr>
</tbody>
</table>

---

**SPECrates**  
- `2017_int_base (339)`
- `2017_int_peak (353)`

---

**Hardware**

- **CPU Name:** Intel Xeon Gold 6258R  
- **Max MHz:** 4000  
- **Nominal:** 2700  
- **Enabled:** 56 cores, 2 chips, 2 threads/core  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 38.5 MB I+D on chip per chip  
- **Orderable:** None  
- **Memory:** 384 GB (24 x 16 GB 1Rx4 PC4-2666V-R)  
- **Storage:** 1 x 1.92 TB SATA SSD  
- **Other:** None

---

**Software**

- **OS:** Red Hat Enterprise Linux release 8.0 (Ootpa)  
  4.18.0-80.el8.x86_64  
- **Compiler:** C/C++: Version 19.1.1.217 of Intel C/C++ Compiler for Linux;  
  Fortran: Version 19.1.1.217 of Intel Fortran Compiler for Linux  
- **Parallel:** No  
- **Firmware:** Version S5BH3B17.H01 released Jun-2020  
- **File System:** xfs  
- **System State:** Run level 3 (Multi-User)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage
SPEC CPU®2017 Integer Rate Result

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Advanced Server DS220
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SPECrate®2017_int_base = 339
SPECrate®2017_int_peak = 353

CUDA Performance Evaluation Corporation

CPU2017 License: 35
Test Sponsor: Hitachi Vantara
Tested by: Hitachi Vantara

Test Date: Aug-2020
Hardware Availability: Mar-2020
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
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<td>112</td>
<td>581</td>
<td>208</td>
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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler. The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux. The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = 
"/root/speccpu/lib/intel64:/root/speccpu/lib/ia32:/root/speccpu/je5.0.1-32"
MALLOCONF = "retain:true"
```
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<tr>
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**CPU2017 License:** 35  
**Test Sponsor:** Hitachi Vantara  
**Tested by:** Hitachi Vantara

**General Notes**

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM  
memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
```
  sync; echo 3 > /proc/sys/vm/drop_caches
```
  
runcpu command invoked through numactl i.e.:  
```
  numactl --interleave=all runcpu <etc>
```
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  

**Platform Notes**

BIOS settings:  
Pwr and Perf Profile set to High Performance  
Sysinfo program /root/spec/cpu/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7ed1e6e46a485a0011  
running on 192-168-159-137 Tue Aug 25 02:52:14 2020  

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo  
```
  model name : Intel(R) Xeon(R) Gold 6258R CPU @ 2.70GHz
  2 "physical id"s (chips)
  112 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 28
  siblings : 56
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30
```

From lscpu:  
```
  Architecture: x86_64
```

(Continued on next page)
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**SPECrates**
- SPEC®2017_int_base = 339
- SPEC®2017_int_peak = 353

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**Platform Notes (Continued)**

- **CPU op-mode(s):** 32-bit, 64-bit
- **Byte Order:** Little Endian
- **CPU(s):** 112
- **On-line CPU(s) list:** 0-111
- **Thread(s) per core:** 2
- **Core(s) per socket:** 28
- **Socket(s):** 2
- **NUMA node(s):** 2
- **Vendor ID:** GenuineIntel
- **CPU family:** 6
- **Model:** 85
- **Model name:** Intel(R) Xeon(R) Gold 6258R CPU @ 2.70GHz
- **Stepping:** 7
- **CPU MHz:** 1000.037
- **BogoMIPS:** 5400.00
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 1024K
- **L3 cache:** 39424K
- **NUMA node0 CPU(s):** 0-27,56-83
- **NUMA node1 CPU(s):** 28-55,84-111
- **Flags:** fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm ablp abm 3nowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_pinn ssbd mba ibrs ibpb ibrs_enhanced tpr_shadow vni flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 ibrms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdsign adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512v1 xsaveopt xsavec xsaveopt xsavec xgetbv1 xsavec xsaveopt cqm_llc cqm_occinfo llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts hwp_epp pku ospke avx512_vnni flush_l1d arch_capabilities

/procord/cpuinfo cache data
- cache size: 39424 KB

From numactl --hardware  
WARNING: a numactl 'node' might or might not correspond to a physical chip.
- available: 2 nodes (0-1)
  - node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83
  - node 0 size: 191835 MB
  - node 0 free: 182884 MB
  - node 1 cpus: 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83

(Continued on next page)
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SPECrate®2017_int_base = 339
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CPU2017 License: 35
Test Sponsor: Hitachi Vantara
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Platform Notes (Continued)

107 108 109 110 111
node 1 size: 193524 MB
node 1 free: 187194 MB
node distances:
  node 0 1
  0: 10 21
  1: 21 10

From /proc/meminfo
- MemTotal: 394608664 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
- os-release:
  - NAME="Red Hat Enterprise Linux"
  - VERSION="8.0 (Ootpa)"
  - ID="rhel"
  - ID_LIKE="fedora"
  - VERSION_ID="8.0"
  - PLATFORM_ID="platform:el8"
  - PRETTY_NAME="Red Hat Enterprise Linux 8.0 (Ootpa)"
  - ANSI_COLOR="0;31"
- redhat-release: Red Hat Enterprise Linux release 8.0 (Ootpa)
- system-release: Red Hat Enterprise Linux release 8.0 (Ootpa)
- system-release-cpe: cpe:/o:redhat:enterprise_linux:8.0:ga

uname -a:
- Linux 192-168-159-137 4.18.0-80.el8.x86_64 #1 SMP Wed Mar 13 12:02:46 UTC 2019 x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
- CVE-2018-3620 (L1 Terminal Fault): Not affected
- Microarchitectural Data Sampling: No status reported
- CVE-2017-5754 (Meltdown): Not affected
- CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
- CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 Aug 24 21:04

SPEC is set to: /root/speccpu

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 1.8T 103G 1.7T 6% /

(Continued on next page)
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Platform Notes (Continued)

From /sys/devices/virtual/dmi/id
BIOS: American Megatrends Inc. S5BH3B17.H01 06/30/2020
Vendor: Hitachi Vantara
Product: Advanced Server DS220

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
24x Samsung M393A2K40BB2-CTD 16 GB 1 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

C | 502.gcc_r(peak)

Intel(R) C Compiler for applications running on IA-32, Version 2021.1 NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
| 525.x264_r(base, peak) 557.xz_r(base)

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1 NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C | 500.perlbench_r(peak) 557.xz_r(peak)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C | 502.gcc_r(peak)

Intel(R) C Compiler for applications running on IA-32, Version 2021.1 NextGen

(Continued on next page)
Hitachi Vantara
Advanced Server DS220
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Compiler Version Notes (Continued)

Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base)
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Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1 NextGen Build 20200304
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==============================================================================
C       | 500.perlbench_r(peak) 557.xz_r(peak)
==============================================================================
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306
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==============================================================================
C       | 502.gcc_r(peak)
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C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base)
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==============================================================================
C       | 500.perlbench_r(peak) 557.xz_r(peak)
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Compiler Version Notes (Continued)

C++
520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak)
531.deepsjeng_r(base, peak) 541.leela_r(base, peak)

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Fortran
548.exchange2_r(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbmk_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
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Base Optimization Flags

C benchmarks:
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-fuse-ld=gold -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse
-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX

(Continued on next page)
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Test Date: Aug-2020
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Peak Portability Flags (Continued)

525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/ia32_lin
-std=gnu89
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fprofile-use=default.profdata(pass 1)
-xCORE-AVX512 -ipo -O3 -fno-strict-overflow -fno-alias
-Qfno-alias -fprofile-use=default.profdata(pass 2)
-XL/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

505.mcf_r: basepeak = yes

525.x264_r: -m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -lfto -O3 -ffast-math -fnextgen -fno-alias
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:

(Continued on next page)
Hitachi Vantara
Advanced Server DS220
(Intel Xeon Gold 6258R, 2.70 GHz)

SPECraten®2017_int_base = 339
SPECraten®2017_int_peak = 353

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Peak Optimization Flags (Continued)

520.omnetpp_r: basepeak = yes
523.xalancbmk_r: basepeak = yes
531.deepsjeng_r: basepeak = yes
541.leela_r: basepeak = yes
Fortran benchmarks:
548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-08-24 14:52:13-0400.
Originally published on 2020-09-15.