**SPEC CPU®2017 Integer Speed Result**

New H3C Technologies Co., Ltd.  
H3C UniServer R6900 G5 (Intel Xeon Platinum 8380HL)

<table>
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<th>Spec Speed</th>
<th>SPECspeed®2017_int_base = 11.9</th>
<th>SPECspeed®2017_int_peak = 12.2</th>
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<td>New H3C Technologies Co., Ltd.</td>
<td>Software Availability:</td>
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**Threads**

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<tr>
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<td>112</td>
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<td>14.4</td>
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<td>625.x264_s</td>
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<td>6.06</td>
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<td>631.deepsjeng_s</td>
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<td>648.exchange2_s</td>
<td>112</td>
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<tr>
<td>657.xz_s</td>
<td>112</td>
<td>27.0</td>
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**Hardware**

- **CPU Name:** Intel Xeon Platinum 8380HL  
- **Max MHz:** 4300  
- **Nominal:** 2900  
- **Enabled:** 112 cores, 4 chips  
- **Orderable:** 1,2,3,4 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 38.5 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 768 GB (48 x 16 GB 2Rx8 PC4-3200V-R, running at 2933)  
- **Storage:** 1 x 960 GB SATA SSD  
- **Other:** None  

**Software**

- **OS:** Red Hat Enterprise Linux release 8.2 (Ootpa)  
- **Compiler:** C/C++: Version 19.1.1.217 of Intel C/C++ Compiler Build 20200306 for Linux; Fortran: Version 19.1.1.217 of Intel Fortran Compiler Build 20200306 for Linux  
- **Parallel:** Yes  
- **Firmware:** Version 5.19 released Jul-2020 BIOS  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage.
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base</th>
<th>Peak</th>
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<tr>
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<td>648.exchange2_s</td>
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<td>169</td>
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<tr>
<td>657.xz_s</td>
<td>112</td>
<td>229</td>
</tr>
</tbody>
</table>

**Compiler Notes**

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler. The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:

- KMP_AFFINITY = "granularity=fine,scatter"
- LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"
- MALLOC_CONF = "retain:true"
- OMP_STACKSIZE = "192M"

**General Notes**

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
General Notes (Continued)

is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS Settings:
Set Hyper-Threading [ALL] to Disabled
Set Power Performance Tuning to BIOS Controls EPB
Set Energy Performance BIAS to Performance
Set XPT Prefetch to Auto
Set Patrol Scrub to Disabled

Sysinfo program /home/spec/cpu/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edbble6e46a485a0011
running on localhost.localdomain Wed Aug 19 21:03:24 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Platinum 8380HL CPU @ 2.90GHz
  4 "physical id"s (chips)
  112 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 28
  siblings : 28
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30
  physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30
  physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30

From lsmpio:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

New H3C Technologies Co., Ltd.
H3C UniServer R6900 G5 (Intel Xeon Platinum 8380HL)

SPECspeed®2017_int_peak = 12.2
SPECspeed®2017_int_base = 11.9

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Test Date: Aug-2020
Tested by: New H3C Technologies Co., Ltd.
Hardware Availability: Sep-2020
Software Availability: Apr-2020

Platform Notes (Continued)

Byte Order: Little Endian
CPU(s): 112
On-line CPU(s) list: 0-111
Thread(s) per core: 1
Core(s) per socket: 28
Socket(s): 4
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Platinum 8380HL CPU @ 2.90GHz
Stepping: 11
CPU MHz: 1365.955
CPU max MHz: 4300.0000
CPU min MHz: 1000.0000
BogoMIPS: 5800.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 39424K
NUMA node0 CPU(s): 0-27
NUMA node1 CPU(s): 28-55
NUMA node2 CPU(s): 56-83
NUMA node3 CPU(s): 84-111
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrnr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_pinn ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
flexpriority epb vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 3dnow invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsavecm xgetbv1 xsaves cqm_llc cqm_ocmp LLc cqm_mwm_total
cqm_mwm_local avx512_bf16 dtherm ida arat pin pts hwp hwp_act_window hwp_epp
hwp_pkg_req pkg ospke avx512_vnni md_clear flush_l1d arch_capabilities

/proc/cpuinfo cache data
cache size: 39424 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
node 0 size: 191823 MB
node 0 free: 191611 MB

(Continued on next page)
## Platform Notes (Continued)

- **node 1 cpus:** 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55
- **node 1 size:** 193529 MB
- **node 1 free:** 192786 MB

- **node 2 cpus:** 56, 57, 58, 59, 60, 61, 62, 63, 64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79, 80, 81, 82, 83
- **node 2 size:** 193529 MB
- **node 2 free:** 192988 MB

- **node 3 cpus:** 84, 85, 86, 87, 88, 89, 90, 91, 92, 93, 94, 95, 96, 97, 98, 99, 100, 101, 102, 103, 104, 105, 106, 107, 108, 109, 110, 111
- **node 3 size:** 193529 MB
- **node 3 free:** 193156 MB

- **node distances:**
  - node 0 to 1: 10, 20, 20, 20
  - node 0 to 2: 20, 10, 20, 20
  - node 0 to 3: 20, 20, 10, 20
  - node 1 to 2: 20, 20, 20, 10
  - node 1 to 3: 20, 20, 20, 10
  - node 2 to 3: 20, 20, 20, 10

From `/proc/meminfo`
- MemTotal: 790950240 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From `/etc/*release* /etc/*version*`
- **os-release:**
  - NAME="Red Hat Enterprise Linux"
  - VERSION="8.2 (Ootpa)"
  - ID="rhel"
  - ID_LIKE="fedora"
  - VERSION_ID="8.2"
  - PLATFORM_ID="platform:el8"
  - PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
  - ANSI_COLOR="0;31"
- **redhat-release:** Red Hat Enterprise Linux release 8.2 (Ootpa)
- **system-release:** Red Hat Enterprise Linux release 8.2 (Ootpa)
- **system-release-cpe:** cpe:/o:redhat:enterprise_linux:8.2:ga

**uname -a:**
```
Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:
- **itlb_multihit:** Not affected
- **CVE-2018-3620 (L1 Terminal Fault):** Not affected
- **Microarchitectural Data Sampling:** Not affected

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

New H3C Technologies Co., Ltd.
H3C UniServer R6900 G5 (Intel Xeon Platinum 8380HL)

SPECspeed®2017_int_base = 11.9
SPECspeed®2017_int_peak = 12.2

Platform Notes (Continued)

CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
tsx_async_abort: Not affected

run-level 3 Aug 19 20:56
SPEC is set to: /home/speccpu

Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 839G 30G 809G 4% /home

From /sys/devices/virtual/dmi/id
BIOS: American Megatrends International, LLC. 5.19 07/23/2020
Product Family: SYSTEM_FAMILY

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
45x Hynix HMA82GR7DJR8N-XN 16 GB 2 rank 3200
3x Micron 18ASF2G72PDZ-3G2E1 16 GB 2 rank 3200

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
  | 625.x264_s(base, peak) 657.xz_s(base, peak)
==============================================================================
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C | 600.perlbench_s(peak)
==============================================================================
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

(Continued on next page)
New H3C Technologies Co., Ltd.
H3C UniServer R6900 G5 (Intel Xeon Platinum 8380HL)

SPECspeed®2017_int_base = 11.9
SPECspeed®2017_int_peak = 12.2

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Aug-2020
Hardware Availability: Sep-2020
Software Availability: Apr-2020

Compiler Version Notes (Continued)

================================================================================
| C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)         |
|         | 625.x264_s(base, peak) 657.xz_s(base, peak)                                |
| Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1 |
|         | NextGen Build 20200304                                                    |
|         | Copyright (C) 1985-2020 Intel Corporation. All rights reserved.            |
================================================================================
| C       | 600.perlbench_s(peak)                                                    |
| Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  |
|         | Version 19.1.1.217 Build 20200306                                        |
|         | Copyright (C) 1985-2020 Intel Corporation. All rights reserved.            |
================================================================================
| C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, |
|         | peak) 641.leela_s(base, peak)                                             |
| Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1|
|         | NextGen Build 20200304                                                    |
|         | Copyright (C) 1985-2020 Intel Corporation. All rights reserved.            |
================================================================================
| Fortran | 648.exchange2_s(base, peak)                                               |
| Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) |
|         | 64, Version 19.1.1.217 Build 20200306                                     |
|         | Copyright (C) 1985-2020 Intel Corporation. All rights reserved.            |
================================================================================

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort
### SPEC CPU®2017 Integer Speed Result

**New H3C Technologies Co., Ltd.**  
H3C UniServer R6900 G5 (Intel Xeon Platinum 8380HL)

**SPECspeed®2017_int_base = 11.9**  
**SPECspeed®2017_int_peak = 12.2**

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<tr>
<td></td>
<td></td>
<td>Technologies Co., Ltd.</td>
<td></td>
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</tbody>
</table>

#### Base Portability Flags

- **600.perlbench_s**: `-DSPEC_LP64 -DSPEC_LINUX_X64`
- **602.gcc_s**: `-DSPEC_LP64`
- **605.mcf_s**: `-DSPEC_LP64`
- **620.omnetpp_s**: `-DSPEC_LP64`
- **623.xalancbmk_s**: `-DSPEC_LP64 -DSPEC_LINUX`
- **625.x264_s**: `-DSPEC_LP64`
- **631.deepsjeng_s**: `-DSPEC_LP64`
- **641.leela_s**: `-DSPEC_LP64`
- **648.exchange2_s**: `-DSPEC_LP64`
- **657.xz_s**: `-DSPEC_LP64`

#### Base Optimization Flags

**C benchmarks:**
- `-m64 -gnextgen -std=c11`
- `-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs`
- `-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops`
- `-fuse-ld=gold -qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP`
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

**C++ benchmarks:**
- `-m64 -gnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries`
- `-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse`
- `-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin -lqkmalloc`

**Fortran benchmarks:**
- `-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -xCORE-AVX512`
- `-O3 -ipo -no-prec-div -qopt-mem-layout-trans=4`
- `-nostandard-realloc-lhs -align array32byte`
- `-mbranches-within-32B-boundaries`

#### Peak Compiler Invocation

**C benchmarks:**
- `icc`

**C++ benchmarks:**
- `icpc`

(Continued on next page)
Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64(*) -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

(*) Indicates a portability flag that was found in a non-portability variable.

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -qnextgen -std=c11 -fuse-ld=gold
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math
-fuse-ld=gold -qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

(Continued on next page)
New H3C Technologies Co., Ltd.
H3C UniServer R6900 G5 (Intel Xeon Platinum 8380HL)

SPECspeed®2017_int_base = 11.9
SPECspeed®2017_int_peak = 12.2

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.
Test Date: Aug-2020
Hardware Availability: Sep-2020
Software Availability: Apr-2020

Peak Optimization Flags (Continued)

657.xz_s: basepeak = yes
C++ benchmarks:
620.omnetpp_s: basepeak = yes
623.xalancbmk_s: basepeak = yes
631.deepsjeng_s: basepeak = yes
641.leela_s: basepeak = yes
Fortran benchmarks:
648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-CPX-RevA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-08-19 09:03:23-0400.
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