## SPEC CPU®2017 Floating Point Speed Result

New H3C Technologies Co., Ltd.

### H3C UniServer R4900 G3 (Intel Xeon Gold 6230R)

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base</th>
<th>136</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_fp_peak</td>
<td>137</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Tested by:</th>
<th>New H3C Technologies Co., Ltd.</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU2017 License:</td>
<td>9066</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Sep-2020</td>
</tr>
<tr>
<td>Test Sponsor:</td>
<td>New H3C Technologies Co., Ltd.</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Mar-2020</td>
</tr>
<tr>
<td>Tested by:</td>
<td>New H3C Technologies Co., Ltd.</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Apr-2020</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name**: Intel Xeon Gold 6230R
- **Max MHz**: 4000
- **Nominal**: 2100
- **Enabled**: 52 cores, 2 chips
- **Orderable**: 1.2 chips
- **Cache L1**: 32 KB I + 32 KB D on chip per core
- **L2**: 1 MB I+D on chip per core
- **L3**: 35.75 MB I+D on chip per chip
- **Memory**: 192 GB (12 x 16 GB 2Rx8 PC4-2933V-R)
- **Storage**: 2 x 600 GB SAS HDD,10k RPM,RAID 1
- **Other**: None

### Software

- **OS**: Red Hat Enterprise Linux release 8.2 (Ootpa)
- **Compiler**: C/C++: Version 19.1.1.217 of Intel C/C++ Compiler Build 20200306 for Linux;
  
  Fortran: Version 19.1.1.217 of Intel Fortran Compiler Build 20200306 for Linux
- **Parallel**: Yes
- **Firmware**: Version 2.00.33 released Aug-2019 BIOS
- **File System**: xfs
- **System State**: Run level 3 (multi-user)
- **Base Pointers**: 64-bit
- **Peak Pointers**: 64-bit
- **Other**: jemalloc memory allocator V5.0.1
- **Power Management**: BIOS set to prefer performance at the cost of additional power usage

### Benchmark Results

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_fp_base</th>
<th>SPECspeed®2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>52</td>
<td></td>
<td></td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>52</td>
<td></td>
<td></td>
</tr>
<tr>
<td>619.ibm_s</td>
<td>52</td>
<td>92.5</td>
<td></td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>52</td>
<td>121</td>
<td></td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>52</td>
<td>111</td>
<td></td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>52</td>
<td>59.9</td>
<td></td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>52</td>
<td>107</td>
<td></td>
</tr>
<tr>
<td>644.nab_s</td>
<td>52</td>
<td></td>
<td>267</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>52</td>
<td></td>
<td>279</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>52</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

---

New H3C Technologies Co., Ltd.
New H3C Technologies Co., Ltd. H3C UniServer R4900 G3 (Intel Xeon Gold 6230R)

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>52</td>
<td>121</td>
<td>488</td>
<td>119</td>
<td>497</td>
<td>52</td>
<td>121</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>52</td>
<td>96.8</td>
<td>172</td>
<td>96.5</td>
<td>173</td>
<td>52</td>
<td>96.8</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>52</td>
<td>56.6</td>
<td>92.8</td>
<td>57.5</td>
<td>91.1</td>
<td>52</td>
<td>56.6</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>52</td>
<td>110</td>
<td>121</td>
<td>110</td>
<td>120</td>
<td>52</td>
<td>105</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>52</td>
<td>79.9</td>
<td>111</td>
<td>79.8</td>
<td>111</td>
<td>52</td>
<td>79.9</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>52</td>
<td>203</td>
<td>58.6</td>
<td>193</td>
<td>61.4</td>
<td>52</td>
<td>203</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>52</td>
<td>134</td>
<td>107</td>
<td>134</td>
<td>107</td>
<td>52</td>
<td>134</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>52</td>
<td>65.6</td>
<td>266</td>
<td>65.5</td>
<td>267</td>
<td>52</td>
<td>62.7</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>52</td>
<td>109</td>
<td>83.7</td>
<td>109</td>
<td>83.6</td>
<td>52</td>
<td>111</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>52</td>
<td>113</td>
<td>139</td>
<td>114</td>
<td>138</td>
<td>52</td>
<td>113</td>
</tr>
</tbody>
</table>

SPECspeed®2017_fp_base = 136
SPECspeed®2017_fp_peak = 137

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

(Continued on next page)
New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 6230R)  

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base = 136</th>
<th>SPECspeed®2017_fp_peak = 137</th>
</tr>
</thead>
</table>

**CPU2017 License:** 9066  
**Test Date:** Sep-2020  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Hardware Availability:** Mar-2020  
**Tested by:** New H3C Technologies Co., Ltd.  
**Software Availability:** Apr-2020

---

**General Notes (Continued)**


---

**Platform Notes**

BIOS Settings:
Set Hyper Threading to Disabled  
Set XPT Prefetch to Auto  
Set Patrol Scrub to Disabled

Sysinfo program /home/speccpu/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7edbbe6e46a485a0111  
running on localhost.localdomain Fri Sep 11 19:41:33 2020

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo:
```
model name : Intel(R) Xeon(R) Gold 6230R CPU @ 2.10GHz
  2 "physical id"s (chips)
  52 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 26
siblings : 26
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28 29
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28 29
```

From lscpu:
```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 52
On-line CPU(s) list: 0-51
Thread(s) per core: 1
Core(s) per socket: 26
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6230R CPU @ 2.10GHz
Stepping: 7
CPU MHz: 3574.423
```

(Continued on next page)
SPEC CPU®2017 Floating Point Speed Result

New H3C Technologies Co., Ltd.

H3C UniServer R4900 G3 (Intel Xeon Gold 6230R)

SPECspeed®2017_fp_base = 136

SPECspeed®2017_fp_peak = 137

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Sep-2020
Hardware Availability: Mar-2020
Software Availability: Apr-2020

Platform Notes (Continued)

CPU max MHz: 4000.0000
CPU min MHz: 1000.0000
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0-25
NUMA node1 CPU(s): 26-51
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid

From /proc/cpuinfo cache data

/cache size : 36608 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

Available: 2 nodes (0-1)
Node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
Node 0 size: 95073 MB
Node 0 free: 92249 MB
Node 1 cpus: 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51
Node 1 size: 96734 MB
Node 1 free: 90330 MB
Node distances:
0: 10 21
1: 21 10

From /proc/meminfo

MemTotal: 196411664 KB
HugePages_Total: 0
Hugepagesize: 2048 KB

From /etc/*release*/etc/*version*
New H3C Technologies Co., Ltd.
H3C UniServer R4900 G3 (Intel Xeon Gold 6230R)

**CPU2017 License:** 9066
**Test Date:** Sep-2020
**Test Sponsor:** New H3C Technologies Co., Ltd.
**Hardware Availability:** Mar-2020
**Tested by:** New H3C Technologies Co., Ltd.
**Software Availability:** Apr-2020

**SPECspeed\textsuperscript{\textregistered}2017\textsubscript{fp}\_base = 136**

**SPECspeed\textsuperscript{\textregistered}2017\textsubscript{fp}\_peak = 137**

**Platform Notes (Continued)**

```bash
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.2 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.2"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga
```

```
uname -a:
Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

- **itlb_multihit:** KVM: Mitigation: Split huge pages
- **CVE-2018-3620 (L1 Terminal Fault):** Not affected
- **Microarchitectural Data Sampling:** Not affected
- **CVE-2017-5754 (Meltdown):** Not affected
- **CVE-2018-3639 (Speculative Store Bypass):** Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- **CVE-2017-5753 (Spectre variant 1):** Mitigation: usercopy/swapgs barriers and \_\_user pointer sanitization
- **CVE-2017-5715 (Spectre variant 2):** Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
- **tsx_async_abort:** Mitigation: Clear CPU buffers; SMT disabled

```
run-level 3 Sep 11 15:34
```

**SPEC is set to:** /home/spec/cpu

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/mapper/rhel-home</td>
<td>xfs</td>
<td>503G</td>
<td>78G</td>
<td>425G</td>
<td>16%</td>
<td>/home</td>
</tr>
</tbody>
</table>

From /sys/devices/virtual/dmi/id

- **BIOS:** American Megatrends Inc. 2.00.33 08/22/2019
- **Vendor:** H3C
- **Product:** RS33M2C9S
- **Product Family:** Rack

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

(Continued on next page)
New H3C Technologies Co., Ltd. | SPECperformance®2017_fp_base = 136
H3C UniServer R4900 G3 (Intel Xeon Gold 6230R) | SPECperformance®2017_fp_peak = 137

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Test Date: Sep-2020
Tested by: New H3C Technologies Co., Ltd.
Hardware Availability: Mar-2020
Software Availability: Apr-2020

Platform Notes (Continued)

Memory:
12x NO DIMM NO DIMM
12x Samsung M393A2K43CB2-CVF 16 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

```
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
| 644.nab_s(base, peak)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

```
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
```

```
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

```
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)
| 654.roms_s(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

```
Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak)
| 628.pop2_s(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.1.1.217 Build 20200306
```

(Continued on next page)
New H3C Technologies Co., Ltd. | SPECspeed®2017_fp_base = 136
H3C UniServer R4900 G3 (Intel Xeon Gold 6230R) | SPECspeed®2017_fp_peak = 137

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>9066</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>New H3C Technologies Co., Ltd.</td>
</tr>
<tr>
<td>Tested by:</td>
<td>New H3C Technologies Co., Ltd.</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Sep-2020</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Mar-2020</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Apr-2020</td>
</tr>
</tbody>
</table>

**Compiler Version Notes (Continued)**

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Intel (R) C Intel (R) 64 Compiler for applications running on Intel (R) 64,
Version 19.1.1.217 Build 20200306

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

--

**Base Compiler Invocation**

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

**Base Portability Flags**

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
   -assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:
-m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries

(Continued on next page)
New H3C Technologies Co., Ltd.
H3C UniServer R4900 G3 (Intel Xeon Gold 6230R)

SPECspeed®2017_fp_base = 136
SPECspeed®2017_fp_peak = 137

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Sep-2020
Hardware Availability: Mar-2020
Software Availability: Apr-2020

Base Optimization Flags (Continued)

Fortran benchmarks:
- \( m64 \) -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -ipo -03
- -no-prec-div -qopt-prefetch -ffinite-math-only
- -qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
- -mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib
- -ljemalloc

Benchmarks using both Fortran and C:
- \( m64 \) -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -03 -no-prec-div
- -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
- -DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
- -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
- \( m64 \) -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -03 -no-prec-div
- -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
- -DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
- -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags
New H3C Technologies Co., Ltd. | SPECspeed®2017_fp_base = 136
H3C UniServer R4900 G3 (Intel Xeon Gold 6230R) | SPECspeed®2017_fp_peak = 137

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.
Test Date: Sep-2020
Hardware Availability: Mar-2020
Software Availability: Apr-2020

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes
638.imagick_s: basepeak = yes
644.nab_s: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

603.bwaves_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX512
-03 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
649.fotonik3d_s: Same as 603.bwaves_s
654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)
-prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
627.cam4_s: basepeak = yes
628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.html
New H3C Technologies Co., Ltd. | SPECspeed®2017_fp_base = 136
H3C UniServer R4900 G3 (Intel Xeon Gold 6230R) | SPECspeed®2017_fp_peak = 137

<table>
<thead>
<tr>
<th>CPU2017 License: 9066</th>
<th>Test Date: Sep-2020</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: New H3C Technologies Co., Ltd.</td>
<td>Hardware Availability: Mar-2020</td>
</tr>
<tr>
<td>Tested by: New H3C Technologies Co., Ltd.</td>
<td>Software Availability: Apr-2020</td>
</tr>
</tbody>
</table>

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.xml

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-09-11 07:41:32-0400.
Originally published on 2020-09-29.