## SPEC CPU®2017 Integer Rate Result

**New H3C Technologies Co., Ltd.**

**H3C UniServer R4900 G3 (Intel Xeon Gold 5222)**

**SPECrate®2017_int_base = 69.7**

**SPECrate®2017_int_peak = 72.0**

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name: Intel Xeon Gold 5222</td>
<td>OS: Red Hat Enterprise Linux release 8.2 (Ootpa) 4.18.0-193.el8.x86_64</td>
</tr>
<tr>
<td>Nominal: 3800</td>
<td>Parallel: No</td>
</tr>
<tr>
<td>Enabled: 8 cores, 2 chips, 2 threads/core</td>
<td>Firmware: Version 2.00.33 released Aug-2019 BIOS</td>
</tr>
<tr>
<td>Orderable: 1.2 chips</td>
<td>File System: xfs</td>
</tr>
<tr>
<td>Cache L1: 32 KB I + 32 KB D on chip per core</td>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>L2: 1 MB I+D on chip per core</td>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>L3: 16.5 MB I+D on chip per chip</td>
<td>Peak Pointers: 32/64-bit</td>
</tr>
<tr>
<td>Other: None</td>
<td>Other: jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2933V-R)</td>
<td>Power Management: BIOS set to prefer performance at the cost of additional power usage</td>
</tr>
<tr>
<td>Storage: 2 x 600 GB SAS HDD,10k RPM,RAID 1</td>
<td></td>
</tr>
<tr>
<td>Other: None</td>
<td></td>
</tr>
</tbody>
</table>

### Software Availability: Apr-2020

**Test Sponsor:** New H3C Technologies Co., Ltd.
**Test Date:** Sep-2020
**Hardware Availability:** Mar-2019

**Tested by:** New H3C Technologies Co., Ltd.

**CPU2017 License:** 9066

---

**Copies**

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<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
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<td>53.7</td>
<td>72.0</td>
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<td>520.omnetpp_r</td>
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<td>99.2</td>
<td></td>
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<td>49.7</td>
<td>141</td>
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<tr>
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<td></td>
</tr>
<tr>
<td>541.leela_r</td>
<td>40.0</td>
<td>40.6</td>
<td></td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>40.0</td>
<td>40.6</td>
<td></td>
</tr>
<tr>
<td>557.xz_r</td>
<td>40.0</td>
<td>40.6</td>
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</tr>
</tbody>
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**Test Date:** Sep-2020
**Hardware Availability:** Mar-2019
**Software Availability:** Apr-2020
New H3C Technologies Co., Ltd. | SPECrate®2017_int_base = 69.7  
H3C UniServer R4900 G3 (Intel Xeon Gold 5222) | SPECrate®2017_int_peak = 72.0  

**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.  
**Test Date:** Sep-2020  
**Hardware Availability:** Mar-2019  
**Software Availability:** Apr-2020  

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tr>
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<td>16</td>
<td>554</td>
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<tr>
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<td>406</td>
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<td>408</td>
<td>55.5</td>
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<td>16</td>
<td>360</td>
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<td>363</td>
<td>62.5</td>
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<td>505.mcf_r</td>
<td>16</td>
<td>210</td>
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<td>123</td>
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<td>45.3</td>
<td>465</td>
<td>45.1</td>
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<tr>
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<tr>
<td>541.leela_r</td>
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<td>533</td>
<td>49.7</td>
<td>536</td>
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<tr>
<td>548.exchange2_r</td>
<td>16</td>
<td>330</td>
<td>127</td>
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<tr>
<td>557.xz_r</td>
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<td>432</td>
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<td>425</td>
<td>40.6</td>
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<td>40.6</td>
<td>425</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler. The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux  
The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD_LIBRARY_PATH =  
"/home/speccpu/lib/intel64:/home/speccpu/lib/ia32:/home/speccpu/je5.0.1-32"  
MALLOCONF = "retain: true"
SPEC CPU®2017 Integer Rate Result

New H3C Technologies Co., Ltd. | SPECrate®2017_int_base = 69.7
H3C UniServer R4900 G3 (Intel Xeon Gold 5222) | SPECrate®2017_int_peak = 72.0

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS settings:
Set SNC to Enabled
Set XPT Prefetch to Enabled
Set IMC Interleaving to 1-way Interleave
Set Patrol Scrub to Disabled

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7eddb1e6e46a485a0011
running on localhost.localdomain Fri Sep 11 17:26:25 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5222 CPU @ 3.80GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 8
physical 0: cores 0 5 9 13
physical 1: cores 0 2 11 13

From lscpu: (Continued on next page)
**SPEC CPU®2017 Integer Rate Result**

New H3C Technologies Co., Ltd. H3C UniServer R4900 G3 (Intel Xeon Gold 5222)

**SPECrate®2017_int_base = 69.7**

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---

**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.

---

**Platform Notes (Continued)**

- **Architecture:** x86_64  
- **CPU op-mode(s):** 32-bit, 64-bit  
- **Byte Order:** Little Endian  
- **CPU(s):** 16  
- **On-line CPU(s) list:** 0-15  
- **Thread(s) per core:** 2  
- **Core(s) per socket:** 4  
- **Socket(s):** 2  
- **NUMA node(s):** 4  
- **Vendor ID:** GenuineIntel  
- **CPU family:** 6  
- **Model:** 85  
- **Model name:** Intel(R) Xeon(R) Gold 5222 CPU @ 3.80GHz  
- **Stepping:** 7  
- **CPU MHz:** 1938.825  
- **CPU max MHz:** 3900.0000  
- **CPU min MHz:** 1200.0000  
- **BogoMIPS:** 7600.00  
- **Virtualization:** VT-x  
- **L1d cache:** 32K  
- **L1i cache:** 32K  
- **L2 cache:** 1024K  
- **L3 cache:** 16896K  
- **NUMA node0 CPU(s):** 0,2,8,10  
- **NUMA node1 CPU(s):** 1,3,9,11  
- **NUMA node2 CPU(s):** 4,5,12,13  
- **NUMA node3 CPU(s):** 6,7,14,15  
- **Flags:** fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 x86r pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abrd3nowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_puin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavevc xsaveci xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req pku ospke avx512_vnni md_clear flush_l1d arch_capabilities

/proc/cpuinfo cache data  
  cache size : 16896 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.  
  available: 4 nodes (0-3)  
  node 0 cpus: 0 2 8 10

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

New H3C Technologies Co., Ltd.
H3C UniServer R4900 G3 (Intel Xeon Gold 5222)

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SPECrate®2017_int_peak = 72.0

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.
Test Date: Sep-2020
Hardware Availability: Mar-2019
Software Availability: Apr-2020

Platform Notes (Continued)

node 0 size: 95079 MB
node 0 free: 88726 MB
node 1 cpus: 1 3 9 11
node 1 size: 96766 MB
node 1 free: 96265 MB
node 2 cpus: 4 5 12 13
node 2 size: 96766 MB
node 2 free: 95840 MB
node 3 cpus: 6 7 14 15
node 3 size: 96738 MB
node 3 free: 96383 MB
node distances:
node 0 1 2 3
0: 10 11 21 21
1: 11 10 21 21
2: 21 21 10 11
3: 21 21 11 10

From /proc/meminfo
MemTotal: 394600176 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.2 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.2"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
    ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
  system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
  Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

  itlb_multihit: KVM: Mitigation: Split huge pages
  CVE-2018-3620 (L1 Terminal Fault): Not affected
  Microarchitectural Data Sampling: Not affected
  CVE-2017-5754 (Meltdown): Not affected

(Continued on next page)
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| Test Sponsor: New H3C Technologies Co., Ltd. | Hardware Availability: Mar-2019  
| Tested by: New H3C Technologies Co., Ltd. | Software Availability: Apr-2020  

### Platform Notes (Continued)

- **CVE-2018-3639 (Speculative Store Bypass):** Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- **CVE-2017-5753 (Spectre variant 1):** Mitigation: usercopy/swapgs barriers and __user pointer sanitization
- **CVE-2017-5715 (Spectre variant 2):** Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
- **tsx_async_abort:** Mitigation: Clear CPU buffers; SMT vulnerable

```
run-level 3 Sep 11 17:24 last=5
```

**SPEC is set to:** /home/speccpu  
**Filesystem**  
<table>
<thead>
<tr>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>xfs</td>
<td>504G</td>
<td>57G</td>
<td>447G</td>
<td>12%</td>
<td>/home</td>
</tr>
</tbody>
</table>

From /sys/devices/virtual/dmi/id  
**BIOS:** American Megatrends Inc. 2.00.33 08/22/2019  
**Vendor:** H3C  
**Product:** RS33M2C9S  
**Product Family:** Rack

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

**Memory:**  
- 12x NO DIMM NO DIMM  
- 1x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933  
- 11x Samsung M393A4K40DB2-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)

### Compiler Version Notes

```
C | 502.gcc_r(peak)
```

**Intel(R) C Compiler for applications running on IA-32, Version 2021.1 NextGen Build 20200304**  
**Copyright (C) 1985-2020 Intel Corporation. All rights reserved.**

```
C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base)
```

**Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1** (Continued on next page)
New H3C Technologies Co., Ltd.

H3C UniServer R4900 G3 (Intel Xeon Gold 5222)

SPEC CPU®2017 Integer Rate Result
Copyright 2017-2020 Standard Performance Evaluation Corporation

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Tested by: New H3C Technologies Co., Ltd.
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Hardware Availability: Mar-2019
Software Availability: Apr-2020

Compiler Version Notes (Continued)

NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------
C | 500.perlbench_r(peak) 557.xz_r(peak)
------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------
C | 502.gcc_r(peak)
------------------------------------------------------------------
Intel(R) C Compiler for applications running on IA-32, Version 2021.1 NextGen
Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------
C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
| 525.x264_r(base, peak) 557.xz_r(base)
------------------------------------------------------------------
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------
C | 500.perlbench_r(peak) 557.xz_r(peak)
------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------
C | 502.gcc_r(peak)
------------------------------------------------------------------
Intel(R) C Compiler for applications running on IA-32, Version 2021.1 NextGen
Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------
C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
------------------------------------------------------------------
(Continued on next page)
New H3C Technologies Co., Ltd.
H3C UniServer R4900 G3 (Intel Xeon Gold 5222)

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

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Test Date: Sep-2020
Hardware Availability: Mar-2019
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Compiler Version Notes (Continued)

<table>
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<tr>
<th>525.x264_r(base, peak) 557.xz_r(base)</th>
</tr>
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<tbody>
<tr>
<td>Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1</td>
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<tr>
<td>NextGen Build 20200304</td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
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<table>
<thead>
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<th>500.perlbench_r(peak) 557.xz_r(peak)</th>
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</tr>
<tr>
<td>Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1</td>
</tr>
<tr>
<td>NextGen Build 20200304</td>
</tr>
<tr>
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<table>
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<th>548.exchange2_r(base, peak)</th>
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</thead>
<tbody>
<tr>
<td>Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306</td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
</tr>
</tbody>
</table>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort
## SPEC CPU®2017 Integer Rate Result

**New H3C Technologies Co., Ltd.**

H3C UniServer R4900 G3 (Intel Xeon Gold 5222)

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base</th>
<th>69.7</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_int_peak</td>
<td>72.0</td>
</tr>
</tbody>
</table>

### CPU2017 License: 9066

**Test Sponsor:** New H3C Technologies Co., Ltd.

**Tested by:** New H3C Technologies Co., Ltd.

**Test Date:** Sep-2020

**Hardware Availability:** Mar-2019

**Software Availability:** Apr-2020

### Base Portability Flags

- `500.perlbench_r`: `-DSPEC_LP64 -DSPEC_LINUX_X64`
- `502.gcc_r`: `-DSPEC_LP64`
- `505.mcf_r`: `-DSPEC_LP64`
- `520.omnetpp_r`: `-DSPEC_LP64`
- `523.xalancbmk_r`: `-DSPEC_LP64 -DSPEC_LINUX`
- `525.x264_r`: `-DSPEC_LP64`
- `531.deepsjeng_r`: `-DSPEC_LP64`
- `541.leela_r`: `-DSPEC_LP64`
- `548.exchange2_r`: `-DSPEC_LP64`
- `557.xz_r`: `-DSPEC_LP64`

### Base Optimization Flags

**C benchmarks:**
- `-m64 -qnextgen -std=c11`
- `-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs`
- `-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops`
- `-fuse-ld=gold -qopt-mem-layout-trans=4`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin`

**C++ benchmarks:**
- `-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries`
- `-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse`
- `-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin`

**Fortran benchmarks:**
- `-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs`
- `-xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4`
- `-nostandard-realloc-lhs -align array32byte -auto`
- `-mbranches-within-32B-boundaries`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin`

### Peak Compiler Invocation

**C benchmarks:**
- `icc`

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Peak Compiler Invocation (Continued)

C++ benchmarks:  
icpc

Fortran benchmarks:  
ifort

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc_r: -D_FILE_OFFSET_BITS=64  
505.mcf_r: -DSPEC_LP64  
520.omnetpp_r: -DSPEC_LP64  
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
525.x264_r: -DSPEC_LP64  
531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64  
548.exchange2_r: -DSPEC_LP64  
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:  
500.perlbench_r: -W1, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2)  
-xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -fno-strict-overflow  
-mbranches-within-32B-boundaries  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin  
-lqkmalloc

502.gcc_r -m32  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/ia32_lin  
-std=gnu89  
-W1, -plugin-opt=-x86-branches-within-32B-boundaries  
-W1, -z, muldefs -fprofile-generate(pass 1)  
-fprofile-use=default.profdump(pass 2) -xCORE-AVX512 -flto  
-Ofast(pass 1) -O3 -ffast-math -qnextgen -fuse-ld=gold  
-qopt-mem-layout-trans=4 -L/usr/local/jemalloc32-5.0.1/lib  
-ljemalloc

505.mcf_r: basepeak = yes

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Peak Optimization Flags (Continued)

525.x264_r: -m64 -qnextgen -std=c11
-W1,-plugin-opt=-x86-branches-within-32B-boundaries
-W1,-z,muldefs -xCORE-AVX512 -f Santo -O3 -ffast-math
-fuse-ld=gold -qopt-mem-layout-trans=4 -fno-alias
-tp/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: -W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-tp/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:
520.omnetpp_r: basepeak = yes
523.xalancbmk_r: basepeak = yes
531.deepsjeng_r: basepeak = yes
541.leela_r: basepeak = yes

Fortran benchmarks:
548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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