**SPEC CPU® 2017 Integer Speed Result**

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
DIT400TR-55RL  
(2.10 GHz, Intel Xeon Gold 5218R)

**CPU2017 License:** 006042  
**Test Date:** Oct-2020

**Test Sponsor:** Netweb Pte Ltd  
**Hardware Availability:** Aug-2020  
**Tested by:** Tyrone Systems  
**Software Availability:** Jun-2020

<table>
<thead>
<tr>
<th>Thread</th>
<th>SPECspeed®2017_int_base = 11.2</th>
<th>SPECspeed®2017_int_peak = 11.5</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s 80</td>
<td>SPECspeed®2017_int_base (11.2)</td>
<td>SPECspeed®2017_int_peak (11.5)</td>
</tr>
<tr>
<td>602.gcc_s 80</td>
<td></td>
<td></td>
</tr>
<tr>
<td>605.mcf_s 80</td>
<td></td>
<td></td>
</tr>
<tr>
<td>620.omnetpp_s 80</td>
<td></td>
<td></td>
</tr>
<tr>
<td>623.xalancbmk_s 80</td>
<td></td>
<td></td>
</tr>
<tr>
<td>625.x264_s 80</td>
<td></td>
<td></td>
</tr>
<tr>
<td>631.deepsjeng_s 80</td>
<td></td>
<td></td>
</tr>
<tr>
<td>641.leela_s 80</td>
<td></td>
<td></td>
</tr>
<tr>
<td>648.exchange2_s 80</td>
<td></td>
<td></td>
</tr>
<tr>
<td>657.xz_s 80</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon Gold 5218R  
- **Max MHz:** 4000  
- **Nominal:** 2100  
- **Enabled:** 40 cores, 2 chips, 2 threads/core  
- **Orderable:** 1.2 (chip)  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 27.5 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933P-R)  
- **Storage:** 1 x 480 GB SATA SSD  
- **Other:** None

**Software**

- **OS:** CentOS Linux release 8.2.2004 (Core)  
- **Compiler:** C/C++: Version 19.1.1.217 of Intel C/C++ Compiler Compiler Build 20200306 for Linux; Fortran: Version 19.1.1.217 of Intel Fortran Compiler Build 20200306 for Linux  
- **Parallel:** Yes  
- **Firmware:** Version V8.102 released Jun-2020  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** Default
Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

DIT400TR-55RL
(2.10 GHz, Intel Xeon Gold 5218R)

SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>80</td>
<td>261</td>
<td>6.80</td>
<td>261</td>
<td>6.80</td>
<td>260</td>
<td>6.82</td>
<td>80</td>
<td>222</td>
<td>8.01</td>
<td>221</td>
<td>8.01</td>
<td>223</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>80</td>
<td>390</td>
<td>10.2</td>
<td>383</td>
<td>10.4</td>
<td>383</td>
<td>10.4</td>
<td>80</td>
<td>368</td>
<td>10.8</td>
<td>363</td>
<td>11.0</td>
<td>363</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>80</td>
<td>254</td>
<td>18.6</td>
<td>255</td>
<td>18.5</td>
<td>250</td>
<td>18.9</td>
<td>80</td>
<td>254</td>
<td>18.6</td>
<td>255</td>
<td>18.5</td>
<td>250</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>80</td>
<td>102</td>
<td>13.9</td>
<td>103</td>
<td>13.8</td>
<td>102</td>
<td>13.9</td>
<td>80</td>
<td>102</td>
<td>13.9</td>
<td>103</td>
<td>13.8</td>
<td>102</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>80</td>
<td>110</td>
<td>16.0</td>
<td>110</td>
<td>16.1</td>
<td>110</td>
<td>16.1</td>
<td>80</td>
<td>106</td>
<td>16.6</td>
<td>107</td>
<td>16.6</td>
<td>106</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>80</td>
<td>244</td>
<td>5.88</td>
<td>243</td>
<td>5.89</td>
<td>243</td>
<td>5.89</td>
<td>80</td>
<td>244</td>
<td>5.88</td>
<td>243</td>
<td>5.89</td>
<td>243</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>80</td>
<td>350</td>
<td>4.88</td>
<td>349</td>
<td>4.89</td>
<td>349</td>
<td>4.88</td>
<td>80</td>
<td>350</td>
<td>4.88</td>
<td>349</td>
<td>4.89</td>
<td>349</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>80</td>
<td>175</td>
<td>16.8</td>
<td>175</td>
<td>16.8</td>
<td>175</td>
<td>16.8</td>
<td>80</td>
<td>175</td>
<td>16.8</td>
<td>175</td>
<td>16.8</td>
<td>175</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>80</td>
<td>263</td>
<td>23.5</td>
<td>261</td>
<td>23.7</td>
<td>261</td>
<td>23.7</td>
<td>80</td>
<td>263</td>
<td>23.5</td>
<td>261</td>
<td>23.7</td>
<td>261</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler. The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=finer,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon 4214R CPU + 384GB RAM memory using Centos 8.2 x86_64
Transparent Huge Pages enabled by default
Prior to runcpu invocation Filesistem page cache synced and cleared with:
```
 sync; echo 3> /proc/sys/vm/drop_caches
```
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
DIT400TR-55RL
(2.10 GHz, Intel Xeon Gold 5218R)

SPECspeed®2017_int_base = 11.2
SPECspeed®2017_int_peak = 11.5

General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7ed4be6e46a485a0011
running on localhost.localdomain Sat Oct 10 14:44:56 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 5218R CPU @ 2.10GHz
  2 "physical id"s (chips)
  80 "processors"
core(s), siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 20
  siblings : 40
physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

From lscpu:

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 80
On-line CPU(s) list: 0-79
Thread(s) per core: 2
Core(s) per socket: 20
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5218R CPU @ 2.10GHz
Stepping: 7

(Continued on next page)
SPEC CPU®2017 Integer Speed Result
Copyright 2017-2020 Standard Performance Evaluation Corporation

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
DIT400TR-55RL
(2.10 GHz, Intel Xeon Gold 5218R)

SPECspeed®2017_int_base = 11.2
SPECspeed®2017_int_peak = 11.5

<table>
<thead>
<tr>
<th>CPU2017 License: 006042</th>
<th>Test Date: Oct-2020</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Netweb Pte Ltd</td>
<td>Hardware Availability: Aug-2020</td>
</tr>
<tr>
<td>Tested by: Tyrone Systems</td>
<td>Software Availability: Jun-2020</td>
</tr>
</tbody>
</table>

Platform Notes (Continued)

CPU MHz: 3692.745
CPU max MHz: 4000.0000
CPU min MHz: 800.0000
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 28160K
NUMA node0 CPU(s): 0-19,40-59
NUMA node1 CPU(s): 20-39,60-79

Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pclid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abal�m abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_pinn ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rdms rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsaves xsavec xgetbv1 xsaveopt cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req pku ospke avx512_vnni md_clear flush_l1d arch_capabilities

/proc/cpuinfo cache data
cache size : 28160 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59
node 0 size: 192103 MB
node 0 free: 159234 MB

node 1 cpus: 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39
node 1 size: 193499 MB
node 1 free: 168465 MB

distance:
node 0 1
0: 10 21
1: 21 10

From /proc/meminfo
MemTotal: 394857704 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

(Continued on next page)
**SPEC CPU**®2017 Integer Speed Result

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
DIT400TR-55RL  
(2.10 GHz, Intel Xeon Gold 5218R)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base = 11.2</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_int_peak = 11.5</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 006042  
**Test Sponsor:** Netweb Pte Ltd  
**Tested by:** Tyrone Systems

<table>
<thead>
<tr>
<th>Test Date:</th>
<th>Oct-2020</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Aug-2020</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Jun-2020</td>
</tr>
</tbody>
</table>

---

### Platform Notes (Continued)

From `/etc/*release*` /etc/*version*

- centos-release: CentOS Linux release 8.2.2004 (Core)
- centos-release-upstream: Derived from Red Hat Enterprise Linux 8.2 (Source)
- os-release:
  - NAME="CentOS Linux"
  - VERSION="8 (Core)"
  - ID="centos"
  - ID_LIKE="rhel fedora"
  - VERSION_ID="8"
  - PLATFORM_ID="platform:el8"
  - PRETTY_NAME="CentOS Linux 8 (Core)"
  - ANSI_COLOR="0;31"
- redhat-release: CentOS Linux release 8.2.2004 (Core)
- system-release: CentOS Linux release 8.2.2004 (Core)
- system-release-cpe: cpe:/o:centos:centos:8

**uname -a:**

Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri May 8 10:59:10 UTC 2020 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

- itlb_multihit:  
  - KVM: Mitigation: Split huge pages
- CVE-2018-3620 (L1 Terminal Fault): Not affected
- Microarchitectural Data Sampling: Not affected
- CVE-2017-5754 (Meltdown): Not affected
- CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- CVE-2017-5753 (Spectre variant 1):  
  - Mitigation: usercopy/swapgs barriers and __user pointer sanitization
- CVE-2017-5715 (Spectre variant 2):  
  - Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
- tsx_async_abort:  
  - Mitigation: Clear CPU buffers; SMT vulnerable

**run-level 3 Oct 9 02:24**

SPEC is set to: /home/cpu2017

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/mapper/cl-home</td>
<td>xfs</td>
<td>392G</td>
<td>109G</td>
<td>284G</td>
<td>28%</td>
<td>/home</td>
</tr>
</tbody>
</table>

From /sys/devices/virtual/dmi/id

- BIOS: American Megatrends Inc. V8.102 06/09/2020
- Vendor: Tyrone Systems
- Product: TP12XH-L2I
- Product Family: empty
- Serial: empty

(Continued on next page)
Platform Notes (Continued)

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
12x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)       |
|         | 625.x264_s(base, peak) 657.xz_s(base, peak)                                |
==============================================================================

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
| C       | 600.perlbench_s(peak)                                                  |
==============================================================================

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
| C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)       |
|         | 625.x264_s(base, peak) 657.xz_s(base, peak)                                |
==============================================================================

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
| C       | 600.perlbench_s(peak)                                                  |
==============================================================================

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
DIT400TR-55RL
(2.10 GHz, Intel Xeon Gold 5218R)

| SPECspeed®2017_int_base = 11.2 |
| SPECspeed®2017_int_peak = 11.5 |

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

---

**Compiler Version Notes (Continued)**

```c++
C++
| 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)
| 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
```

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

```fortran
Fortran
| 648.exchange2_s(base, peak)
```

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

**Base Compiler Invocation**

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Fortran benchmarks:
```
ifort
```

---

**Base Portability Flags**

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```
SPEC CPU®2017 Integer Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
DIT400TR-55RL
(2.10 GHz, Intel Xeon Gold 5218R)

SPECspeed®2017_int_base = 11.2
SPECspeed®2017_int_peak = 11.5

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

Test Date: Oct-2020
Hardware Availability: Aug-2020
Software Availability: Jun-2020

Base Optimization Flags

C benchmarks:
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-fuse-ld=gold -qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse
-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -xCORE-AVX512
-O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-mbranches-within-32B-boundaries

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64(*) -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
DIT400TR-55RL
(2.10 GHz, Intel Xeon Gold 5218R)

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

SPECspeed®2017_int_base = 11.2
SPECspeed®2017_int_peak = 11.5

Test Date: Oct-2020
Hardware Availability: Aug-2020
Software Availability: Jun-2020

Peak Portability Flags (Continued)

648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

(*) Indicates a portability flag that was found in a non-portability variable.

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -m64 -qnextgen -std=c11 -fuse-ld=gold
-Wl,-plugin-opt-=x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -m64 -qnextgen -std=c11
-Wl,-plugin-opt-=x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math
-fuse-1d=gold -qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Tyrone Systems  
(Test Sponsor: Netweb Pte Ltd)  
DIT400TR-55RL  
(2.10 GHz, Intel Xeon Gold 5218R)  

SPECspeed®2017_int_base = 11.2  
SPECspeed®2017_int_peak = 11.5

CPU2017 License: 006042  
Test Sponsor: Netweb Pte Ltd  
Tested by: Tyrone Systems  
Test Date: Oct-2020  
Hardware Availability: Aug-2020  
Software Availability: Jun-2020

648.exchange2_s: basepeak = yes

Peak Optimization Flags (Continued)

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/TyroneIT-Platform-Settings-V1-CLX-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/TyroneIT-Platform-Settings-V1-CLX-revA.xml

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-10-10 14:44:56-0400.  
Originally published on 2020-10-27.