**Inspur Corporation**

**Inspur NF8260M5 (Intel Xeon Platinum 8260)**

<table>
<thead>
<tr>
<th><strong>Thread</strong></th>
<th><strong>SPECspeed®2017_int_base</strong></th>
<th><strong>SPECspeed®2017_int_peak</strong></th>
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<td>6.77</td>
<td>7.1</td>
</tr>
<tr>
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<td>9.48</td>
<td>9.96</td>
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<td>605.mcf_s</td>
<td>9.25</td>
<td>16.6</td>
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<td>620.omnetpp_s</td>
<td>13.2</td>
<td>15.8</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>5.64</td>
<td>16.4</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>4.80</td>
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<td></td>
</tr>
<tr>
<td>657.xz_s</td>
<td>4.80</td>
<td></td>
</tr>
</tbody>
</table>

**Software**

- **OS:** Red Hat Enterprise Linux release 8.1 (Ootpa) 4.18.0-147.el8.x86_64
- **Compiler:** C/C++: Version 19.1.1.217 of Intel C/C++ Compiler Build 20200306 for Linux; Fortran: Version 19.1.1.217 of Intel Fortran Compiler Build 20200306 for Linux
- **Parallel:** Yes
- **Firmware:** Version 4.1.8 released Jun-2019
- **File System:** xfs
- **System State:** Run level 5 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.

**Hardware**

- **CPU Name:** Intel Xeon Platinum 8260
- **Max MHz:** 3900
- **Nominal:** 2400
- **Enabled:** 96 cores, 4 chips
- **Orderable:** 2.4 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 35.75 MB I+D on chip per chip
- **Memory:** 1536 GB (48 x 32 GB 2Rx4 PC4-2933Y-R)
- **Storage:** 1 x 1 TB SATA SSD
- **Other:** None

**SPEC CPU®2017 Integer Speed Result**

Copyright 2017-2020 Standard Performance Evaluation Corporation
## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Base</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Fallback</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Ratio</th>
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</tbody>
</table>

**Specspeed®2017 int_base = 10.8**  
**Specspeed®2017 int_peak = 11.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler. The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
SCALING_GOVERNOR set to Performance

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP_AFFINITY = "granularity=fine,scatter"  
LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/je5.0.1-64"  
MALLOC_CONF = "retain:true"  
OMP_STACKSIZE = "192M"
SPEC CPU®2017 Integer Speed Result

Inspur Corporation

Inspur NF8260M5 (Intel Xeon Platinum 8260)

<table>
<thead>
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<th>SPECspeed®2017_int_base = 10.8</th>
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</tr>
</tbody>
</table>

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
  numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5,
and the system compiler gcc 4.8.5;
sources available from jemalloc.net or

Platform Notes

BIOS configuration:
ENERGY_PERF_BIAS_CFG mode set to Performance
Hardware Prefetch set to Disable
VT Support set to Disable
C1E Support set to Disable
IMC (Integrated memory controller) Interleaving set to 1-way
Sub NUMA Cluster (SNC) set to Enable
Intel Hyper Threading Technology set to Disable

Sysinfo program /home/CPU2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f88a3d7edbble6e46a485a0011
running on localhost.localdomain Fri Jun 22 07:15:33 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
  https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Platinum 8260 CPU @ 2.40GHz
  4 "physical id"'s (chips)
  96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following

(Continued on next page)
## SPEC CPU®2017 Integer Speed Result

**Inspur Corporation**

**Inspur NF8260M5 (Intel Xeon Platinum 8260)**

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</table>

**CPU2017 License:** 3358  
**Test Sponsor:** Inspur Corporation  
**Tested by:** Inspur Corporation

---

**Platform Notes (Continued)**

```
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 24
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
```

From `lscpu`:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95
Thread(s) per core: 1
Core(s) per socket: 24
Socket(s): 4
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Platinum 8260 CPU @ 2.40GHz
Stepping: 7
CPU MHz: 999.457
CPU max MHz: 3900.0000
CPU min MHz: 1000.0000
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0-23
NUMA node1 CPU(s): 24-47
NUMA node2 CPU(s): 48-71
NUMA node3 CPU(s): 72-95
Flags:
```
<p>| | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
</tr>
</thead>
</table>
| fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xptr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_c3 cdp_c0 l1d_c3 l1d_c0 festivities pni pt flags 16-lbit imm属于 intel p5 power mgx swlt_x swlt_x以外的Other Misc flags:
```

---

(Continued on next page)
Inspur Corporation

Inspur NF8260M5 (Intel Xeon Platinum 8260)

**SPEC CPU®2017 Integer Speed Result**

**SPECspeed®2017_int_base = 10.8**

**SPECspeed®2017_int_peak = 11.0**

CPU2017 License: 3358
Test Date: Sep-2020
Test Sponsor: Inspur Corporation
Hardware Availability: Apr-2019
Tested by: Inspur Corporation
Software Availability: Apr-2020

Platform Notes (Continued)

ospke avx512_vnni md_clear flush_l1d arch_capabilities

```
/proc/cpuinfo cache data
    cache size : 36608 KB
From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
    physical chip.
    available: 4 nodes (0-3)
        node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
        node 0 size: 385605 MB
        node 0 free: 385379 MB
        node 1 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47
        node 1 size: 387066 MB
        node 1 free: 386707 MB
        node 2 cpus: 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71
        node 2 size: 387066 MB
        node 2 free: 386877 MB
        node 3 cpus: 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95
        node 3 size: 387066 MB
        node 3 free: 386621 MB
    node distances:
        node 0 1 2 3
            0: 10 21 21 21
            1: 21 10 21 21
            2: 21 21 10 21
            3: 21 21 21 10
```

From /proc/meminfo

```
MemTotal:       1583928912 kB
HugePages_Total:       0
Hugepagesize:       2048 kB
```

From /etc/*release* /etc/*version*

```
os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.1 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.1"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.1 (Ootpa)"
    ANSI_COLOR="0;31"
    redhat-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
    system-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:8.1:ga
```

uname -a:

(Continued on next page)
Inspur Corporation

Inspur NF8260M5 (Intel Xeon Platinum 8260)

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

SPECspeed®2017_int_base = 10.8
SPECspeed®2017_int_peak = 11.0

Test Date: Sep-2020
Hardware Availability: Apr-2019
Software Availability: Apr-2020

Platform Notes (Continued)

Linux localhost.localdomain 4.18.0-147.el8.x86_64 #1 SMP Thu Sep 26 15:52:44 UTC 2019
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 5 Jun 22 07:11

SPEC is set to: /home/CPU2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 838G 126G 713G 15% /home

From /sys/devices/virtual/dmi/id
BIOS: American Megatrends Inc. 4.1.8 06/11/2019
Vendor: Inspur
Product: NF8260M5
Serial: 220714936

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
  48x Samsung M393A4G43AB3-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) |
|         | 625.x264_s(base, peak) 657.xz_s(base, peak)                  |
==============================================================================

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

(Continued on next page)
Compiler Version Notes (Continued)

==============================================================================
C       | 600.perlbench_s(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
------------------------------------------------------------------------------
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1 NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C       | 600.perlbench_s(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C++      | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1 NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
Fortran  | 648.exchange2_s(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
**SPEC CPU®2017 Integer Speed Result**

**Inspur Corporation**

**Inspur NF8260M5 (Intel Xeon Platinum 8260)**

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**CPU2017 License:** 3358  
**Test Sponsor:** Inspur Corporation  
**Tested by:** Inspur Corporation

**Test Date:** Sep-2020  
**Hardware Availability:** Apr-2019  
**Software Availability:** Apr-2020

---

**Base Compiler Invocation**

- C benchmarks: `icc`
- C++ benchmarks: `icpc`
- Fortran benchmarks: `ifort`

---

**Base Portability Flags**

- `600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64`
- `602.gcc_s: -DSPEC_LP64`
- `605.mcf_s: -DSPEC_LP64`
- `620.omnetpp_s: -DSPEC_LP64`
- `623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX`
- `625.x264_s: -DSPEC_LP64`
- `631.deepsjeng_s: -DSPEC_LP64`
- `641.leela_s: -DSPEC_LP64`
- `648.exchange2_s: -DSPEC_LP64`
- `657.xz_s: -DSPEC_LP64`

---

**Base Optimization Flags**

- C benchmarks:
  - `-m64` `-qnextgen` `-std=c11`
  - `-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs`
  - `-xCORE-AVX512 -O3 -ffast-math -ftlo -mfpmath=sse -funroll-loops`
  - `-fuse-ld=gold -qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP`
  - `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

- C++ benchmarks:
  - `-m64` `-qnextgen` `-Wl,-plugin-opt=-x86-branches-within-32B-boundaries`
  - `-Wl,-z,muldefs` `-xCORE-AVX512 -O3 -ffast-math -ftlo -mfpmath=sse`
  - `-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4`
  - `-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin`
  - `-lqkmalloc`

- Fortran benchmarks:
  - `-m64` `-Wl,-plugin-opt=-x86-branches-within-32B-boundaries` `-xCORE-AVX512`
  - `-O3 -ipo -no-prec-div -qopt-mem-layout-trans=4`
  - `-nostandard-realloc-lhs -align array32byte`

(Continued on next page)
Inspur Corporation

Inspur NF8260M5 (Intel Xeon Platinum 8260)

SPECspeed®2017_int_base = 10.8
SPECspeed®2017_int_peak = 11.0

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Test Date: Sep-2020
Tested by: Inspur Corporation
Hardware Availability: Apr-2019
Software Availability: Apr-2020

Base Optimization Flags (Continued)

Fortran benchmarks (continued):
-mbranches-within-32B-boundaries

Peak Compiler Invocation

C benchmarks:
icc
C++ benchmarks:
icpc
Fortran benchmarks:
ifort

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64(*) -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

(*) Indicates a portability flag that was found in a non-portability variable.

Peak Optimization Flags

C benchmarks:
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

(Continued on next page)
Inspur Corporation

Inspur NF8260M5 (Intel Xeon Platinum 8260)

Peak Optimization Flags (Continued)

602.gcc_s: -m64 -qnextgen -std=c11 -fuse-ld=gold
-W1,-plugin-opt=-x86-branches-within-32B-boundaries
-W1,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -gopt-mem-layout-trans=4
-\L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -m64 -qnextgen -std=c11
-W1,-plugin-opt=-x86-branches-within-32B-boundaries
-W1,-z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math
-fuse-ld=gold -gopt-mem-layout-trans=4 -fno-alias
-\L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V1.9.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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