## SPEC CPU®2017 Integer Speed Result

**Inspur Corporation**

**Inspur NE5260M5 (Intel Xeon Gold 5220)**

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base = 8.01</th>
<th>SPECspeed®2017_int_peak = 8.20</th>
</tr>
</thead>
</table>

**CPU2017 License:** 3358  
**Test Date:** Nov-2020  
**Test Sponsor:** Inspur Corporation  
**Hardware Availability:** Apr-2019  
**Tested by:** Inspur Corporation  
**Software Availability:** Apr-2020

### Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench</td>
<td>36</td>
</tr>
<tr>
<td>gcc</td>
<td>36</td>
</tr>
<tr>
<td>mcf</td>
<td>36</td>
</tr>
<tr>
<td>omnetpp</td>
<td>36</td>
</tr>
<tr>
<td>xalang</td>
<td>36</td>
</tr>
<tr>
<td>x264</td>
<td>36</td>
</tr>
<tr>
<td>deepsjeng</td>
<td>36</td>
</tr>
<tr>
<td>leela</td>
<td>36</td>
</tr>
<tr>
<td>exchange2</td>
<td>36</td>
</tr>
<tr>
<td>xz</td>
<td>36</td>
</tr>
</tbody>
</table>

### SPECspeed®2017_int_base (8.01)  
### SPECspeed®2017_int_peak (8.20)

### Hardware

- **CPU Name:** Intel Xeon Gold 5220  
- **Max MHz:** 3900  
- **Nominal:** 2200  
- **Enabled:** 36 cores, 2 chips  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 24.75 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 192 GB (12 x 16 GB 2Rx4 PC4-2933Y-R, running at 2666)  
- **Storage:** 1 x 4 TB SATA, 7200 RPM  
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux release 8.2 (Ootpa)  
- **Compiler:** C/C++: Version 19.1.1.217 of Intel C/C++ Compiler Build 20200306 for Linux; Fortran: Version 19.1.1.217 of Intel Fortran Compiler Build 20200306 for Linux  
- **Parallel:** Yes  
- **Firmware:** Version 2.12.0 released Mar-2020  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
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---

**SPECspeed®2017_int_base = 8.01**

**SPECspeed®2017_int_peak = 8.20**

---

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>600.perlbench_s</td>
<td>36</td>
<td>375</td>
<td>4.74</td>
<td>376</td>
<td>4.71</td>
<td><strong>376</strong></td>
<td><strong>4.72</strong></td>
<td>36</td>
<td>324</td>
<td>5.48</td>
<td>321</td>
<td>5.53</td>
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<td>602.gcc_s</td>
<td>36</td>
<td><strong>519</strong></td>
<td><strong>7.68</strong></td>
<td>517</td>
<td>7.70</td>
<td>520</td>
<td>7.66</td>
<td>36</td>
<td>498</td>
<td>8.00</td>
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<tr>
<td>605.mcf_s</td>
<td>36</td>
<td><strong>338</strong></td>
<td><strong>14.0</strong></td>
<td>338</td>
<td>13.9</td>
<td>336</td>
<td>14.1</td>
<td>36</td>
<td><strong>338</strong></td>
<td><strong>14.0</strong></td>
<td>338</td>
<td>13.9</td>
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<tr>
<td>620.omnetpp_s</td>
<td>36</td>
<td>219</td>
<td>7.45</td>
<td>225</td>
<td>7.26</td>
<td><strong>222</strong></td>
<td><strong>7.36</strong></td>
<td>36</td>
<td>219</td>
<td>7.45</td>
<td>225</td>
<td>7.26</td>
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<tr>
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<td>36</td>
<td>159</td>
<td>11.1</td>
<td>159</td>
<td>11.1</td>
<td><strong>159</strong></td>
<td><strong>11.1</strong></td>
<td>36</td>
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<td><strong>11.5</strong></td>
<td>154</td>
<td>11.5</td>
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<tr>
<td>641.leea_s</td>
<td>36</td>
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<td>3.32</td>
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<td><strong>3.31</strong></td>
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<td>3.31</td>
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<td>3.32</td>
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<tr>
<td>648.exchange2_s</td>
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<td>11.4</td>
<td><strong>258</strong></td>
<td><strong>11.4</strong></td>
<td>257</td>
<td>11.4</td>
<td>36</td>
<td>258</td>
<td>11.4</td>
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<td><strong>11.4</strong></td>
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<td>657.xz_s</td>
<td>36</td>
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<td>17.7</td>
<td><strong>350</strong></td>
<td><strong>17.7</strong></td>
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<td>17.7</td>
<td>36</td>
<td>349</td>
<td>17.7</td>
<td><strong>350</strong></td>
<td><strong>17.7</strong></td>
</tr>
</tbody>
</table>

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**SPECspeed®2017_int_base = 8.01**

**SPECspeed®2017_int_peak = 8.20**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler.

The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

SCALING_GOVERNOR set to Performance

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

- KMP_AFFINITY = "granularity=fine,scatter"
- LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/je5.0.1-64"
- MALLOC_CONF = "retain:true"
- OMP_STACKSIZE = "192M"
**SPEC CPU® 2017 Integer Speed Result**

**Inspur Corporation**

Inspur NE5260M5 (Intel Xeon Gold 5220)

---

**SPECspeed® 2017_int_peak = 8.20**

**SPECspeed® 2017_int_base = 8.01**

---

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM  
memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
`sync; echo 3>/proc/sys/vm/drop_caches`  
runcpu command invoked through numactl i.e.:  
`numactl --interleave=all runcpu <etc>`

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5,  
and the system compiler gcc 4.8.5;  
sources available from jemalloc.net or  

---

### Platform Notes

BIOS configuration:  
ENERGY_PERF_BIAS_CFG mode set to Performance  
Hardware Prefetch set to Disable  
VT Support set to Disable  
C1E Support set to Disable  
IMC (Integrated memory controller) Interleaving set to 1-way  
Sub NUMA Cluster (SNC) set to Enable  
Intel Hyper Threading Technology set to Disable

Sysinfo program /home/CPUS2017/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7edbe6e46a485a0011  
running on localhost.localdomain Thu Nov 5 16:35:37 2020

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 5220 CPU @ 2.20GHz  
2 "physical id"s (chips)  
36 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following

(Continued on next page)
Inspur Corporation

Inspur NE5260M5 (Intel Xeon Gold 5220)

**CPU2017 License:** 3358
**Test Sponsor:** Inspur Corporation
**Tested by:** Inspur Corporation

**Test Date:** Nov-2020
**Hardware Availability:** Apr-2019
**Software Availability:** Apr-2020

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**Platform Notes (Continued)**

excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 18
siblings : 18
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27

From lscpu:
- **Architecture:** x86_64
- **CPU op-mode(s):** 32-bit, 64-bit
- **Byte Order:** Little Endian
- **CPU(s):** 36
- **On-line CPU(s) list:** 0-35
- **Thread(s) per core:** 1
- **Core(s) per socket:** 18
- **Socket(s):** 2
- **NUMA node(s):** 2
- **Vendor ID:** GenuineIntel
- **CPU family:** 6
- **Model:** 85
- **Model name:** Intel(R) Xeon(R) Gold 5220 CPU @ 2.20GHz
- **Stepping:** 7
- **CPU MHz:** 2697.624
- **CPU max MHz:** 3900.0000
- **CPU min MHz:** 1000.0000
- **BogoMIPS:** 4400.00
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 1024K
- **L3 cache:** 25344K
- **NUMA node0 CPU(s):** 0-17
- **NUMA node1 CPU(s):** 18-35

**Flags:**
- fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
- pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
- lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
- aperfmperf pni pclmulqdq dtes64 dtscpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
- pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
- rdrand lahf_lm abm 3dnowprefetch cpuid_fault ebp cat_l3 cdp_13 invpcid_single
- intel_pcin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vmcall flexpriority ept
- vpid fsgsb baseURL tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cmpx rrdt a
- avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
- xsaveopt xsavec xsaveopt xsaves cqm_xllc cqm_occusp_xllc cqm_mbm_total cqm_mbm_local
- dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req pku ospke avx512_vnni
- md_clear flush_lld arch_capabilities

/proc/cpuinfo cache data
- cache size : 25344 KB

(Continued on next page)
Platform Notes (Continued)

From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17
node 0 size: 95315 MB
node 0 free: 94518 MB
node 1 cpus: 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35
node 1 size: 96763 MB
node 1 free: 95819 MB
node distances:
node 0 1
0:  10  21
1:  21  10

From /proc/meminfo
MemTotal:       196688284 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*

os-release:
  NAME="Red Hat Enterprise Linux"
  VERSION="8.2 (Ootpa)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="8.2"
  PLATFORM_ID="platform:el8"
  PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
  ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

itlb_multihit:          KVM: Mitigation: Split huge pages
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user
Inspur Corporation

Inspur NE5260M5 (Intel Xeon Gold 5220)

SPEC CPU®2017 Integer Speed Result

SPECspeed®2017_int_base = 8.01
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Platform Notes (Continued)

CVE-2017-5715 (Spectre variant 2):
pointer sanitization
Mitigation: Enhanced IBRS, IBPB: conditional,
RSB filling
Mitigation: Clear CPU buffers; SMT disabled

tsx_async_abort:

run-level 3 Nov 5 16:33

SPEC is set to: /home/CPU2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 3.5T 75G 3.4T 3% /home

From /sys/devices/virtual/dmi/id
BIOS: Inspur 2.12.0 03/02/2020
Vendor: Inspur
Product: NE5260M5
Product Family: Not specified
Serial: 220658095

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SM BIOS" standard.

Memory:
12x Hynix HMA82GR7JJR8N-WM 16 GB 2 rank 2933
4x NO DIMM NO DIMM

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
| 625.x264_s(base, peak) 657.xz_s(base, peak)
==============================================================================

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C       | 600.perlbench_s(peak)
==============================================================================

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

(Continued on next page)
Compiler Version Notes (Continued)

==============================================================================
C    | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 
    | 625.x264_s(base, peak) 657.xz_s(base, peak) 
-----------------------------------------------------------------------------
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1 
   NextGen Build 20200304 
Copyright (C) 1985-2020 Intel Corporation. All rights reserved. 
------------------------------------------------------------------------------

==============================================================================
C    | 600.perlbench_s(peak) 
-----------------------------------------------------------------------------
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1 
   NextGen Build 20200304 
Copyright (C) 1985-2020 Intel Corporation. All rights reserved. 
------------------------------------------------------------------------------

==============================================================================
C++  | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 
     | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak) 
-----------------------------------------------------------------------------
Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1 
   NextGen Build 20200304 
Copyright (C) 1985-2020 Intel Corporation. All rights reserved. 
------------------------------------------------------------------------------

==============================================================================
Fortran | 648.exchange2_s(base, peak) 
-----------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2019.1.1.217 Build 20200306 
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks: 
   icc

C++ benchmarks: 
   icpc

Fortran benchmarks: 
   ifort
**SPEC CPU®2017 Integer Speed Result**

Inspur Corporation

Inspur NE5260M5 (Intel Xeon Gold 5220)

| SPECspeed®2017_int_base = 8.01 |
| SPECspeed®2017_int_peak = 8.20 |

**CPU2017 License:** 3358  
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**Software Availability:** Apr-2020

### Base Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

### Base Optimization Flags

**C benchmarks:**

```
-m64 -qnxtgen -std=c11
-WL,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-fuse-ld=gold -qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

**C++ benchmarks:**

```
-m64 -qnxtgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse
-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc
```

**Fortran benchmarks:**

```
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -xCORE-AVX512
-O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-mbranches-within-32B-boundaries
```

### Peak Compiler Invocation

**C benchmarks:**

```
icc
```

**C++ benchmarks:**

```
icpc
```

(Continued on next page)
# SPEC CPU®2017 Integer Speed Result

## Inspec Corporation

**Inspur NE5260M5 (Intel Xeon Gold 5220)**

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**SPECspeed®2017_int_base = 8.01**

**SPECspeed®2017_int_peak = 8.20**

---

**Peak Compiler Invocation (Continued)**

Fortran benchmarks:

ifort

---

### Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64  
602.gcc_s: -DSPEC_LP64(*) -DSPEC_LP64  
605.mcf_s: -DSPEC_LP64  
620.omnetpp_s: -DSPEC_LP64  
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX  
625.x264_s: -DSPEC_LP64  
631.deepsjeng_s: -DSPEC_LP64  
641.leela_s: -DSPEC_LP64  
648.exchange2_s: -DSPEC_LP64  
657.xz_s: -DSPEC_LP64

(*) Indicates a portability flag that was found in a non-portability variable.

---

### Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)  
-xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -fno-strict-overflow  
-mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -qnextgen -std=c11 -fuse-ld=gold  
-Wl, -plugin-opt=-x86-branches-within-32B-boundaries  
-Wl, -z,muldefs -fprofile-generate(pass 1)  
-fprofile-use=default.profiledata(pass 2) -xCORE-AVX512 -flto  
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -m64 -qnextgen -std=c11  
-Wl, -plugin-opt=-x86-branches-within-32B-boundaries  
-Wl, -z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math  
-fuse-ld=gold -qopt-mem-layout-trans=4 -fno-alias  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

(Continued on next page)
Inspur Corporation

Inspur NE5260M5 (Intel Xeon Gold 5220)

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CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Test Date: Nov-2020
Hardware Availability: Apr-2019
Software Availability: Apr-2020

Peak Optimization Flags (Continued)

657.xz_s: basepeak = yes

C++ benchmarks:
620.omnetpp_s: basepeak = yes
623.xalancbmk_s: basepeak = yes
631.deepsjeng_s: basepeak = yes
641.leela_s: basepeak = yes

Fortran benchmarks:
648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V1.9.xml

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