## SPEC CPU®2017 Integer Speed Result

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
**Tyrone Camarero DS400TR-212R4**  
(3.00 GHz, Intel Xeon Gold 6248R)

<table>
<thead>
<tr>
<th>Test Sponsor: Netweb Pte Ltd</th>
<th>Hardware Availability: Dec-2020</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tested by: Tyrone Systems</td>
<td>Software Availability: Dec-2020</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 006042  
**Test Date:** Jan-2021

### SPECspeed®2017_int_base = 10.2  
SPECspeed®2017_int_peak = 10.5

### Threads

<table>
<thead>
<tr>
<th>Test</th>
<th>Threads</th>
<th>SPECspeed®2017_int_base (10.2)</th>
<th>SPECspeed®2017_int_peak (10.5)</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>96</td>
<td></td>
<td></td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>96</td>
<td></td>
<td></td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>96</td>
<td></td>
<td></td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>96</td>
<td></td>
<td></td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>96</td>
<td></td>
<td></td>
</tr>
<tr>
<td>625.x264_s</td>
<td>96</td>
<td></td>
<td></td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>96</td>
<td></td>
<td></td>
</tr>
<tr>
<td>641.leela_s</td>
<td>96</td>
<td></td>
<td></td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>96</td>
<td></td>
<td></td>
</tr>
<tr>
<td>657.xz_s</td>
<td>96</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 6248R  
- **Max MHz:** 4000  
- **Nominal:** 3000
- **Enabled:** 48 cores, 2 chips, 2 threads/core  
- **Orderable:** 1.2 (chip)s
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 35.75 MB I+D on chip per chip
- **Other:** None  
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)  
- **Storage:** 1 x 480 GB SATA SSD  
- **Other:** None

### Software

- **OS:** CentOS Linux release 8.3.2011  
  4.18.0-240.el8.x86_64
- **Compiler:** C/C++: Version 19.1.1.217 of Intel C/C++ Compiler for Linux Build 20200306;  
  Fortran: Version 19.1.1.217 of Intel Fortran Compiler for Linux Build 20200306;
- **Parallel:** Yes
- **Firmware:** Version 3.4 released Oct-2020
- **File System:** xfs
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage
SPEC CPU® 2017 Integer Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero DS400TR-212R4
(3.00 GHz, Intel Xeon Gold 6248R)

Copyright 2017-2021 Standard Performance Evaluation Corporation

Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

CPU2017 License: 006042

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>96</td>
<td>286</td>
<td>6.20</td>
<td>286</td>
<td>6.21</td>
<td>286</td>
<td>6.20</td>
<td>96</td>
<td>245</td>
<td>7.25</td>
<td>243</td>
<td>7.32</td>
<td>245</td>
<td>7.25</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>96</td>
<td>414</td>
<td>9.63</td>
<td>415</td>
<td>9.60</td>
<td>414</td>
<td>9.61</td>
<td>96</td>
<td>396</td>
<td>10.1</td>
<td>397</td>
<td>10.0</td>
<td>397</td>
<td>10.0</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>96</td>
<td>268</td>
<td>17.6</td>
<td>269</td>
<td>17.6</td>
<td>269</td>
<td>17.6</td>
<td>96</td>
<td>268</td>
<td>17.6</td>
<td>269</td>
<td>17.6</td>
<td>269</td>
<td>17.6</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>96</td>
<td>213</td>
<td>7.66</td>
<td>211</td>
<td>7.64</td>
<td>209</td>
<td>7.60</td>
<td>96</td>
<td>213</td>
<td>7.66</td>
<td>211</td>
<td>7.64</td>
<td>209</td>
<td>7.60</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>96</td>
<td>113</td>
<td>12.6</td>
<td>113</td>
<td>12.6</td>
<td>112</td>
<td>12.6</td>
<td>96</td>
<td>113</td>
<td>12.6</td>
<td>113</td>
<td>12.6</td>
<td>112</td>
<td>12.6</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>96</td>
<td>121</td>
<td>14.5</td>
<td>123</td>
<td>14.4</td>
<td>125</td>
<td>14.1</td>
<td>96</td>
<td>119</td>
<td>14.8</td>
<td>120</td>
<td>14.7</td>
<td>120</td>
<td>14.7</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>96</td>
<td>267</td>
<td>5.38</td>
<td>267</td>
<td>5.37</td>
<td>266</td>
<td>5.38</td>
<td>96</td>
<td>267</td>
<td>5.38</td>
<td>267</td>
<td>5.37</td>
<td>266</td>
<td>5.38</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>96</td>
<td>386</td>
<td>4.42</td>
<td>386</td>
<td>4.42</td>
<td>386</td>
<td>4.42</td>
<td>96</td>
<td>386</td>
<td>4.42</td>
<td>386</td>
<td>4.42</td>
<td>386</td>
<td>4.42</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>96</td>
<td>194</td>
<td>15.2</td>
<td>193</td>
<td>15.2</td>
<td>193</td>
<td>15.2</td>
<td>96</td>
<td>194</td>
<td>15.2</td>
<td>193</td>
<td>15.2</td>
<td>193</td>
<td>15.2</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>96</td>
<td>264</td>
<td>23.4</td>
<td>264</td>
<td>23.4</td>
<td>262</td>
<td>23.6</td>
<td>96</td>
<td>264</td>
<td>23.4</td>
<td>264</td>
<td>23.4</td>
<td>262</td>
<td>23.6</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes
The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler.
The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux
The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOCS_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes
Binaries compiled on a system with 2x Intel Cascade Lake CPU + 384GB RAM memory using Centos 8.2 x86_64
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches

(Continued on next page)
**SPEC CPU®2017 Integer Speed Result**

**Tyrone Systems**
(Test Sponsor: Netweb Pte Ltd)

**Tyrone Camarero DS400TR-212R4**
(3.00 GHz, Intel Xeon Gold 6248R)

---

**General Notes (Continued)**

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation built with the Centos 8.2 x86_64, and the system compiler gcc 8.3.1 sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases

---

**Platform Notes**

BIOS Settings:
Power Technology = Custom
Power Performance Tuning = BIOS Controls EPB
ENERGY_PERF_BIAS_CFG mode = Extreme Performance
SNC = Enable
Stale AtoS = Disable
IMC Interleaving = 1-way Interleave
Patrol Scrub = Disable

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on spec Tue Jan 26 20:19:23 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 6248R CPU @ 3.00GHz
  2 "physical id"s (chips)
    96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 48
  physical 0: cores 0 1 2 3 4 5 6 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
  physical 1: cores 0 1 2 3 4 5 6 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95
```

(Continued on next page)
Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

Tyrone Camarero DS400TR-212R4
(3.00 GHz, Intel Xeon Gold 6248R)

SPECspeed®2017_int_base = 10.2
SPECspeed®2017_int_peak = 10.5

Tyrone Systems
(3.00 GHz, Intel Xeon Gold 6248R)

SPEC2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

Thread(s) per core: 2
Core(s) per socket: 24
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel (R) Xeon (R) Gold 6248R CPU @ 3.00GHz
Stepping: 7
CPU MHz: 3600.002
CPU max MHz: 4000.0000
CPU min MHz: 1200.0000
BogoMIPS: 6000.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0-3,7,8,12-14,18-20,48-51,55,56,60-62,66-68
NUMA node1 CPU(s): 4-6,9-11,15-17,21-23,52-54,57-59,63-65,69-71
NUMA node2 CPU(s): 24-27,31,32,36-38,42-44,72-75,79,80,84-86,90-92
NUMA node3 CPU(s): 28-30,33-35,39-41,45-47,76-78,81-83,87-89,93-95
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good ntopa tsc_deadline_timer aces xsave avx16c
rdx rsi rdi r8 r9 r10 r11 r12 r13 r14 r15 si di ox0 ox1 td te ne np
pni pclmulqdq dtes64 aes f16c lahf_lm abm 3nowprefetch cpuid_fault epb cat_13 cmp capable
intel_pinn ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle avx2
smep bmi2 erms invpcid cqm mpx rdt_a avx512f avx512dq rsidead adidas cflushopt
clwb intel_pt avx512cd avx512f cvx512vl xsaves cqm_llc cqm_mmb_total cqm_mmb_local dtherm ida arat pln pts pku ospke
avx512_vnni md_clear flush_l1d arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 7 8 12 13 14 18 19 20 48 49 50 51 55 56 60 61 62 66 67 68
node 0 size: 90049 MB
node 0 free: 78568 MB
node 1 cpus: 4 5 6 9 10 11 15 16 17 21 22 23 52 53 54 57 58 59 63 64 65 69 70 71
node 1 size: 91864 MB
node 1 free: 82073 MB
node 2 cpus: 24 25 26 27 31 32 36 37 38 42 43 44 72 73 74 75 79 80 84 85 86 90 91 92

(Continued on next page)
**SPEC CPU®2017 Integer Speed Result**

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)

Tyrone Camarero DS400TR-212R4  
(3.00 GHz, Intel Xeon Gold 6248R)

**SPECspeed®2017_int_base = 10.2**  
**SPECspeed®2017_int_peak = 10.5**

<table>
<thead>
<tr>
<th>CPU2017 License: 006042</th>
<th>Test Date: Jan-2021</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Netweb Pte Ltd</td>
<td>Hardware Availability: Dec-2020</td>
</tr>
<tr>
<td>Tested by: Tyrone Systems</td>
<td>Software Availability: Dec-2020</td>
</tr>
</tbody>
</table>

**Platform Notes (Continued)**

- node 2 size: 92318 MB
- node 2 free: 82006 MB
- node 3 cpus: 28 29 30 33 34 35 39 40 41 45 46 47 76 77 78 81 82 83 87 88 89 93 94 95
- node 3 size: 92051 MB
- node 3 free: 82054 MB
- node distances:
  - node 0 1 2 3
  - 0: 10 11 21 21
  - 1: 11 10 21 21
  - 2: 21 21 10 11
  - 3: 21 21 11 10

From /proc/meminfo
- MemTotal: 394855232 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

/sbin/tuned-adm active
- Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
- centos-release: CentOS Linux release 8.3.2011
- centos-release-upstream: Derived from Red Hat Enterprise Linux 8.3
- os-release:
  - NAME="CentOS Linux"
  - VERSION="8"
  - ID="centos"
  - ID_LIKE="rhel fedora"
  - VERSION_ID="8"
  - PLATFORM_ID="platform:el8"
  - PRETTY_NAME="CentOS Linux 8"
  - ANSI_COLOR="0;31"
- redhat-release: CentOS Linux release 8.3.2011
- system-release: CentOS Linux release 8.3.2011
- system-release-cpe: cpe:/o:centos:centos:8

uname -a:
- Linux spec 4.18.0-240.el8.x86_64 #1 SMP Fri Sep 25 19:48:47 UTC 2020 x86_64 x86_64
- x86_64 GNU/Linux

Kernel self-reported vulnerability status:

- CVE-2018-12207 (iTLB Multihit): KVM: Mitigation: Split huge pages
- CVE-2018-3620 (L1 Terminal Fault): Not affected

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

Tyrone Camarero DS400TR-212R4
(3.00 GHz, Intel Xeon Gold 6248R)

SPECspeed®2017_int_base = 10.2
SPECspeed®2017_int_peak = 10.5

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

Platform Notes (Continued)

Microarchitectural Data Sampling:
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass):
CVE-2017-5753 (Spectre variant 1):
CVE-2017-5715 (Spectre variant 2):
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort):

run-level 3 Jan 25 14:25

SPEC is set to: /home/cpu2017

Filesystem          Type  Size  Used Avail Use% Mounted on
/dev/mapper/cl-home xfs   372G  155G  218G  42% /home

From /sys/devices/virtual/dmi/id
Vendor:         Tyrone Systems
Product:        Tyrone Camarero DS400E1
Serial:         S263875X9527668

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
12x NO DIMM NO DIMM
12x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933, configured at 2934

BIOS:
BIOS Vendor: American Megatrends Inc.
BIOS Version: 3.4
BIOS Date: 10/30/2020
BIOS Revision: 5.14

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
| 625.x264_s(base, peak) 657.xz_s(base, peak)
==============================================================================

(Continued on next page)
Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero DS400TR-212R4
(3.00 GHz,Intel Xeon Gold 6248R)

 SPECscore®2017_int_base = 10.2
 SPECscore®2017_int_peak = 10.5

Compiler Version Notes (Continued)

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C       | 600.perlbench_s(peak)
==============================================================================

Intel(R) C Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
       | 625.x264_s(base, peak) 657.xz_s(base, peak)
==============================================================================

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C       | 600.perlbench_s(peak)
==============================================================================

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)
       | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
==============================================================================

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
Fortran | 648.exchange2_s(base, peak)
==============================================================================

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
SPEC CPU®2017 Integer Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero DS400TR-212R4
(3.00 GHz, Intel Xeon Gold 6248R)

SPECspeed®2017_int_base = 10.2
SPECspeed®2017_int_peak = 10.5

<table>
<thead>
<tr>
<th>CPU2017 License: 006042</th>
<th>Test Date: Jan-2021</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Netweb Pte Ltd</td>
<td>Hardware Availability: Dec-2020</td>
</tr>
<tr>
<td>Tested by: Tyrone Systems</td>
<td>Software Availability: Dec-2020</td>
</tr>
</tbody>
</table>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -qnextgen -std=c11
-Wl, -plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ffast-math -fito -mfpmath=sse -funroll-loops
-fuse-ld=gold -qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-m64 -qnextgen -Wl, -plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -fito -mfpmath=sse
-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -xCORE-AVX512
-O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero DS400TR-212R4
(3.00 GHz, Intel Xeon Gold 6248R)

SPECspeed®2017_int_base = 10.2
SPECspeed®2017_int_peak = 10.5

Base Optimization Flags (Continued)

Fortran benchmarks (continued):
-mbranches-within-32B-boundaries

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64(*) -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

(*) Indicates a portability flag that was found in a non-portability variable.

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -03 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/je5.0.1-64/lib -ljemalloc

(Continued on next page)
Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero DS400TR-212R4
(3.00 GHz, Intel Xeon Gold 6248R)

SPECspeed®2017_int_base = 10.2
SPECspeed®2017_int_peak = 10.5

Peak Optimization Flags (Continued)

602.gcc_s: -m64 -qnextgen -std=c11 -fuse-ld=gold
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -gopt-mem-layout-trans=4
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math
-fuse-ld=gold -gopt-mem-layout-trans=4 -fno-alias
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes
623.xalancbmk_s: basepeak = yes
631.deepsjeng_s: basepeak = yes
641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-CLX-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-CLX-revB.xml

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.5 on 2021-01-26 09:49:23-0500.
Report generated on 2021-02-16 16:26:17 by CPU2017 PDF formatter v6255.
Originally published on 2021-02-16.