**SPEC CPU®2017 Integer Rate Result**

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)

Tyrone Camarero QS400TU-224R4  
(2.40 GHz, Intel Xeon Gold 6148)

---

**SPECrate®2017_int_base = 454**  
**SPECrate®2017_int_peak = 470**

---

**CPU2017 License:** 006042  
**Test Date:** Feb-2021  
**Test Sponsor:** Netweb Pte Ltd  
**Hardware Availability:** Aug-2020  
**Tested by:** Tyrone Systems  
**Software Availability:** Dec-2020

---

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
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<tbody>
<tr>
<td>perlbench_r</td>
<td>160</td>
<td>376</td>
<td>321</td>
</tr>
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<td>gcc_r</td>
<td>160</td>
<td>332</td>
<td>339</td>
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<tr>
<td>mcf_r</td>
<td>160</td>
<td>747</td>
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<tr>
<td>omnetpp_r</td>
<td>160</td>
<td>593</td>
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</tr>
<tr>
<td>xalancbmk_r</td>
<td>160</td>
<td>259</td>
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</tr>
<tr>
<td>x264_r</td>
<td>160</td>
<td>953</td>
<td>953</td>
</tr>
<tr>
<td>xalancbmk_r</td>
<td>160</td>
<td>273</td>
<td>273</td>
</tr>
</tbody>
</table>

---

**Software**

**OS:** CentOS Linux release 8.3.2011  
**Kernel:** 4.18.0-240.el8.x86_64

**Compiler:**  
C/C++: Version 19.1.2.254 of Intel C/C++  
Compiler Build 20200623 for Linux;

**Fortran:** Version 19.1.2.254 of Intel Fortran  
Compiler Build 20200623 for Linux

**Parallel:** No

**Firmware:** Version 3.4 released Nov-2020

**File System:** xfs

**System State:** Run level 3 (multi-user)

**Base Pointers:** 64-bit

**Peak Pointers:** 32/64-bit

**Other:** jemalloc memory allocator V5.0.1

**Power Management:** BIOS set to prefer performance at the cost of additional power usage.
Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
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CPU2017 License: 006042
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Results Table

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<thead>
<tr>
<th>Benchmark</th>
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<th>Ratio</th>
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<tr>
<td>500.perlbench_r</td>
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<td>794</td>
<td>321</td>
<td>794</td>
<td>321</td>
<td>797</td>
<td>320</td>
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<tr>
<td>502.gcc_r</td>
<td>160</td>
<td>681</td>
<td>332</td>
<td>690</td>
<td>329</td>
<td>676</td>
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<tr>
<td>505.mcf_r</td>
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<td>343</td>
<td>755</td>
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<tr>
<td>520.omnetpp_r</td>
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<td>810</td>
<td>259</td>
<td>812</td>
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<td>959</td>
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<td>354</td>
<td>729</td>
<td>363</td>
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<tr>
<td>548.exchange2_r</td>
<td>160</td>
<td>470</td>
<td>893</td>
<td>469</td>
<td>893</td>
<td>471</td>
<td>891</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>160</td>
<td>634</td>
<td>273</td>
<td>633</td>
<td>273</td>
<td>633</td>
<td>273</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes
The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler.
The correct version of C/C++ compiler is: Version 19.1.2.254 Build 20200623 Compiler for Linux
The correct version of Fortran compiler is: Version 19.1.2.254 Build 20200623 Compiler for Linux

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = 
"/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"
MALLOC_CONF = "retain:true"
**SPEC CPU®2017 Integer Rate Result**

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
Tyrone Camarero QS400TU-224R4  
(2.40 GHz, Intel Xeon Gold 6148)

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**CPU2017 License:** 006042  
**Test Date:** Feb-2021

**Test Sponsor:** Netweb Pte Ltd  
**Hardware Availability:** Aug-2020

**Tested by:** Tyrone Systems  
**Software Availability:** Dec-2020

---

**General Notes**

Binaries compiled on a system with 2x Intel Cascade Lake CPU 4214R + 384 GB RAM memory using Centos 8.2 x86_64  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  

---

**Platform Notes**

**BIOS Settings:**  
Power Technology = Custom  
Power Performance Tuning = BIOS Controls EPB  
ENERGY_PERF_BIAS_CFG mode = Extreme Performance  
SNC = Enable  
Stale AtoS = Disable  
IMC Interleaving = 1-way Interleave  
Patrol Scrub = Disable

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d438e2f1c  
running on localhost.localdomain Fri Feb 19 11:33:41 2021

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 6148 CPU @ 2.40GHz  
4 "physical id"s (chips)  
160 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 20  
siblings : 40  
physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

(Continued on next page)
Platform Notes (Continued)

physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 2: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 3: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 160
On-line CPU(s) list: 0-159
Thread(s) per core: 2
Core(s) per socket: 20
Socket(s): 4
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6148 CPU @ 2.40GHz
Stepping: 4
CPU MHz: 3398.091
CPU max MHz: 3700.0000
CPU min MHz: 1000.0000
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 32K
L1l cache: 32K
L2 cache: 1024K
L3 cache: 28160K
NUMA node0 CPU(s): 0-19, 80-99
NUMA node1 CPU(s): 20-39, 100-119
NUMA node2 CPU(s): 40-59, 120-139
NUMA node3 CPU(s): 60-79, 140-159
Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx fnrew rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single pti intel_pni ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mxm pdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occcll cqm_mbm_total cqm_mbb_local dtherm ida arat pln pts pkub ospke md_clear flush_l1d

/cacheinfo cache data
    cache size: 28160 KB

(Continued on next page)
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SPECrate®2017_int_base = 454
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CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems
Test Date: Feb-2021
Hardware Availability: Aug-2020
Software Availability: Dec-2020

Platform Notes (Continued)

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 80 81 82 83 84 85 86 87
88 89 90 91 92 93 94 95 96 97 98 99
node 0 size: 90855 MB
node 0 free: 89019 MB
node 1 cpus: 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 100 101 102
103 104 105 106 107 108 109 110 111 112 113 114 115 116 117 118 119
node 1 size: 92768 MB
node 1 free: 94043 MB
node 2 cpus: 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 120 121 122
123 124 125 126 127 128 129 130 131 132 133 134 135 136 137 138 139
node 2 size: 93101 MB
node 2 free: 95294 MB
node 3 cpus: 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 140 141 142
143 144 145 146 147 148 149 150 151 152 153 154 155 156 157 158 159
node 3 size: 92128 MB
node 3 free: 96297 MB
node distances:
node 0 1 2 3
0: 10 21 21 21
1: 21 10 21 21
2: 21 21 10 21
3: 21 21 21 10

From /proc/meminfo
MemTotal: 394580636 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
/sbin/tuned-adm active
Current active profile: throughput-performance
/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
centos-release: CentOS Linux release 8.3.2011
centos-release-upstream: Derived from Red Hat Enterprise Linux 8.3
os-release:
NAME="CentOS Linux"
VERSION="8"
ID="centos"
ID_LIKE="rhel fedora"
VERSION_ID="8"
PLATFORM_ID="platform:el8"

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

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Platform Notes (Continued)

PRETTY_NAME="CentOS Linux 8"
ANSI_COLOR="0;31"
redhat-release: CentOS Linux release 8.3.2011
system-release: CentOS Linux release 8.3.2011
system-release-cpe: cpe:/o:centos:centos:8

uname -a:
    Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Fri Sep 25 19:48:47 UTC 2020
    x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): KVM: Mitigation: Split huge pages
CVE-2018-3620 (L1 Terminal Fault): Mitigation: PTE Inversion; VMX: conditional cache flushes, SMT vulnerable

Microarchitectural Data Sampling:

CVE-2017-5754 (Meltdown):
CVE-2018-3639 (Speculative Store Bypass):
Mitigation: Speculative Store Bypass disabled via prctl and seccomp

CVE-2017-5753 (Spectre variant 1):
Mitigation: usercopy/swapgs barriers and __user pointer sanitation

CVE-2017-5715 (Spectre variant 2):
Mitigation: Full generic retpoline, IBPB: conditional, IBRS_FW, STIBP: conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort):
Mitigation: Clear CPU buffers; SMT vulnerable

run-level 3 Mar 19 16:16

SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/cl-home xfs 372G 6.9G 366G 2% /home

From /sys/devices/virtual/dmi/id
    Vendor: Tyrone Systems
    Product: Tyrone Camarero DS400TU-224R4
    Product Family: SMC X11
    Serial: 123456789

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow

(Continued on next page)
Platform Notes (Continued)

hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

24x NO DIMM NO DIMM
24x Samsung M393A2K40DB2-CVF 16 GB 1 rank 2933, configured at 2666

BIOS:

BIOS Vendor: American Megatrends Inc.
BIOS Version: 3.4
BIOS Date: 11/04/2020
BIOS Revision: 5.14

(End of data from sysinfo program)
Sysinfo incorrectly parsed dmidecode output. Configured memory speed is 2933.

Compiler Version Notes

==============================================================================
C       | 502.gcc_r(peak)
------------------------------------------------------------------------------
Intel(R) C Compiler for applications running on IA-32, Version 2021.1 NextGen
Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
icc (NextGen): command line warning #10006: ignoring unknown option
'-i_version=19.1.2.254' [-Woption-ignored]
------------------------------------------------------------------------------

==============================================================================
C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
| 525.x264_r(base, peak) 557.xz_r(base)
------------------------------------------------------------------------------
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
icc (NextGen): command line warning #10006: ignoring unknown option
'-i_version=19.1.2.254' [-Woption-ignored]
------------------------------------------------------------------------------

==============================================================================
C       | 500.perlbench_r(peak) 557.xz_r(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.2.254 Build 20200623
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

(Continued on next page)
SPEC CPU®2017 Integer Rate Result
Copyright 2017-2021 Standard Performance Evaluation Corporation

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Compiler Version Notes (Continued)

==============================================================================
C       | 502.gcc_r(peak)
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==============================================================================
C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
      | 525.x264_r(base, peak) 557.xz_r(base)
------------------------------------------------------------------------------
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
icc (NextGen): command line warning #10006: ignoring unknown option
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==============================================================================
C       | 500.perlbench_r(peak) 557.xz_r(peak)
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==============================================================================
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icc (NextGen): command line warning #10006: ignoring unknown option
  '-i_version=19.1.2.254' [-Woption-ignored]
------------------------------------------------------------------------------

==============================================================================
C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
      | 525.x264_r(base, peak) 557.xz_r(base)
------------------------------------------------------------------------------
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
icc (NextGen): command line warning #10006: ignoring unknown option

(Continued on next page)
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**Tested by:** Tyrone Systems  
**Hardware Availability:** Aug-2020  
**Software Availability:** Dec-2020

---

**Compiler Version Notes (Continued)**

```
'-i_version=19.1.2.254' [-Woption-ignored]
```

---

```
C   | 500.perlbench_r(peak) 557.xz_r(peak)
```

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.2.254 Build 20200623  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

```
C++  | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
```

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1 NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
icpc (NextGen): command line warning #10006: ignoring unknown option  
'-i_version=19.1.2.254' [-Woption-ignored]

---

```
Fortran | 548.exchange2_r(base, peak)
```

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.1.2.254 Build 20200623  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

**Base Compiler Invocation**

C benchmarks:  
```
icc
```

C++ benchmarks:  
```
icpc
```

Fortran benchmarks:  
```
ifort
```
SPEC CPU®2017 Integer Rate Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

Tyrone Camarero QS400TU-224R4
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Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.2.254/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.2.254/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.2.254/linux/compiler/lib/intel64_lin
-lqkmalloc

Peak Compiler Invocation

C benchmarks:
icc

(Continued on next page)
Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.2.254/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.2.254/linux/compiler/lib/ia32_lin
-std=gnu89
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qnextgen
-qopt-mem-layout-trans=4 -L/usr/local/je5.0.1-32/lib
-ljemalloc

505.mcf_r: basepeak = yes

(Continued on next page)
## SPEC CPU®2017 Integer Rate Result

### Tyrone Systems
*(Test Sponsor: Netweb Pte Ltd)*

**Tyrone Camarero QS400TU-224R4**
*(2.40 GHz, Intel Xeon Gold 6148)*

<table>
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<th>CPU2017 License: 006042</th>
<th>Test Date: Feb-2021</th>
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<td>Test Sponsor: Netweb Pte Ltd</td>
<td>Hardware Availability: Aug-2020</td>
</tr>
<tr>
<td>Tested by: Tyrone Systems</td>
<td>Software Availability: Dec-2020</td>
</tr>
</tbody>
</table>

**SPECrate®2017_int_base = 454**

**SPECrate®2017_int_peak = 470**

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### Peak Optimization Flags (Continued)

525.x264_r: `-m64 -qnextgen -std=c11`  
`-Wl,-plugin-opt=-x86-branches-within-32B-boundaries`  
`-Wl,-z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math`  
`-qopt-mem-layout-trans=4 -fno-alias`  
`-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.2.254/linux/compiler/lib/intel64_lin`  
`-lqkmalloc`  

557.xz_r: `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`  
`-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries`  
`-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.2.254/linux/compiler/lib/intel64_lin`  
`-lqkmalloc`

### C++ benchmarks:

520.omnetpp_r: `basepeak = yes`

523.xalancbmk_r: `basepeak = yes`

531.deepsjeng_r: `basepeak = yes`

541.leela_r: `basepeak = yes`

### Fortran benchmarks:

548.exchange2_r: `basepeak = yes`

The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-CLX-revB.html](http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-CLX-revB.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-CLX-revB.xml](http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-CLX-revB.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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