SPEC CPU®2017 Integer Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero DS400E1U-224R4
(2.00 GHz, Intel Xeon Gold 5117)

**CPU2017 License:** 006042
**Test Sponsor:** Netweb Pte Ltd
**Tested by:** Tyrone Systems

---

### Hardware

- **CPU Name:** Intel Xeon Gold 5117
- **Max MHz:** 2800
- **Nominal:** 2000
- **Enabled:** 28 cores, 2 chips, 2 threads/core
- **Orderable:** 1.2 (chip)s
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 19.25 MB I+D on chip per chip
- **Other:** None
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R, running at 2400)
- **Storage:** 1 x 480 GB SATA SSD
- **Other:** None

### Software

- **OS:** CentOS Linux release 8.3.2011
  - Kernel 4.18.0-240.el8.x86_64
  - 4.18.0-240.el8.x86_64
- **Compiler:**
  - C/C++: Version 19.1.1.217 of Intel C/C++ Compiler for Linux Build 20200306;
  - Fortran: Version 19.1.1.217 of Intel Fortran Compiler for Linux Build 20200306;
- **Parallel:** Yes
- **Firmware:** Version 3.4 released Oct-2020
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage.

---

SPECspeed®2017_int_base = 7.99
SPECspeed®2017_int_peak = 8.19

---

**Test Date:** Feb-2021
**Hardware Availability:** Aug-2020
**Software Availability:** Dec-2020

---

Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Results</th>
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<tbody>
<tr>
<td>perlbench_s</td>
<td>600.48i</td>
</tr>
<tr>
<td>gcc_s</td>
<td>602.562</td>
</tr>
<tr>
<td>mcf_s</td>
<td>605.752</td>
</tr>
<tr>
<td>omnetpp_s</td>
<td>620.789</td>
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<tr>
<td>xalancbmk_s</td>
<td>623.139</td>
</tr>
<tr>
<td>x264_s</td>
<td>625.115</td>
</tr>
<tr>
<td>deepsjeng_s</td>
<td>631.11.9</td>
</tr>
<tr>
<td>leela_s</td>
<td>641.11.8</td>
</tr>
<tr>
<td>exchange2_s</td>
<td>648.17.1</td>
</tr>
<tr>
<td>xz_s</td>
<td>657.17.1</td>
</tr>
</tbody>
</table>

---

**SPECspeed®2017_int_base (7.99)**
**SPECspeed®2017_int_peak (8.19)**
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Seconds</th>
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<tr>
<td>600.perlbench_s</td>
<td>56</td>
<td>369</td>
<td>4.81</td>
<td>368</td>
<td>4.82</td>
<td>370</td>
<td>4.80</td>
<td>56</td>
<td>317</td>
<td>5.60</td>
<td>315</td>
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<tr>
<td>602.gcc_s</td>
<td>56</td>
<td>522</td>
<td>7.63</td>
<td>529</td>
<td>7.52</td>
<td>531</td>
<td>7.50</td>
<td>56</td>
<td>505</td>
<td>7.89</td>
<td>512</td>
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<tr>
<td>605.mcf_s</td>
<td>56</td>
<td>337</td>
<td>14.0</td>
<td>341</td>
<td>13.9</td>
<td><strong>339</strong></td>
<td><strong>13.9</strong></td>
<td>56</td>
<td>337</td>
<td>14.0</td>
<td>341</td>
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<tr>
<td>620.omnetpp_s</td>
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<td><strong>254</strong></td>
<td><strong>6.42</strong></td>
<td>253</td>
<td>6.45</td>
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<td>258</td>
<td>6.32</td>
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<td>9.79</td>
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<td>4.23</td>
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<td>4.22</td>
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<td>641.leela_s</td>
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<td>497</td>
<td>3.43</td>
<td>56</td>
<td>497</td>
<td>3.44</td>
<td>497</td>
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<tr>
<td>648.exchange2_s</td>
<td>56</td>
<td>250</td>
<td>11.8</td>
<td>249</td>
<td>11.8</td>
<td>253</td>
<td>11.6</td>
<td>56</td>
<td>250</td>
<td>11.8</td>
<td>249</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>56</td>
<td>360</td>
<td>17.1</td>
<td>360</td>
<td>17.2</td>
<td>364</td>
<td>17.0</td>
<td>56</td>
<td>360</td>
<td>17.1</td>
<td>360</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler.
The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux
The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"

### General Notes

Binaries compiled on a system with 2x Intel Cascade Lake CPU 4214R + 384GB RAM memory using Centos 8.2 x86_64
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
```
sync; echo 3> /proc/sys/vm/drop_caches
```

(Continued on next page)
General Notes (Continued)

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
Power Technology = Custom
Power Performance Tuning = BIOS Controls EPB
ENERGY_PERF_BIAS_CFG mode = Extreme Performance
SNC = Enable
Stale AtoS = Disable
IMC Interleaving = 1-way Interleave
Patrol Scrub = Disable

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c running on spec Sat Feb 27 10:23:21 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 5117 CPU @ 2.00GHz
  2 "physical id"s (chips)
  56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 14
  siblings : 28
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14

From lscpu:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian

(Continued on next page)
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SPECspeed®2017_int_base = 7.99
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Tested by: Tyrone Systems

Platform Notes (Continued)

CPU(s): 56
On-line CPU(s) list: 0-55
Thread(s) per core: 2
Core(s) per socket: 14
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5117 CPU @ 2.00GHz
Stepping: 4
CPU MHz: 2363.135
CPU max MHz: 2800.0000
CPU min MHz: 800.0000
BogoMIPS: 4000.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 19712K
NUMA node0 CPU(s): 0-13, 28-41
NUMA node1 CPU(s): 14-27, 42-55
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single pti intel_paging_ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority
epi vpid ad fs.gsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cmx
rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw
avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total

cache size: 19712 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.

node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 28 29 30 31 32 33 34 35 36 37 38 39 40 41
node 0 size: 183414 MB
node 0 free: 170492 MB
node 1 cpus: 14 15 16 17 18 19 20 21 22 23 24 25 26 27 42 43 44 45 46 47 48 49 50 51 52
53 54 55
node 1 size: 183603 MB
node 1 free: 173659 MB
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<tr>
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<tr>
<td>Tested by: Tyrone Systems</td>
<td>Software Availability: Dec-2020</td>
</tr>
</tbody>
</table>

Platform Notes (Continued)

node distances:
node 0 1
 0: 10 21
 1: 21 10

From /proc/meminfo
MemTotal: 394864892 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
  Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
  centos-release: CentOS Linux release 8.3.2011
  centos-release-upstream: Derived from Red Hat Enterprise Linux 8.3
  os-release:
    NAME="CentOS Linux"
    VERSION="8"
    ID="centos"
    ID_LIKE="rhel fedora"
    VERSION_ID="8"
    platform_id="platform:el8"
    PRETTY_NAME="CentOS Linux 8"
    ANSI_COLOR="0;31"
  redhat-release: CentOS Linux release 8.3.2011
  system-release: CentOS Linux release 8.3.2011
  system-release-cpe: cpe:/o:centos:centos:8

uname -a:
  Linux spec 4.18.0-240.el8.x86_64 #1 SMP Fri Sep 25 19:48:47 UTC 2020 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):
CVE-2018-3620 (L1 Terminal Fault):
Microarchitectural Data Sampling:
CVE-2017-5754 (Meltdown):
CVE-2018-3639 (Speculative Store Bypass):

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

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Platform Notes (Continued)

CVE-2017-5753 (Spectre variant 1):
Mitigation: usercopy/swapgs barriers and __user pointer sanitization

CVE-2017-5715 (Spectre variant 2):
Mitigation: Full generic retpoline, IBBP: conditional, IBRS_FW, STIBP: conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected

CVE-2019-11135 (TSX Asynchronous Abort):
Mitigation: Clear CPU buffers; SMT vulnerable

run-level 3 Feb 25 17:35

SPEC is set to: /home/cpu2017

From /sys/devices/virtual/dmi/id
Vendor: Tyrone Systems
Product: Tyrone Camarero DS400E1
Serial: S263875X9527668

Memory:
12x NO DIMM NO DIMM
12x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933, configured at 2400

BIOS:
BIOS Vendor: American Megatrends Inc.
BIOS Version: 3.4
BIOS Date: 10/30/2020
BIOS Revision: 5.14

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| C    | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)  
|      | 625.x264_s(base, peak) 657.xz_s(base, peak)  
==============================================================================

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1

(Continued on next page)
**SPEC CPU®2017 Integer Speed Result**

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
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<table>
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</tr>
</tbody>
</table>

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### Compiler Version Notes (Continued)

NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

```
C       | 600.perlbench_s(peak)
---
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

```
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)  
| 625.x264_s(base, peak) 657.xz_s(base, peak)
---
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

```
C       | 600.perlbench_s(peak)
---
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

```
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)  
| 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
---
Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

```
Fortran | 648.exchange2_s(base, peak)
---
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```
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---

## Base Compiler Invocation

- **C benchmarks:**
  - icc

- **C++ benchmarks:**
  - icpc

- **Fortran benchmarks:**
  - ifort

---

## Base Portability Flags

```shell
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

---

## Base Optimization Flags

**C benchmarks:**

- `-m64 -qnextgen -std=c11`
- `-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs`
- `-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops`
- `-fuse-ld=gold -qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP`
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

**C++ benchmarks:**

- `-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries`
- `-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse`
- `-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin`
- `-lqkmalloc`

**Fortran benchmarks:**

- `-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -xCORE-AVX512`
- `-O3 -ipo -no-prec-div -qopt-mem-layout-trans=4`
- `-nostandard-realloc-lhs -align array32byte`

(Continued on next page)
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Copyright 2017-2021 Standard Performance Evaluation Corporation

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Base Optimization Flags (Continued)

Fortran benchmarks (continued):
-mbranches-within-32B-boundaries

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64(*) -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

(*) Indicates a portability flag that was found in a non-portability variable.

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/je5.0.1-64/lib -ljemalloc

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Peak Optimization Flags (Continued)

602.gcc_s: -m64 -qnextgen -std=c11 -fuse-ld=gold
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -gopt-mem-layout-trans=4
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: basepeak = yes
625.x264_s: -m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math
-fuse-ld=gold -gopt-mem-layout-trans=4 -fno-alias
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:
620.omnetpp_s: basepeak = yes
623.xalancbhmk_s: basepeak = yes
631.deepsjeng_s: basepeak = yes
641.leela_s: basepeak = yes

Fortran benchmarks:
648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-CLX-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-CLX-revB.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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