## SPEC CPU®2017 Floating Point Speed Result

### Hardware

<table>
<thead>
<tr>
<th>Feature</th>
<th>Specification</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>CPU Name:</strong></td>
<td>Intel Xeon Silver 4210</td>
<td></td>
</tr>
<tr>
<td><strong>Max MHz:</strong></td>
<td>3200</td>
<td></td>
</tr>
<tr>
<td><strong>Nominal:</strong></td>
<td>2200</td>
<td></td>
</tr>
<tr>
<td><strong>Enabled:</strong></td>
<td>20 cores, 2 chips, 2 threads/core</td>
<td></td>
</tr>
<tr>
<td><strong>Orderable:</strong></td>
<td>1.2 (chip)s</td>
<td></td>
</tr>
<tr>
<td><strong>Cache L1:</strong></td>
<td>32 KB I + 32 KB D on chip per core</td>
<td></td>
</tr>
<tr>
<td><strong>L2:</strong></td>
<td>1 MB I+D on chip per core</td>
<td></td>
</tr>
<tr>
<td><strong>L3:</strong></td>
<td>13.75 MB I+D on chip per chip</td>
<td></td>
</tr>
<tr>
<td><strong>Memory:</strong></td>
<td>384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R, running at 2400)</td>
<td></td>
</tr>
<tr>
<td><strong>Storage:</strong></td>
<td>1 x 480 GB SATA SSD</td>
<td></td>
</tr>
<tr>
<td><strong>Other:</strong></td>
<td>None</td>
<td></td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Feature</th>
<th>Specification</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>OS:</strong></td>
<td>CentOS Linux release 8.3.2011</td>
<td></td>
</tr>
<tr>
<td><strong>Kernel:</strong></td>
<td>4.18.0-240.el8.x86_64</td>
<td></td>
</tr>
<tr>
<td><strong>Compiler:</strong></td>
<td>C/C++: Version 19.1.2.254 of Intel C/C++ Compiler for Linux Build 20200623; Fortran: Version 19.1.2.254 of Intel Fortran Compiler for Linux Build 20200623;</td>
<td></td>
</tr>
<tr>
<td><strong>Parallel:</strong></td>
<td>Yes</td>
<td></td>
</tr>
<tr>
<td><strong>Firmware:</strong></td>
<td>Version 3.4 released Nov-2020</td>
<td></td>
</tr>
<tr>
<td><strong>File System:</strong></td>
<td>xfs</td>
<td></td>
</tr>
<tr>
<td><strong>System State:</strong></td>
<td>Run level 3 (multi user)</td>
<td></td>
</tr>
<tr>
<td><strong>Base Pointers:</strong></td>
<td>64-bit</td>
<td></td>
</tr>
<tr>
<td><strong>Peak Pointers:</strong></td>
<td>64-bit</td>
<td></td>
</tr>
<tr>
<td><strong>Other:</strong></td>
<td>jemalloc memory allocator V5.0.1</td>
<td></td>
</tr>
<tr>
<td><strong>Power Management:</strong></td>
<td>BIOS set to prefer performance at the cost of additional power usage.</td>
<td></td>
</tr>
</tbody>
</table>
Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>20</td>
<td>168</td>
<td>351</td>
<td>168</td>
<td>351</td>
<td>168</td>
<td>351</td>
<td>168</td>
<td>351</td>
<td>168</td>
<td>351</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>20</td>
<td>185</td>
<td>90.1</td>
<td>182</td>
<td>91.5</td>
<td>191</td>
<td>87.5</td>
<td>185</td>
<td>90.1</td>
<td>182</td>
<td>91.5</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>20</td>
<td>80.4</td>
<td>65.1</td>
<td>81.3</td>
<td>64.5</td>
<td>80.6</td>
<td>65.0</td>
<td>80.4</td>
<td>65.1</td>
<td>81.3</td>
<td>64.5</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>20</td>
<td>178</td>
<td>74.3</td>
<td>180</td>
<td>73.5</td>
<td>179</td>
<td>73.9</td>
<td>169</td>
<td>78.0</td>
<td>170</td>
<td>77.6</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>20</td>
<td>190</td>
<td>46.6</td>
<td>190</td>
<td>46.7</td>
<td>190</td>
<td>46.6</td>
<td>190</td>
<td>46.7</td>
<td>190</td>
<td>46.6</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>20</td>
<td>215</td>
<td>55.3</td>
<td>213</td>
<td>55.7</td>
<td>214</td>
<td>55.5</td>
<td>215</td>
<td>55.3</td>
<td>213</td>
<td>55.7</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>20</td>
<td>347</td>
<td>41.5</td>
<td>346</td>
<td>41.6</td>
<td>346</td>
<td>41.7</td>
<td>347</td>
<td>41.5</td>
<td>346</td>
<td>41.6</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>20</td>
<td>161</td>
<td>109</td>
<td>161</td>
<td>109</td>
<td>161</td>
<td>109</td>
<td>135</td>
<td>129</td>
<td>136</td>
<td>129</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>20</td>
<td>137</td>
<td>66.5</td>
<td>136</td>
<td>67.1</td>
<td>137</td>
<td>66.3</td>
<td>135</td>
<td>65.6</td>
<td>137</td>
<td>66.4</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>20</td>
<td>218</td>
<td>72.2</td>
<td>217</td>
<td>72.7</td>
<td>218</td>
<td>72.3</td>
<td>218</td>
<td>72.2</td>
<td>217</td>
<td>72.7</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.6.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "128M"

General Notes

Binaries compiled on a system with 2x Intel Cascade Lake 4214R CPU + 384 GB RAM memory using Centos 8.2 x86_64
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Files system page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation built with the Centos 8.2 x86_64, and the system compiler gcc 4.8.5
General Notes (Continued)


Platform Notes

BIOS Settings:
- Power Technology = Custom
- Power Performance Tuning = BIOS Controls EPB
- ENERGY_PERF_BIAS_CFG mode = Maximum Performance
- SNC = Enable
- Stale AtoS = Disable
- IMC Interleaving = 1-way Interleave
- Patrol Scrub = Disable

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Sat Feb 27 05:13:23 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) Silver 4210 CPU @ 2.20GHz
  2 "physical id"s (chips)
  40 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 10
  siblings : 20
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 40
- On-line CPU(s) list: 0-39
- Thread(s) per core: 2
- Core(s) per socket: 10
- Socket(s): 2
- NUMA node(s): 2
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Silver 4210 CPU @ 2.20GHz

(Continued on next page)
\textbf{SPEC CPU®2017 Floating Point Speed Result}

\textbf{Tyrone Systems}  
(Test Sponsor: Netweb Pte Ltd)  
Tyrone Camarero DS400TN-55R  
(2.20 GHz, Intel Xeon Silver 4210)

\begin{table}[h]
\centering
\begin{tabular}{|l|l|}
\hline
CPU2017 License & 006042 \\
Test Sponsor & Netweb Pte Ltd \\
Tested by & Tyrone Systems \\
\hline
\end{tabular}
\end{table}

\begin{table}[h]
\centering
\begin{tabular}{|l|}
\hline
SPECspeed®2017_fp_base = 78.2 \\
SPECspeed®2017_fp_peak = 80.0 \\
\hline
\end{tabular}
\end{table}

\textbf{Platform Notes (Continued)}

Stepping: \hspace{1cm} 7  
CPU MHZ: \hspace{1cm} 1257.720  
CPU max MHZ: \hspace{1cm} 3200.0000  
CPU min MHZ: \hspace{1cm} 1000.0000  
BogoMIPS: \hspace{1cm} 4400.00  
Virtualization: \hspace{1cm} VT-x  
L1d cache: \hspace{1cm} 32K  
L1i cache: \hspace{1cm} 32K  
L2 cache: \hspace{1cm} 1024K  
L3 cache: \hspace{1cm} 14080K  
NUMA node0 CPU(s): \hspace{1cm} 0-9,20-29  
NUMA node1 CPU(s): \hspace{1cm} 10-19,30-39  
Flags: \hspace{1cm} fpu vme de pse msr mcr pse36 apic sep mtrr pge mca cmov 
\hspace{1cm} pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp 
\hspace{1cm} lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid 
\hspace{1cm} aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sse4_1 sse4_2 
\hspace{1cm} x2apic movbe popcnt tsc_deadline_timer aes xsave 
\hspace{1cm} avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 
\hspace{1cm} invpcid_single intel_ppcin ssbd mba ibrs ibpb ibrs_enhanced tpr_shadow vnumi 
\hspace{1cm} flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 etsm 
\hspace{1cm} invpcid cqm mpx rdrt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt 
\hspace{1cm} avx512cd avx512bw avx512v1 xsaveopt xsavevc xsaves cqm_llc cqm_occmp llc 
\hspace{1cm} cqm_mbb_total cqm_mbb_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear 
\hspace{1cm} flush_l1d arch_capabilities

\texttt{/proc/cpuinfo cache data}
\texttt{cache size : 14080 KB}

\texttt{From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a}
\texttt{physical chip.}
\texttt{available: 2 nodes (0-1)}
\texttt{node 0 cpus: 0 1 2 3 4 5 6 7 8 9 20 21 22 23 24 25 26 27 28 29}
\texttt{node 0 size: 185188 MB}
\texttt{node 0 free: 161834 MB}
\texttt{node 1 cpus: 10 11 12 13 14 15 16 17 18 19 30 31 32 33 34 35 36 37 38 39}
\texttt{node 1 size: 186668 MB}
\texttt{node 1 free: 178596 MB}
\texttt{node distances:}
\texttt{node 0 1}
\texttt{0: 10 21}
\texttt{1: 21 10}

\texttt{From /proc/meminfo}
\texttt{MemTotal: 394870792 kB}
\texttt{HugePages_Total: 0}
\texttt{Hugepagesize: 2048 kB}

\textit{(Continued on next page)}
SPEC CPU®2017 Floating Point Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero DS400TN-55R
(2.20 GHz, Intel Xeon Silver 4210)

SPECspeed®2017_fp_base = 78.2
SPECspeed®2017_fp_peak = 80.0

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

Test Date: Feb-2021
Hardware Availability: Aug-2020
Software Availability: Dec-2020

Platform Notes (Continued)

/sbin/tuned-adm active
  Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
  centos-release: CentOS Linux release 8.3.2011
  centos-release-upstream: Derived from Red Hat Enterprise Linux 8.3
  os-release:
    NAME="CentOS Linux"
    VERSION="8"
    ID="centos"
    ID_LIKE="rhel fedora"
    VERSION_ID="8"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="CentOS Linux 8"
    ANSI_COLOR="0;31"
  redhat-release: CentOS Linux release 8.3.2011
  system-release: CentOS Linux release 8.3.2011
  system-release-cpe: cpe:/o:centos:centos:8

uname -a:
  Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Fri Sep 25 19:48:47 UTC 2020
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):
  KVM: Mitigation: Split huge pages
CVE-2018-3620 (L1 Terminal Fault):
  Not affected
Microarchitectural Data Sampling:
  Not affected
CVE-2017-5754 (Meltdown):
  Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass):
CVE-2017-5753 (Spectre variant 1):
  Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):
  Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling):
CVE-2019-11135 (TSX Asynchronous Abort):
  Mitigation: TSX disabled

run-level 3 Feb 25 02:33

SPEC is set to: /home/cpu2017

Filesystem Type Size Used Avail Use% Mounted on

(Continued on next page)
## SPEC CPU®2017 Floating Point Speed Result

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
Tyrone Camarero DS400TN-55R  
(2.20 GHz, Intel Xeon Silver 4210)  

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base</th>
<th>78.2</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_fp_peak</td>
<td>80.0</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 006042  
**Test Sponsor:** Netweb Pte Ltd  
**Tested by:** Tyrone Systems  

### Platform Notes (Continued)

```
/devmapper/cl-home xfs  372G  41G  332G  11% /home
```

From /sys/devices/virtual/dmi/id
- Vendor: Tyrone Systems
- Product: X11DPi-N(T)
- Product Family: SMC X11
- Serial: 123456789

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

#### Memory:
- 4x NO DIMM NO DIMM
- 12x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933, configured at 2400

#### BIOS:
- BIOS Vendor: American Megatrends Inc.
- BIOS Version: 3.4
- BIOS Date: 11/23/2020
- BIOS Revision: 5.14

(End of data from sysinfo program)

### Compiler Version Notes

```
C        619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.2.254 Build 20200623  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

```
C++, C, Fortran  607.cactuBSSN_s(base, peak)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.2.254 Build 20200623  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

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**SPEC CPU®2017 Floating Point Speed Result**

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero DS400TN-55R
(2.20 GHz, Intel Xeon Silver 4210)

| SPECspeed®2017_fp_base = 78.2 | SPECspeed®2017_fp_peak = 80.0 |

**CPU2017 License:** 006042
**Test Sponsor:** Netweb Pte Ltd
**Tested by:** Tyrone Systems

**Compiler Version Notes (Continued)**

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)  
               | 654.roms_s(base, peak)    
-----------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 
64, Version 19.1.2.254 Build 20200623
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------
Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak)  
               | 628.pop2_s(base, peak)    
-----------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 
64, Version 19.1.2.254 Build 20200623
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------

**Base Compiler Invocation**

C benchmarks:

```bash
icc
```

Fortran benchmarks:

```bash
ifort
```

Benchmarks using both Fortran and C:

```bash
ifort icc
```

Benchmarks using Fortran, C, and C++:

```bash
icpc icc ifort
```

**Base Portability Flags**

- 603.bwaves_s: -DSPEC_LP64
- 607.cactuBSSN_s: -DSPEC_LP64
- 619.lbm_s: -DSPEC_LP64

(Continued on next page)
SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Tyrone Systems
(2.20 GHz, Intel Xeon Silver 4210)

SPECspeed®2017_fp_base = 78.2
SPECspeed®2017_fp_peak = 80.0

Base Portability Flags (Continued)

621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
   -assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
- std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries

Fortran benchmarks:
- std=c11 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
-mbranches-within-32B-boundaries -L/usr/local/je5.0.1-64/lib -ljemalloc

Benchmarks using both Fortran and C:
-std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-^L/usr/local/je5.0.1-64/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
-std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-^L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:
   icc

Fortran benchmarks:
   ifort

(Continued on next page)
Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
619.lbm_s: basepeak = yes
638.imagick_s: basepeak = yes
644.nab_s: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -gopenmp -DSPEC_OPENMP -mbranches-within-32B-boundaries -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
603.bwaves_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -gopenmp -nostandard-realloc-lhs -mbranches-within-32B-boundaries -L/usr/local/je5.0.1-64/lib -ljemalloc
649.fotonik3d_s: Same as 603.bwaves_s
654.roms_s: basepeak = yes

Benches using both Fortran and C:
621.wrf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
TYRONE SYSTEMS
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero DS400TN-55R
(2.20 GHz, Intel Xeon Silver 4210)

SPECspeed®2017_fp_base = 78.2
SPECspeed®2017_fp_peak = 80.0

CPU2017 License: 006042
Test Date: Feb-2021
Test Sponsor: Netweb Pte Ltd
Hardware Availability: Aug-2020
Tested by: Tyrone Systems
Software Availability: Dec-2020

Peak Optimization Flags (Continued)

621.wrf_s (continued):
-DSPEC SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/je5.0.1-64/lib -ljemalloc

627.cam4_s: basepeak = yes
628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:
607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-CLX-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-CLX-revB.xml