SPEC CPU®2017 Floating Point Speed Result

Dell Inc.
PowerEdge MX750c (Intel Xeon Platinum 8352Y, 2.20 GHz)

SPECspeed®2017_fp_base = 204
SPECspeed®2017_fp_peak = 206

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed®2017_fp_base (204)</th>
<th>SPECspeed®2017_fp_peak (206)</th>
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<tr>
<td>603.bwaves_s 64</td>
<td>712</td>
<td>715</td>
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<tr>
<td>607.cactuBSSN_s 64</td>
<td>247</td>
<td>715</td>
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<tr>
<td>619.libm_s 64</td>
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<tr>
<td>621.wrf_s 64</td>
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<tr>
<td>627.cam4_s 64</td>
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<tr>
<td>628.pop2_s 64</td>
<td>85.2</td>
<td>85.2</td>
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<tr>
<td>638.imagick_s 64</td>
<td>190</td>
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<tr>
<td>644.nab_s 64</td>
<td>380</td>
<td>380</td>
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<tr>
<td>649.fotonik3d_s 64</td>
<td>428</td>
<td>428</td>
</tr>
<tr>
<td>654.roms_s 64</td>
<td>269</td>
<td>269</td>
</tr>
</tbody>
</table>

Hardware
CPU Name: Intel Xeon Platinum 8352Y
Max MHz: 3400
Nominal: 2200
Enabled: 64 cores, 2 chips
Orderable: 1.2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1.25 MB I+D on chip per core
L3: 48 MB I+D on chip per chip
Other: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R)
Storage: 125 GB on tmpfs
Other: None

Software
OS: Red Hat Enterprise Linux 8.3 (Ootpa)
4.18.0-240.el8.x86_64
Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++
Compiler Build 20201113 for Linux;
Fortran: Version 2021.1 of Intel Fortran Compiler
Classic Build 20201112 for Linux;
C/C++: Version 2021.1 of Intel C/C++ Compiler
Classic Build 20201112 for Linux
Parallel: Yes
Firmware: Version 1.1.0 released Mar-2021
File System: tmpfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance
at the cost of additional power usage.
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Dell Inc.
PowerEdge MX750c (Intel Xeon Platinum 8352Y, 2.20 GHz)

SPECspeed®2017_fp_base = 204
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<td>64</td>
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<td>719</td>
<td>82.8</td>
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<td>247</td>
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<td>621.wrf_s</td>
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<td>60.5</td>
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<td>59.3</td>
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<td>628.pop2_s</td>
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<td>85.5</td>
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<td>85.2</td>
<td>139</td>
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<tr>
<td>638.imagick_s</td>
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<td>191</td>
<td>76.0</td>
<td>190</td>
<td>75.5</td>
<td>191</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>64</td>
<td>45.9</td>
<td>380</td>
<td>45.9</td>
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<tr>
<td>649.fotonik3d_s</td>
<td>64</td>
<td>81.9</td>
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<td>82.0</td>
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<td>81.9</td>
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</tr>
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SPECspeed®2017_fp_base = 204
SPECspeed®2017_fp_peak = 206

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-ic19.1u1/lib/intel64:/mnt/ramdisk/cpu2017-ic19.1u1/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Dell Inc.
PowerEdge MX750c (Intel Xeon Platinum 8352Y, 2.20 GHz)

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation,
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS Settings:
Logical Processor : Disabled
Virtualization Technology : Disabled

System Profile : Custom
CPU Power Management : Maximum Performance
C1E : Disabled
C States : Autonomous
Memory Patrol Scrub : Disabled
Energy Efficiency Policy : Performance
CPU Interconnect Bus Link
  Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-ic19.1u1/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edeb1e6e46a485a0011
running on localhost.localdomain Mon Mar 29 02:46:40 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

  model name : Intel(R) Xeon(R) Platinum 8352Y CPU @ 2.20GHz
  2 "physical id"s (chips)
    64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 32
  siblings : 32
  physical 0: cores 0 1 2 3 4 5 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
  physical 1: cores 0 1 2 3 4 5 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31

(Continued on next page)
Dell Inc.
PowerEdge MX750c (Intel Xeon Platinum 8352Y, 2.20 GHz)

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CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Software Availability: Mar-2021
Hardware Availability: Apr-2021

Test Date: Mar-2021

Platform Notes (Continued)

From lscpu:

- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 64
- On-line CPU(s) list: 0-63
- Thread(s) per core: 1
- Core(s) per socket: 32
- Socket(s): 2
- NUMA node(s): 2
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 106
- Model name: Intel(R) Xeon(R) Platinum 8352Y CPU @ 2.20GHz
- Stepping: 6
- CPU MHz: 2911.941
- BogoMIPS: 4400.00
- Virtualization: VT-x
- L1d cache: 48K
- L1i cache: 32K
- L2 cache: 1280K
- L3 cache: 49152K
- NUMA node0 CPU(s):
  - 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58
  - 60,62
- NUMA node1 CPU(s):
  - 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59
  - 61,63
- Flags: fpu vme de pse tsc msr mcr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdelgb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single intel_pinn sxbd mba ibrs ibpb stibp ibrs_enhanced fsbgbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavevc xgetbv1 xsaves cmqm_llc cmqm_occup_llc cmqm_mbm_total cmqm_mbm_local split_lock_detect wbinvd dtherm ida arat pln pts avx512vmbi umip pku ospe avx512_vmbi2 gfnl vaes vclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

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<tr>
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<td>= 206</td>
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CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

**Platform Notes (Continued)**

available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62
node 0 size: 243962 MB
node 0 free: 255813 MB
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47 49 51 53 55 57 59 61 63
node 1 size: 244014 MB
node 1 free: 232038 MB
node distances:
node 0 1
0: 10 20
1: 20 10

From /proc/meminfo
MemTotal: 527807184 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.3 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga
uname -a:
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

itlb_multihit: Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization

(Continued on next page)
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Dell Inc.

**SPECspeed®2017_fp_base = 204**

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**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

---

**Software Availability:** Mar-2021  
**Hardware Availability:** Apr-2021  
**Test Date:** Mar-2021

---

**Platform Notes (Continued)**

CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling  
srbds: Not affected  
tsx_async_abort: Not affected

run-level 3 Mar 29 00:01 last=5

SPEC is set to: /mnt/ramdisk/cpu2017-ic19.1u1  
Filesystem Type Size Used Avail Use% Mounted on  
tmpfs tmpfs 125G 16G 110G 13% /mnt/ramdisk

---

**Compiler Version Notes**

```
C                  619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

---

```
C                  644.nab_s(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

---

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### Compiler Version Notes (Continued)

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<tr>
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<td>Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000</td>
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Hardware Availability: Apr-2021  
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Compiler Version Notes (Continued)  

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
Intel (R) C Intel (R) 64 Compiler Classic for applications running on Intel (R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  

Base Compiler Invocation  

C benchmarks: 

icc  

Fortran benchmarks: 

ifort  

Benchmarks using both Fortran and C: 

ifort icc  

Benchmarks using Fortran, C, and C++: 

icpc icc ifort  

Base Portability Flags  

603.bwaves_s: -DSPEC_LP64  
607.cactuBSSN_s: -DSPEC_LP64  
619.lbm_s: -DSPEC_LP64  
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian  
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG  
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian  
-assume byterecl  
638.imagick_s: -DSPEC_LP64  
644.nab_s: -DSPEC_LP64  
649.fotonik3d_s: -DSPEC_LP64  
654.roms_s: -DSPEC_LP64  

Base Optimization Flags  

C benchmarks: 

-m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-pref-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP  
-mbranches-within-32B-boundaries  

(Continued on next page)
### Dell Inc.

**PowerEdge MX750c (Intel Xeon Platinum 8352Y, 2.20 GHz)**

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- **Software Availability:** Mar-2021

### Base Optimization Flags (Continued)

Fortran benchmarks:
- `-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -gopenmp -nostandard-realloc-lhs -mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

Benchmarks using both Fortran and C:

Benchmarks using Fortran, C, and C++:

### Peak Compiler Invocation

C benchmarks (except as noted below):
- `icc`
- `644.nab_s: icx`

Fortran benchmarks:
- `ifort`

Benchmarks using both Fortran and C:
- `ifort icc`

Benchmarks using Fortran, C, and C++:
- `icpc icc ifort`

### Peak Portability Flags

Same as Base Portability Flags
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Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes
638.imagick_s: basepeak = yes
644.nab_s: -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-fito -mfpmath=sse -funroll-loops -fiopenmp
-DSPEC_OPENMP -qopt-mem-layout-trans=4
-fimf-accuracy-bits=14:sqrt
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

603.bwaves_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX512
-o3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

649.fotonik3d_s: Same as 603.bwaves_s

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)
-prof-use(pass 2) -ipo -xCORE-AVX512 -03 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

627.cam4_s: basepeak = yes
628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes
## SPEC CPU®2017 Floating Point Speed Result

### Dell Inc.

**PowerEdge MX750c (Intel Xeon Platinum 8352Y, 2.20 GHz)**

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</tbody>
</table>

| CPU2017 License: 55 | Test Date: Mar-2021 |
| Test Sponsor: Dell Inc. | Hardware Availability: Apr-2021 |
| Tested by: Dell Inc. | Software Availability: Mar-2021 |

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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