### SPEC CPU®2017 Floating Point Speed Result

New H3C Technologies Co., Ltd.
H3C UniServer R6900 G5 (Intel Xeon Platinum 8376HL)

<table>
<thead>
<tr>
<th>Threads</th>
<th>Name</th>
<th>SPECspeed®2017_fp_base</th>
<th>SPECspeed®2017_fp_peak</th>
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<tr>
<td>112</td>
<td>603.bwaves_s</td>
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<td>112</td>
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**CPU2017 License:** 9066
**Test Date:** Apr-2021
**Test Sponsor:** New H3C Technologies Co., Ltd.
**Hardware Availability:** Sep-2020
**Tested by:** New H3C Technologies Co., Ltd.
**Software Availability:** Dec-2020

### Hardware

- **CPU Name:** Intel Xeon Platinum 8376HL
- **Max MHz:** 4300
- **Nominal:** 2600
- **Enabled:** 112 cores, 4 chips
- **Orderable:** 1,2,3,4 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 38.5 MB I+D on chip per chip
- **Memory:** 768 GB (48 x 16 GB 2Rx8 PC4-3200V-R)
- **Storage:** 1 x 1.0 TB SATA SSD
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux release 8.2 (Ootpa)
- **Compiler:** C/C++: Version 2021.1 of Intel oneAPI, DPC++/C++, Compiler Build 20201113 for Linux, Fortran: Version 2021.1 of Intel Fortran Compiler, Classic Build 20201112 for Linux
- **Parallel:** Yes
- **Firmware:** Version 5.15 released Mar-2021 BIOS
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage.
## New H3C Technologies Co., Ltd. H3C UniServer R6900 G5 (Intel Xeon Platinum 8376HL)

**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.

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</tr>
</tbody>
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**SPECspeed®2017_fp_base = 240**  
**SPECspeed®2017_fp_peak = 244**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit --s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP_AFFINITY = "granularity=fine,compact"  
LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"  
MALLOC_CONF = "retain:true"  
OMP_STACKSIZE = "192M"

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM  
memory using Redhat Enterprise Linux 8.0  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3>/proc/sys/vm/drop_caches  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

(Continued on next page)
General Notes (Continued)


Platform Notes

BIOS Settings:
Set Hyper-Threading to Disabled
Set Power Performance Tuning to BIOS Controls EPB
Set Energy Performance BIAS to Performance
Set Patrol Scrub to Disabled

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Tue Apr 20 14:42:44 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Platinum 8376HL CPU @ 2.60GHz
  4 "physical id"s (chips)
  112 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 28
siblings : 28
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
  28 29 30
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
  28 29 30
physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
  28 29 30
physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
  28 29 30

From lscpu:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                112
On-line CPU(s) list:   0-111
Thread(s) per core:    1
Core(s) per socket:    28
Socket(s):             4
NUMA node(s):          4
Vendor ID:             GenuineIntel

(Continued on next page)
New H3C Technologies Co., Ltd.  
H3C UniServer R6900 G5 (Intel Xeon Platinum 8376HL)

**SPEC CPU®2017 Floating Point Speed Result**

**SPECspeed®2017_fp_base = 240**

**SPECspeed®2017_fp_peak = 244**

---

**Platform Notes (Continued)**

- **CPU family:** 6
- **Model:** 85
- **Model name:** Intel(R) Xeon(R) Platinum 8376HL CPU @ 2.60GHz
- **Stepping:** 11
- **CPU MHz:** 2760.825
- **CPU max MHz:** 4300.0000
- **CPU min MHz:** 1000.0000
- **BogoMIPS:** 5200.00
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 1024K
- **L3 cache:** 39424K
- **NUMA node0 CPU(s):** 0-27
- **NUMA node1 CPU(s):** 28-55
- **NUMA node2 CPU(s):** 56-83
- **NUMA node3 CPU(s):** 84-111
- **Flags:** fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_pmm ssbd mba ibrs ibpb ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmob hle avx2 smep bmi2  ertz invpcid rtm cmpmpx rdart_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsavesopt xsaves xgetbv1 xsaves cqm_llc cqm_occump llc cqm_mbm_total cqm_mbm_local avx512_bf16 dtherm ida arat pin pts hwp hwp_act_window hwp_epp hwp_pkg_req pku ospke avx512_vnni md_clear flush_l1d arch_capabilities

/proc/cpuinfo cache data

---

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

- available: 4 nodes (0-3)
- node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
- node 0 size: 191827 MB
- node 0 free: 189072 MB
- node 1 cpus: 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55
- node 1 size: 193529 MB
- node 1 free: 193268 MB
- node 2 cpus: 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83
- node 2 size: 193529 MB
- node 2 free: 188344 MB

(Continued on next page)
New H3C Technologies Co., Ltd.
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SPECspeed®2017_fp_base = 240
SPECspeed®2017_fp_peak = 244

Platform Notes (Continued)

node 3 cpus: 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105
106 107 108 109 110 111
node 3 size: 193529 MB
node 3 free: 193301 MB
node distances:
node 0 1 2 3
0: 10 20 20 20
1: 20 10 20 20
2: 20 20 10 20
3: 20 20 20 10

From /proc/meminfo
MemTotal: 790953848 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance
/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release*/etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.2 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.2"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga
uname -a:
Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store

(Continued on next page)
### Compiler Version Notes

```
C          | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
          | 644.nab_s(base)
```

---

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
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(Continued on next page)
## SPEC CPU® 2017 Floating Point Speed Result

**New H3C Technologies Co., Ltd.**

H3C UniServer R6900 G5 (Intel Xeon Platinum 8376HL)

**SPECspeed®2017_fp_base** = 240

**SPECspeed®2017_fp_peak** = 244

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<tr>
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<th>New H3C Technologies Co., Ltd.</th>
<th>Test Sponsor: New H3C Technologies Co., Ltd.</th>
<th>Test Date: Apr-2021</th>
<th>Hardware Availability: Sep-2020</th>
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<td>Tested by</td>
<td>New H3C Technologies Co., Ltd.</td>
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<td>Software Availability: Dec-2020</td>
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### Compiler Version Notes (Continued)

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<th>Compiler</th>
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<tbody>
<tr>
<td>C</td>
<td>644.nab_s(peak)</td>
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#### Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113

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<table>
<thead>
<tr>
<th>C</th>
<th>619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base)</th>
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</table>

#### Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

<table>
<thead>
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<th>C</th>
<th>644.nab_s(peak)</th>
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#### Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113

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<table>
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<tr>
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<th>607.cactuBSSN_s(base, peak)</th>
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#### Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on Intel® 64, Version 2021.1 Build 20201112_000000

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#### Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel® 64, Version 2021.1 Build 20201112_000000

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#### Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel® 64, Version 2021.1 Build 20201112_000000

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<table>
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<tr>
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<th>603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)</th>
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#### Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel® 64, Version 2021.1 Build 20201112_000000

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New H3C Technologies Co., Ltd.  
H3C UniServer R6900 G5 (Intel Xeon Platinum 8376HL)  

**SPEC CPU®2017 Floating Point Speed Result**  

**SPECspeed®2017_fp_base = 240**  
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**Compiler Version Notes (Continued)**

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<th>Fortran, C</th>
<th>621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)</th>
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---

**Base Compiler Invocation**

C benchmarks:  
`icc`

Fortran benchmarks:  
`ifort`

Benchmarks using both Fortran and C:  
`ifort icc`

Benchmarks using Fortran, C, and C++:  
`icpc icc ifort`

---

**Base Portability Flags**

603.bwaves_s: -DSPEC_LP64  
607.cactuBSSN_s: -DSPEC_LP64  
619.lbm_s: -DSPEC_LP64  
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian  
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG  
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl  
638.imagick_s: -DSPEC_LP64  
644.nab_s: -DSPEC_LP64  
649.fotonik3d_s: -DSPEC_LP64  
654.roms_s: -DSPEC_LP64
New H3C Technologies Co., Ltd.
H3C UniServer R6900 G5 (Intel Xeon Platinum 8376HL)

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Base Optimization Flags

C benchmarks:
-m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries

Fortran benchmarks:
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
-mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

Benchmarks using both Fortran and C:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

644.nab_s: icx

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort
New H3C Technologies Co., Ltd.  
H3C UniServer R6900 G5 (Intel Xeon Platinum 8376HL)

**SPEC CPU®2017 Floating Point Speed Result**

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**Software Availability:** Dec-2020

**Peak Portability Flags**

Same as Base Portability Flags

**Peak Optimization Flags**

**C benchmarks:**

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes


**Fortran benchmarks:**

603.bwaves_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs -mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

649.fotonik3d_s: Same as 603.bwaves_s

654.roms_s: basepeak = yes

**Benchmarks using both Fortran and C:**

621.wrf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

(Continued on next page)
New H3C Technologies Co., Ltd.  
H3C UniServer R6900 G5 (Intel Xeon Platinum 8376HL)

| SPECspeed®2017_fp_base = 240 | SPECspeed®2017_fp_peak = 244 |

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Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Apr-2021
Hardware Availability: Sep-2020
Software Availability: Dec-2020

Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at:

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-CPX-RevC.xml

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