### Dell Inc.

**PowerEdge R750 (Intel Xeon Platinum 8368, 2.40 GHz)**

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>Test Sponsor: Dell Inc.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Date:</td>
<td>Hardware Availability: May-2021</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Software Availability: Feb-2021</td>
</tr>
</tbody>
</table>

#### Copies

<table>
<thead>
<tr>
<th>Workload</th>
<th>Copies</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench_r</td>
<td>545</td>
</tr>
<tr>
<td>gcc_r</td>
<td>525</td>
</tr>
<tr>
<td>mcf_r</td>
<td>525</td>
</tr>
<tr>
<td>omnetpp_r</td>
<td>525</td>
</tr>
<tr>
<td>xalancbmk_r</td>
<td>525</td>
</tr>
<tr>
<td>x264_r</td>
<td>525</td>
</tr>
<tr>
<td>deepsjeng_r</td>
<td>525</td>
</tr>
<tr>
<td>leela_r</td>
<td>525</td>
</tr>
<tr>
<td>exchange2_r</td>
<td>525</td>
</tr>
<tr>
<td>xz_r</td>
<td>525</td>
</tr>
</tbody>
</table>

#### SPECrate®2017_int_base = 525

#### SPECrate®2017_int_peak = 545

### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon Platinum 8368</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz:</td>
<td>3400</td>
</tr>
<tr>
<td>Nominal:</td>
<td>2400</td>
</tr>
<tr>
<td>Enabled:</td>
<td>76 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1.2 chips</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>1.25 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3:</td>
<td>57 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R)</td>
</tr>
<tr>
<td>Storage:</td>
<td>225 GB on tmpfs</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>OS:</th>
<th>Red Hat Enterprise Linux 8.3 (Ootpa) 4.18.0-240.15.1.el8_3.x86_64</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux</td>
</tr>
<tr>
<td>Parallel:</td>
<td>No</td>
</tr>
<tr>
<td>Firmware:</td>
<td>Version 1.1.2 released Apr-2021</td>
</tr>
<tr>
<td>File System:</td>
<td>tmpfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 5 (graphical multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>Power Management:</td>
<td>BIOS and OS set to prefer performance at the cost of additional power usage.</td>
</tr>
</tbody>
</table>
Dell Inc.
PowerEdge R750 (Intel Xeon Platinum 8368, 2.40 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

SPECrate®2017_int_base = 525
SPECrate®2017_int_peak = 545

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>152</td>
<td>629</td>
<td>385</td>
<td>629</td>
<td>384</td>
<td>152</td>
<td>629</td>
<td>384</td>
<td></td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>152</td>
<td>566</td>
<td>380</td>
<td>564</td>
<td>381</td>
<td>152</td>
<td>459</td>
<td>469</td>
<td>458</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>152</td>
<td>302</td>
<td>814</td>
<td>302</td>
<td>815</td>
<td>152</td>
<td>302</td>
<td>814</td>
<td>302</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>152</td>
<td>740</td>
<td>270</td>
<td>742</td>
<td>269</td>
<td>152</td>
<td>740</td>
<td>270</td>
<td>742</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>152</td>
<td>247</td>
<td>651</td>
<td>246</td>
<td>652</td>
<td>152</td>
<td>247</td>
<td>651</td>
<td>246</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>152</td>
<td>233</td>
<td>1140</td>
<td>232</td>
<td>1150</td>
<td>152</td>
<td>221</td>
<td>1200</td>
<td>222</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>152</td>
<td>395</td>
<td>441</td>
<td>395</td>
<td>441</td>
<td>152</td>
<td>395</td>
<td>441</td>
<td>395</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>152</td>
<td>580</td>
<td>434</td>
<td>583</td>
<td>432</td>
<td>152</td>
<td>580</td>
<td>434</td>
<td>583</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>152</td>
<td>331</td>
<td>1200</td>
<td>331</td>
<td>1200</td>
<td>152</td>
<td>331</td>
<td>1200</td>
<td>331</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>152</td>
<td>557</td>
<td>295</td>
<td>557</td>
<td>295</td>
<td>152</td>
<td>575</td>
<td>286</td>
<td>577</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/mnt/ramdisk2/cpu2017-1.1.5-ic2021.1/lib/intel64:/mnt/ramdisk2/cpu2017-1.1.5-ic2021.1/lib/ia32:/mnt/ramdisk2/cpu2017-1.1.5-ic2021.1/je5.0.1-32"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
### General Notes (Continued)

numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 225 GB ramdisk created with the cmd: "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"

### Platform Notes

**BIOS Settings:**

- **Sub NUMA Cluster:** 2-Way Clustering
- **Virtualization Technology:** Disabled
- **System Profile:** Custom
- **CPU Power Management:** Maximum Performance
- **C1E:** Disabled
- **C States:** Autonomous
- **Memory Patrol Scrub:** Disabled
- **Energy Efficiency Policy:** Performance
- **CPU Interconnect Bus Link Power Management:** Disabled

 Sysinfo program /mnt/ramdisk2/cpu2017-1.1.5-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2flc
running on localhost.localdomain Wed Apr 21 16:02:19 2021

**SUT (System Under Test) info as seen by some common utilities:**

For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

- **model name:** Intel(R) Xeon(R) Platinum 8368 CPU @ 2.40GHz
- **2 "physical id"s (chips)**
- **152 "processors"**

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

(Continued on next page)
**Platform Notes (Continued)**

cpu cores : 38
siblings : 76
physical 0: cores 0 1 2 3 4 5 6 7 8 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
26 27 28 29 30 31 32 33 34 35 36 37
physical 1: cores 0 1 2 3 4 5 6 7 8 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
26 27 28 29 30 31 32 33 34 35 36 37

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 152
On-line CPU(s) list: 0-151
Thread(s) per core: 2
Core(s) per socket: 38
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Platinum 8368 CPU @ 2.40GHz
Stepping: 6
CPU MHz: 1552.329
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 58368K
NUMA node0 CPU(s):
0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58
,60,62,64,66,68,70,72,74,76,78,80,82,84,86,88,90,92,94,96,98,100,102,104,106,108,110
NUMA node1 CPU(s):
1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59
,61,63,65,67,69,71,73,75,77,79,81,83,85,87,89,91,93,95,97,99,101,103,105,107,109,111
,113,115,117,119,121,123,125,127,129,131,133,135,137,139,141,143,145,147,149,151
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pccid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single intel_pcin ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaves xsaveopt xsaves xsavec cqmm_llc cqmm_occup_llc cqmm mbm_total cqmm mbm_local split_lock_detect wbnoinvd

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Dell Inc.
PowerEdge R750 (Intel Xeon Platinum 8368, 2.40 GHz)  

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base</th>
<th>525</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_int_peak</td>
<td>545</td>
</tr>
</tbody>
</table>

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.  

Test Date: Apr-2021  
Hardware Availability: May-2021  
Software Availability: Feb-2021

Platform Notes (Continued)

dtherm ida arat pln pts avx512vbmi umip pku ospe avx512_vbmi2 qfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d arch_capabilities

/proc/cpuinfo cache data  
cache size : 58368 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.  
available: 2 nodes (0-1)  
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 72 74 76 78 80 82 84 86 88 90 92 94 96 98 100 102 104 106 108 110 112 114 116 118 120 122 124 126 128 130 132 134 136 138 140 142 144 146 148 150
node 0 size: 240144 MB
node 0 free: 241366 MB
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47 49 51 53 55 57 59 61 63 65 67 69 71 73 75 77 79 81 83 85 87 89 91 93 95 97 99 101 103 105 107 109 111 113 115 117 119 121 123 125 127 129 131 133 135 137 139 141 143 145 147 149 151
node 1 size: 241490 MB
node 1 free: 256257 MB
node distances:
node 0 1
0: 10 20
1: 20 10

From /proc/meminfo  
MemTotal: 527787560 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active  
Current active profile: throughput-performance

From /etc/*release* /etc/*version*

os-release:
  NAME="Red Hat Enterprise Linux"
  VERSION="8.3 (Ootpa)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="8.3"
  PLATFORM_ID="platform:el8"
  PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
  ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

(Continued on next page)
## Dell Inc.

**PowerEdge R750 (Intel Xeon Platinum 8368, 2.40 GHz)**

### SPEC CPU®2017 Integer Rate Result

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>Dell Inc.</th>
<th>SPECrate®2017_int_base</th>
<th>Apr-2021</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor</td>
<td>Dell Inc.</td>
<td>SPECrate®2017_int_peak</td>
<td>May-2021</td>
</tr>
<tr>
<td>Tested by</td>
<td>Dell Inc.</td>
<td></td>
<td>Feb-2021</td>
</tr>
</tbody>
</table>

---

### Platform Notes (Continued)

**uname -a:**
```
Linux localhost.localdomain 4.18.0-240.15.1.el8_3.x86_64 #1 SMP Wed Feb 3 03:12:15 EST 2021 x86_64 x86_64 x86_64 GNU/Linux
```

**Kernel self-reported vulnerability status:**

- CVE-2018-12207 (iTLB Multihit): Not affected
- CVE-2018-3620 (L1 Terminal Fault): Not affected
- Microarchitectural Data Sampling: Not affected
- CVE-2017-5754 (Meltdown): Not affected
- CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
- CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
- CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
- CVE-2019-11135 (TSX Asynchronous Abort): Not affected

**run-level 5 Apr 21 15:57**

** SPEC is set to: /mnt/ramdisk2/cpu2017-1.1.5-ic2021.1**

**Filesystem | Type | Size | Used | Avail | Use% | Mounted on**
<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>tmpfs</td>
<td>tmpfs</td>
<td>225G</td>
<td>6.9G</td>
<td>219G</td>
<td>4%</td>
<td>/mnt/ramdisk2</td>
</tr>
</tbody>
</table>

**From /sys/devices/virtual/dmi/id**

- **Vendor:** Dell Inc.
- **Product:** PowerEdge R750
- **Product Family:** PowerEdge
- **Serial:** 1234567

**Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.**

**Memory:**
- 12x 002C069D002C 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200
- 4x 00AD063200AD HMMA4GR7AJR8N-XN 32 GB 2 rank 3200

**BIOS:**
- BIOS Vendor: Dell Inc.
- BIOS Version: 1.1.2
- BIOS Date: 04/09/2021
- BIOS Revision: 1.1

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Dell Inc.
PowerEdge R750 (Intel Xeon Platinum 8368, 2.40 GHz)

CPU2017 License: 55
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SPECCredit®2017_int_base = 525
SPECCredit®2017_int_peak = 545

Test Date: Apr-2021
Hardware Availability: May-2021
Software Availability: Feb-2021

Platform Notes (Continued)

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| C       | 500.perlbench_r(peak) 557.xz_r(peak) |
|---------------------------------------------------|
| Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
  64, Version 2021.1 Build 20201112_000000
<table>
<thead>
<tr>
<th>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</th>
</tr>
</thead>
</table>
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113
<table>
<thead>
<tr>
<th>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</th>
</tr>
</thead>
</table>
| C       | 502.gcc_r(peak) 505.mcf_r(base, peak)
  525.x264_r(base, peak) 557.xz_r(base) |
|---------------------------------------------------|
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2021.1 Build 20201113
<table>
<thead>
<tr>
<th>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</th>
</tr>
</thead>
<tbody>
<tr>
<td>C</td>
</tr>
<tr>
<td>---------------------------------------------------</td>
</tr>
</tbody>
</table>
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<thead>
<tr>
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</table>
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113
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(Continued on next page)
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Compiler Version Notes (Continued)

==============================================================================
C      | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
      | 525.x264_r(base, peak) 557.xz_r(base)
==============================================================================
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
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==============================================================================
C      | 500.perlbench_r(peak) 557.xz_r(peak)
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Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 20201112_000000
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==============================================================================
C      | 502.gcc_r(peak)
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version
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==============================================================================
C      | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
      | 525.x264_r(base, peak) 557.xz_r(base)
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
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==============================================================================
C++    | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak)
      | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
==============================================================================
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================
Fortran | 548.exchange2_r(base, peak)
==============================================================================
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on

(Continued on next page)
Dell Inc.
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Compiler Version Notes (Continued)

Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbmk_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries

(Continued on next page)
### Dell Inc.

**PowerEdge R750 (Intel Xeon Platinum 8368, 2.40 GHz)**

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
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**SPECrate®2017_int_base = 525**

**SPECrate®2017_int_peak = 545**

---

**Base Optimization Flags (Continued)**

C++ benchmarks (continued):
```bash
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:
```bash
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-auto -mbranched-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc
```

---

### Peak Compiler Invocation

C benchmarks (except as noted below):
- **icx**
- **500.perlbench_r: icc**
- **557.xz_r: icc**

C++ benchmarks:
- **icpx**

Fortran benchmarks:
- **ifort**

---

### Peak Portability Flags

- **500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64**
- **502.gcc_r: -D_FILE_OFFSET_BITS=64**
- **505.mcf_r: -DSPEC_LP64**
- **520.omnetpp_r: -DSPEC_LP64**
- **523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX**
- **525.x264_r: -DSPEC_LP64**
- **531.deepsjeng_r: -DSPEC_LP64**
- **541.leela_r: -DSPEC_LP64**
- **548.exchange2_r: -DSPEC_LP64**
- **557.xz_r: -DSPEC_LP64**
# SPEC CPU®2017 Integer Rate Result

## Dell Inc.

**PowerEdge R750 (Intel Xeon Platinum 8368, 2.40 GHz)**

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</table>

**SPECrate®2017_int_base = 525**

**SPECrate®2017_int_peak = 545**

## Peak Optimization Flags

**C benchmarks:**

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -flto
-O3 -ffast-math -qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

**C++ benchmarks:**

520.omnetpp_r: basepeak = yes

523.xalanbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

**Fortran benchmarks:**

548.exchange2_r: basepeak = yes
Dell Inc.
PowerEdge R750 (Intel Xeon Platinum 8368, 2.40 GHz)

SPECraten®2017_int_base = 525
SPECraten®2017_int_peak = 545

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.
Test Date: Apr-2021
Hardware Availability: May-2021
Software Availability: Feb-2021

The flags files that were used to format this result can be browsed at:

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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