## SPEC CPU®2017 Integer Speed Result

### Dell Inc.

PowerEdge MX750c (Intel Xeon Platinum 8352Y, 2.20 GHz)

<table>
<thead>
<tr>
<th>Test Date:</th>
<th>Apr-2021</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Apr-2021</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Feb-2021</td>
</tr>
</tbody>
</table>

**SPECspeed®2017_int_base = 11.6**

**SPECspeed®2017_int_peak = 11.9**

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>55</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Dell Inc.</td>
</tr>
</tbody>
</table>

### Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>64</td>
<td>8.88</td>
<td>10.6</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>64</td>
<td>11.0</td>
<td>19.3</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>64</td>
<td>11.9</td>
<td></td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>64</td>
<td></td>
<td></td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>64</td>
<td>13.0</td>
<td></td>
</tr>
<tr>
<td>625.x264_s</td>
<td>64</td>
<td></td>
<td></td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>64</td>
<td>5.76</td>
<td></td>
</tr>
<tr>
<td>641.leela_s</td>
<td>64</td>
<td>4.72</td>
<td></td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>64</td>
<td></td>
<td>18.8</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>64</td>
<td></td>
<td>23.6</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Platinum 8352Y
- **Max MHz:** 3400
- **Nominal:** 2200
- **Enabled:** 64 cores, 2 chips
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 48 KB D on chip per core
- **L2:** 1.25 MB I+D on chip per core
- **L3:** 48 MB I+D on chip per chip
- **Other:** None
- **Memory:** 512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R)
- **Storage:** 125 GB on tmpfs
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux 8.3 (Ootpa)
- **Compiler:**
  - C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;
  - Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;
  - C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux
- **Parallel:** Yes
- **Firmware:** Version 1.1.0 released Mar-2021
- **File System:** tmpfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
## SPEC CPU®2017 Integer Speed Result

**Dell Inc.**

PowerEdge MX750c (Intel Xeon Platinum 8352Y, 2.20 GHz)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>11.6</td>
<td>11.9</td>
</tr>
</tbody>
</table>

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>64</td>
<td>253</td>
<td>7.02</td>
<td>254</td>
<td>6.98</td>
<td>219</td>
<td>8.10</td>
<td>220</td>
<td>8.08</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>64</td>
<td>370</td>
<td>10.8</td>
<td>374</td>
<td>10.6</td>
<td>362</td>
<td>11.0</td>
<td>361</td>
<td>11.0</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>64</td>
<td>245</td>
<td>19.3</td>
<td>245</td>
<td>19.3</td>
<td>245</td>
<td>19.3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>64</td>
<td>137</td>
<td>11.9</td>
<td>137</td>
<td>11.9</td>
<td>137</td>
<td>11.9</td>
<td></td>
<td></td>
</tr>
<tr>
<td>623.xalanbmk_s</td>
<td>64</td>
<td>107</td>
<td>13.2</td>
<td>109</td>
<td>13.0</td>
<td>107</td>
<td>13.2</td>
<td>109</td>
<td>13.0</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>64</td>
<td>106</td>
<td>16.7</td>
<td>106</td>
<td>16.7</td>
<td>101</td>
<td>17.5</td>
<td>101</td>
<td>17.4</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>64</td>
<td>248</td>
<td>5.77</td>
<td>249</td>
<td>5.76</td>
<td>248</td>
<td>5.77</td>
<td>249</td>
<td>5.76</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>64</td>
<td>362</td>
<td>4.72</td>
<td>362</td>
<td>4.72</td>
<td>362</td>
<td>4.72</td>
<td>362</td>
<td>4.72</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>64</td>
<td>156</td>
<td>18.8</td>
<td>156</td>
<td>18.8</td>
<td>156</td>
<td>18.8</td>
<td>156</td>
<td>18.8</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>64</td>
<td>261</td>
<td>23.6</td>
<td>262</td>
<td>23.6</td>
<td>261</td>
<td>23.6</td>
<td>262</td>
<td>23.6</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

- KMP_AFFINITY = "granularity=fine,scatter"
- LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/je5.0.1-64"
- MALLOC_CONF = "retain:true"
- OMP_STACKSIZE = "192M"

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default
Prior to runcpu invocation

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Dell Inc.

PowerEdge MX750c (Intel Xeon Platinum 8352Y, 2.20 GHz)

SPECspeed®2017_int_base = 11.6
SPECspeed®2017_int_peak = 11.9

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Apr-2021
Hardware Availability: Apr-2021
Software Availability: Feb-2021

General Notes (Continued)

Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:
Logical Processor : Disabled
Virtualization Technology : Disabled

System Profile : Custom
CPU Power Management : Maximum Performance
C1E : Disabled
C States : Autonomous
Memory Patrol Scrub : Disabled
Energy Efficiency Policy : Performance
CPU Interconnect Bus Link
Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.7-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Fri Apr  2 20:25:04 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8352Y CPU @ 2.20GHz
  2 "physical id"s (chips)
  64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 32
  siblings : 32
  physical 0: cores 0 1 2 3 4 5 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
  physical 1: cores 0 1 2 3 4 5 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31

(Continued on next page)
 SPEC CPU®2017 Integer Speed Result

Dell Inc.  
PowerEdge MX750c (Intel Xeon Platinum 8352Y, 2.20 GHz)

 SPECspeed®2017_int_base = 11.6
 SPECspeed®2017_int_peak = 11.9

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.  

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Thread(s) per core: 1
Core(s) per socket: 32
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Platinum 8352Y CPU @ 2.20GHz
Stepping: 6
CPU MHz: 2865.170
BogoMIPS: 4400.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 49152K
NUMA node0 CPU(s):
0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58
,60,62
NUMA node1 CPU(s):
1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59
,61,63
Flags:
  fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpes gb rdtscp lm constant tsp art arch_perfmon pebs bts rep_good nopt xtopology nonstop_tsc cpuid aperfmperf pni pclmuldq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abtm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single intel_pni ssbd mba ibrs ibpb stibp ibrs enhanced fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cmqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd shaɓi_avx512bw avx512vl xsaveopt xsaves cqm_llc cqm Occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect wbinvd dtm dtherm ida arat pln pts avx512vbmi umip pk uos pev avx512vbmi2 gfn fvi vaes pcclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdrid md_clear pconfi flush_lid
arch_capabilities

/platform CPUinfo cache data
   cache size : 49152 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

(Continued on next page)
Platform Notes (Continued)

available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46 48 50 52 54 56 58 60 62
node 0 size: 243884 MB
node 0 free: 247198 MB
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47 49 51 53 55 57 59 61 63
node 1 size: 244252 MB
node 1 free: 251576 MB
node distances:
node 0 1
0: 10 20
1: 20 10

MemTotal: 527807184 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
/sbin/tuned-adm active
   Current active profile: throughput-performance

uname -a:
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
   x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store
Dell Inc.
PowerEdge MX750c (Intel Xeon Platinum 8352Y, 2.20 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

SPECspeed®2017_int_base = 11.6
SPECspeed®2017_int_peak = 11.9

Test Date: Apr-2021
Hardware Availability: Apr-2021
Software Availability: Feb-2021

Platform Notes (Continued)

CVE-2017-5753 (Spectre variant 1):
Bypass disabled via prctl and seccomp
Mitigation: usercopy/swapgs barriers and __user pointer sanitization

CVE-2017-5715 (Spectre variant 2):
Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected

CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Apr 2 20:10 last=5

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.7-ic2021.1

Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 125G 4.4G 121G 4% /mnt/ramdisk

From /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge MX750c
Product Family: PowerEdge
Serial: 1234567

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
16x 002C0632002C 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200
16x Not Specified Not Specified

BIOS:
BIOS Vendor: Dell Inc.
BIOS Version: 1.1.0
BIOS Date: 03/25/2021
BIOS Revision: 1.1

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 600.perlbench_s(peak)
==============================================================================

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
## SPEC CPU®2017 Integer Speed Result

**Dell Inc.**  
PowerEdge MX750c (Intel Xeon Platinum 8352Y, 2.20 GHz)

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>55</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Dell Inc.</td>
</tr>
</tbody>
</table>

| SPECspeed®2017_int_base = 11.6 |
| SPECspeed®2017_int_peak = 11.9 |

Test Date: Apr-2021  
Hardware Availability: Apr-2021  
Software Availability: Feb-2021

---

### Compiler Version Notes (Continued)

<table>
<thead>
<tr>
<th>Compiler</th>
<th>Benchmark(s)</th>
</tr>
</thead>
</table>
| C        | perlbench_s(base)  
           | gcc_s(base, peak)  
           | mcf_s(base, peak)  
           | x264_s(base, peak)  
           | xz_s(base, peak)  |
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |

<table>
<thead>
<tr>
<th>Compiler</th>
<th>Benchmark(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>C</td>
<td>perlbench_s(peak)</td>
</tr>
</tbody>
</table>
| Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |

<table>
<thead>
<tr>
<th>Compiler</th>
<th>Benchmark(s)</th>
</tr>
</thead>
</table>
| C        | perlbench_s(base)  
           | gcc_s(base, peak)  
           | mcf_s(base, peak)  
           | x264_s(base, peak)  
           | xz_s(base, peak)  |
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |

<table>
<thead>
<tr>
<th>Compiler</th>
<th>Benchmark(s)</th>
</tr>
</thead>
</table>
| C++      | omnetpp_s(base, peak)  
           | xalancbmk_s(base, peak)  
           | deepsjeng_s(base, peak)  
           | leela_s(base, peak)  |
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |

<table>
<thead>
<tr>
<th>Fortran</th>
<th>Benchmark(s)</th>
</tr>
</thead>
</table>
| Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |

---

Compiler Version Notes (Continued)
SPEC CPU®2017 Integer Speed Result

Dell Inc.
PowerEdge MX750c (Intel Xeon Platinum 8352Y, 2.20 GHz)

SPECspeed®2017_int_base = 11.6
SPECspeed®2017_int_peak = 11.9

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-1qkmalloc

Fortran benchmarks:
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries

Test Date: Apr-2021
Hardware Availability: Apr-2021
Software Availability: Feb-2021
Dell Inc.  
PowerEdge MX750c (Intel Xeon Platinum 8352Y, 2.20 GHz)  

SPEC CPU®2017 Integer Speed Result  

SPECspeed®2017_int_base = 11.6  
SPECspeed®2017_int_peak = 11.9

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.  

Test Date: Apr-2021  
Hardware Availability: Apr-2021  
Software Availability: Feb-2021

Peak Compiler Invocation

C benchmarks (except as noted below):
- icx
- 600.perlbench_s: icc

C++ benchmarks:
- icpx

Fortran benchmarks:
- ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
- 600.perlbench_s: -W1,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)  
  -xCORE-AVX512 -ipo -O3 -no-prec-div  
  -qopt-mem-layout-trans=4 -fno-strict-overflow  
  -mbranches-within-32B-boundaries  
  -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
- 602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)  
  -fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto  
  -Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4  
  -mbranches-within-32B-boundaries  
  -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
- 605.mcf_s: basepeak = yes
- 625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs  
  -xCORE-AVX512 -flto -O3 -ffast-math  
  -qopt-mem-layout-trans=4 -fno-alias  
  -mbranches-within-32B-boundaries  
  -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
- 657.xz_s: basepeak = yes

(Continued on next page)
Dell Inc.
PowerEdge MX750c (Intel Xeon Platinum 8352Y, 2.20 GHz)

SPECspeed®2017_int_base = 11.6
SPECspeed®2017_int_peak = 11.9

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Apr-2021
Hardware Availability: Apr-2021
Software Availability: Feb-2021

Peak Optimization Flags (Continued)

C++ benchmarks:

620.omnetpp_s: basepeak = yes
623.xalancbmk_s: basepeak = yes
631.deepsjeng_s: basepeak = yes
641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.7 on 2021-04-02 20:25:03-0400.
Originally published on 2021-05-18.