## SPEC CPU®2017 Integer Speed Result

**Dell Inc.**

PowerEdge MX750c (Intel Xeon Gold 6354, 3.00 GHz)

<table>
<thead>
<tr>
<th>Thread</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
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</thead>
<tbody>
<tr>
<td>perlbench_s 36</td>
<td>11.1</td>
<td>11.6</td>
</tr>
<tr>
<td>gcc_s 36</td>
<td>20.3</td>
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<tr>
<td>mcf_s 36</td>
<td>9.22</td>
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<tr>
<td>omnetpp_s 36</td>
<td>13.9</td>
<td>17.3</td>
</tr>
<tr>
<td>xalancbmk_s 36</td>
<td>6.09</td>
<td>18.0</td>
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<tr>
<td>x264_s 36</td>
<td>23.1</td>
<td></td>
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<tr>
<td>deepsjeng_s 36</td>
<td>4.99</td>
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<tr>
<td>leela_s 36</td>
<td>19.9</td>
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<tr>
<td>exchange2_s 36</td>
<td></td>
<td></td>
</tr>
<tr>
<td>xz_s 36</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### Software

**OS:** Red Hat Enterprise Linux 8.2 (Ootpa) 4.18.0-193.el8.x86_64

**Compiler:**
- C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;
- Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;
- C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux

**Parallel:** Yes

**Firmware:** Version 1.1.1 released Apr-2021

**File System:** tmpfs

**System State:** Run level 3 (multi-user)

**Base Pointers:** 64-bit

**Peak Pointers:** 64-bit

**Other:** jemalloc memory allocator V5.0.1

**Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.

### Hardware

**CPU Name:** Intel Xeon Gold 6354

**Max MHz:** 3600

**Nominal:** 3000

**Enabled:** 36 cores, 2 chips

**Orderable:** 1.2 chips

**Cache L1:** 32 KB I + 48 KB D on chip per core

**L2:** 1.25 MB I+D on chip per core

**L3:** 39 MB I+D on chip per chip

**Other:** None

**Memory:** 1 TB (32 x 32 GB 2Rx8 PC4-3200AA-R)

**Storage:** 125 GB on tmpfs

**Other:** None
Dell Inc.

PowerEdge MX750c (Intel Xeon Gold 6354, 3.00 GHz)

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>600.perlbench_s</td>
<td>36</td>
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<td>7.41</td>
<td>241</td>
<td>7.38</td>
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<td>209</td>
<td>8.51</td>
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<tr>
<td>602.gcc_s</td>
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<td>356</td>
<td>11.2</td>
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<td>11.6</td>
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<td>20.3</td>
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<td>177</td>
<td>9.22</td>
<td>175</td>
<td>9.29</td>
<td>36</td>
<td>177</td>
<td>9.22</td>
<td>175</td>
<td>9.29</td>
<td></td>
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<td>623.xalancbmk_s</td>
<td>36</td>
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<td>14.0</td>
<td>102</td>
<td>13.9</td>
<td>36</td>
<td>101</td>
<td>14.0</td>
<td>102</td>
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<tr>
<td>625.x264_s</td>
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<td>17.3</td>
<td>102</td>
<td>17.3</td>
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<td>631.deepsjeng_s</td>
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<td>23.1</td>
<td>267</td>
<td>23.1</td>
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</tr>
</tbody>
</table>

**SPECspeed**\textsuperscript{2017\_int\_base} = 11.8

**SPECspeed**\textsuperscript{2017\_int\_peak} = 12.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes
Environment variables set by runcpu before the start of the run:
KMP\_AFFINITY = "granularity=fine,scatter"
LD\_LIBRARY\_PATH = "/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/jem5.0.1-64"
MALLOC\_CONF = "retain:true"
OMP\_STACK\_SIZE = "192M"

### General Notes
Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default
Prior to runcpu invocation

(Continued on next page)
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PowerEdge MX750c (Intel Xeon Gold 6354, 3.00 GHz)

SPECspeed®2017_int_base = 11.8
SPECspeed®2017_int_peak = 12.0

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Apr-2021
Hardware Availability: Apr-2021
Software Availability: Feb-2021

General Notes (Continued)

Filesystem page cache synced and cleared with:
  sync; echo 3>/proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:
  Logical Processor : Disabled
  Virtualization Technology : Disabled

  System Profile : Custom
  CPU Power Management : Maximum Performance
  C1E : Disabled
  C States : Autonomous
  Memory Patrol Scrub : Disabled
  Energy Efficiency Policy : Performance
  CPU Interconnect Bus Link
  Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.7-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Fri Apr 9 20:16:45 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
  https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 6354 CPU @ 3.00GHz
  2 "physical id"s (chips)
  36 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following
  excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 18
  siblings : 18
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17

From lscpu:
  Architecture: x86_64

(Continued on next page)
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Platform Notes (Continued)

CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 36
On-line CPU(s) list: 0-35
Thread(s) per core: 1
Core(s) per socket: 18
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6354 CPU @ 3.00GHz
Stepping: 6
CPU MHz: 3600.000
BogoMIPS: 6000.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 39936K
NUMA node0 CPU(s): 0-8
NUMA node1 CPU(s): 9-17
NUMA node2 CPU(s): 18-26
NUMA node3 CPU(s): 27-35
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l13 invpcid_single ssbd
mbs ibrs ibpb ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase

tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cmp rdt_a avx512f avx512dq
rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw
avx512vl xsxavopt xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total

cqm_mbm_local wbinvd dtherm ida arat pln pts avx512vbmi umip pkpu ospke
avx512_vbmi2 gfnl vaes vpcmcmldq avx512_vnni avx512_bitalg tme avx512_vpopcntdq
la57 rdpid md_clear pconfi flush_l1d arch_capabilities

/proc/cpuinfo cache data
  cache size : 39936 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
  available: 4 nodes (0-3)
  node 0 cpus: 0 1 2 3 4 5 6 7 8
  node 0 size: 257413 MB
  node 0 free: 257422 MB

(Continued on next page)
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Platform Notes (Continued)

node 1 cpus: 9 10 11 12 13 14 15 16 17
node 1 size: 258045 MB
node 1 free: 257583 MB
node 2 cpus: 18 19 20 21 22 23 24 25 26
node 2 size: 258045 MB
node 2 free: 251428 MB
node 3 cpus: 27 28 29 30 31 32 33 34 35
node 3 size: 258043 MB
node 3 free: 255078 MB
node distances:
node 0 1 2 3
0: 10 11 20 20
1: 11 10 20 20
2: 20 20 10 11
3: 20 20 11 10

From /proc/meminfo
MemTotal: 1056304992 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tunedadm active
Current active profile: throughput-performance

From /etc/*release*/etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.2 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.2"
PLATFORM_ID="platform:e18"
PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

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CPU2017 License: 55
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Tested by: Dell Inc.

Test Date: Apr-2021
Hardware Availability: Apr-2021
Software Availability: Feb-2021

Platform Notes (Continued)

CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): No status reported
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Apr 9 18:50
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.7-ic2021.1
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 125G 4.4G 121G 4% /mnt/ramdisk

From /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge MX750c
Product Family: PowerEdge
Serial: 1234567

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
15x 00AD063200AD HMA84GR7CJR4N-XN 32 GB 2 rank 3200
17x 00AD063200AD HMAA4GR7AJR8N-XN 32 GB 2 rank 3200

BIOS:
BIOS Vendor: Dell Inc.
BIOS Version: 1.1.1
BIOS Date: 04/02/2021
BIOS Revision: 1.1

(End of data from sysinfo program)

Compiler Version Notes

C | 600.perlbench_s(peak)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)

(Continued on next page)
Dell Inc.

PowerEdge MX750c (Intel Xeon Gold 6354, 3.00 GHz)

| SPECspeed\textsuperscript{®}2017\textsuperscript{-}int\textsubscript{base} = 11.8 |
| SPECspeed\textsuperscript{®}2017\textsuperscript{-}int\textsubscript{peak} = 12.0 |

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Compiler Version Notes (Continued)

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| C       | 600.perlbench\textsubscript{s}(base) 602.gcc\textsubscript{s}(base, peak) 605.mcf\textsubscript{s}(base, peak) |
|         | 625.x264\textsubscript{s}(base, peak) 657.xz\textsubscript{s}(base, peak) |

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

| C       | 600.perlbench\textsubscript{s}(peak) |

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

| C       | 600.perlbench\textsubscript{s}(base) 602.gcc\textsubscript{s}(base, peak) 605.mcf\textsubscript{s}(base, peak) |
|         | 625.x264\textsubscript{s}(base, peak) 657.xz\textsubscript{s}(base, peak) |

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

| C++     | 620.omnetpp\textsubscript{s}(base, peak) 623.xalancbmk\textsubscript{s}(base, peak) |
|         | 631.deepsjeng\textsubscript{s}(base, peak) 641.leela\textsubscript{s}(base, peak) |

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
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| Fortran | 648.exchange2\textsubscript{s}(base, peak) |

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
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Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-lqkmalloc

Fortran benchmarks:
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries
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PowerEdge MX750c (Intel Xeon Gold 6354, 3.00 GHz)

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</tr>
</tbody>
</table>

Peak Compiler Invocation

C benchmarks (except as noted below):
   icx
   600.perlbench_s: icc

C++ benchmarks:
   icpx

Fortran benchmarks:
   ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
   600.perlbench_s: -Wl,-z muldefs -prof-gen(pass 1) -prof-use(pass 2)
   -xCORE-AVX512 -ipo -O3 -no-prec-div
   -qopt-mem-layout-trans=4 -fno-strict-overflow
   -mbranches-within-32B-boundaries
   -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
   602.gcc_s: -m64 -std=c11 -Wl,-z muldefs -fprofile-generate(pass 1)
   -fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
   -Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
   -mbranches-within-32B-boundaries
   -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
   605.mcf_s: basepeak = yes
   625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z muldefs
   -xCORE-AVX512 -flto -O3 -ffast-math
   -qopt-mem-layout-trans=4 -fno-alias
   -mbranches-within-32B-boundaries
   -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
   657.xz_s: basepeak = yes

(Continued on next page)
# SPEC CPU®2017 Integer Speed Result

## Dell Inc.

Dell Inc. tested a PowerEdge MX750c (Intel Xeon Gold 6354, 3.00 GHz) using SPECspeed®2017 and the following results were obtained:

**SPECspeed®2017_int_base = 11.8**

**SPECspeed®2017_int_peak = 12.0**

<table>
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<tr>
<th>CPU2017 License</th>
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<tr>
<td>55</td>
<td>Apr-2021</td>
<td>Apr-2021</td>
<td>Feb-2021</td>
</tr>
</tbody>
</table>

**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

### Peak Optimization Flags (Continued)

**C++ benchmarks:**

- 620.omnetpp_s: basepeak = yes
- 623.xalancbmk_s: basepeak = yes
- 631.deepsjeng_s: basepeak = yes
- 641.leela_s: basepeak = yes

**Fortran benchmarks:**

- 648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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