SPEC CPU®2017 Integer Rate Result

Dell Inc.

PowerEdge MX750c (Intel Xeon Gold 6314U, 2.30 GHz)

SPECrate®2017_int_base = 220
SPECrate®2017_int_peak = 229

Cores: 64

<table>
<thead>
<tr>
<th>Test</th>
<th>Result</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>155</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>181</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>208</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>354</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>273</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>467</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>172</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>170</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>461</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>124</td>
</tr>
</tbody>
</table>

Hardware

CPU Name: Intel Xeon Gold 6314U
Max MHz: 3400
Nominal: 2300
Enabled: 32 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1.25 MB I+D on chip per core
L3: 48 MB I+D on chip per chip
Other: None
Memory: 512 GB (8 x 64 GB 2Rx4 PC4-3200AA-R)
Storage: 125 GB on tmpfs
Other: None

Software

OS: Red Hat Enterprise Linux 8.3 (Ootpa)
4.18.0-240.el8.x86_64
Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++
Compiler Build 20201113 for Linux;
Fortran: Version 2021.1 of Intel Fortran Compiler
Classic Build 20201112 for Linux;
C/C++: Version 2021.1 of Intel C/C++ Compiler
Classic Build 20201112 for Linux
Parallel: No
Firmware: Version 1.1.0 released Mar-2021
File System: tmpfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.
# Dell Inc. PowerEdge MX750c (Intel Xeon Gold 6314U, 2.30 GHz)

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlb vs Bench</td>
<td>64</td>
<td>659</td>
<td>155</td>
<td>656</td>
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</tr>
<tr>
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<tr>
<td>505.mcf</td>
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<td>291</td>
</tr>
<tr>
<td>520.omnetpp</td>
<td>64</td>
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<td>133</td>
<td>632</td>
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<td>64</td>
<td>633</td>
<td>133</td>
<td>632</td>
<td>133</td>
<td>64</td>
<td>633</td>
<td>133</td>
<td>632</td>
</tr>
<tr>
<td>523.xalancbmk</td>
<td>64</td>
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<td>273</td>
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<tr>
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<td>172</td>
<td>64</td>
<td>426</td>
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<tr>
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</tr>
<tr>
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<td>363</td>
<td>462</td>
<td>364</td>
<td>461</td>
<td>64</td>
<td>363</td>
<td>462</td>
<td>364</td>
<td>461</td>
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<td>363</td>
<td>462</td>
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</tr>
<tr>
<td>557.xz</td>
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<td>555</td>
<td>124</td>
<td>553</td>
<td>125</td>
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<td>124</td>
<td>553</td>
<td>125</td>
<td>64</td>
<td>555</td>
<td>124</td>
<td>553</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:

```bash
LD_LIBRARY_PATH = 
"/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/lib/ia32:/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/je5.0.1-32"

MALLOC_CONF = "retain:true"
```

**General Notes**

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)

(Continued on next page)
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SPECrater®2017_int_base = 220  
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General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
  numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:
  Sub NUMA Cluster : 2-Way Clustering
  Virtualization Technology : Disabled

  System Profile : Custom
  CPU Power Management : Maximum Performance
    C1E : Disabled
    C States : Autonomous
  Memory Patrol Scrub : Disabled
  Energy Efficiency Policy : Performance
  CPU Interconnect Bus Link
    Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.7-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Sat Apr 3 19:16:21 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
  https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 6314U CPU @ 2.30GHz
  1 "physical id"s (chips)
  64 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following
(Continued on next page)
Dell Inc.

PowerEdge MX750c (Intel Xeon Gold 6314U, 2.30 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

SPECRate\textsuperscript{®}2017\_int\_base = 220
SPECRate\textsuperscript{®}2017\_int\_peak = 229

Test Date: Apr-2021
Hardware Availability: Apr-2021
Software Availability: Feb-2021

Platform Notes (Continued)

excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

<table>
<thead>
<tr>
<th>CPU cores: 32</th>
</tr>
</thead>
<tbody>
<tr>
<td>siblings: 64</td>
</tr>
<tr>
<td>physical 0: cores 0 1 2 3 4 5 10 11 12 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31</td>
</tr>
</tbody>
</table>

From lscpu:

<table>
<thead>
<tr>
<th>Architecture: x86_64</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU op-mode(s): 32-bit, 64-bit</td>
</tr>
<tr>
<td>Byte Order: Little Endian</td>
</tr>
<tr>
<td>CPU(s): 64</td>
</tr>
<tr>
<td>On-line CPU(s) list: 0-63</td>
</tr>
<tr>
<td>Thread(s) per core: 2</td>
</tr>
<tr>
<td>Core(s) per socket: 32</td>
</tr>
<tr>
<td>Socket(s): 1</td>
</tr>
<tr>
<td>NUMA node(s): 2</td>
</tr>
<tr>
<td>Vendor ID: GenuineIntel</td>
</tr>
<tr>
<td>CPU family: 6</td>
</tr>
<tr>
<td>Model: 106</td>
</tr>
<tr>
<td>Model name: Intel\textregistered(R) Xeon\textregistered(R) Gold 6314U CPU @ 2.30GHz</td>
</tr>
<tr>
<td>Stepping: 6</td>
</tr>
<tr>
<td>CPU MHz: 2922.860</td>
</tr>
<tr>
<td>BogoMIPS: 4600.00</td>
</tr>
<tr>
<td>Virtualization: VT-x</td>
</tr>
<tr>
<td>L1d cache: 48K</td>
</tr>
<tr>
<td>L1i cache: 32K</td>
</tr>
<tr>
<td>L2 cache: 1280K</td>
</tr>
<tr>
<td>L3 cache: 49152K</td>
</tr>
<tr>
<td>NUMA node0 CPU(s): 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58</td>
</tr>
<tr>
<td>NUMA node1 CPU(s): 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59</td>
</tr>
<tr>
<td>Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma cx16 xtpr pdcm pclid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault ebpx cat_l3 invpcid_single intel_pni ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 ertz invvpid cmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsavesopt xsaves vecopt xgetbv1 xsaveopt xsaves cmq_llc cmq_occup_llc cmq_mbm_total cmq_mbm_local split_lock_detect wbnoinvd dtherm ida arat pni pts avx512vmbi unpk pkuides vpu avx512_vmbi2 gfni vaes pclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdkg lalgr dpdpd md_clear pconfig flush_1id arch_capabilities</td>
</tr>
</tbody>
</table>

(Continued on next page)
Dell Inc. PowerEdge MX750c (Intel Xeon Gold 6314U, 2.30 GHz)

SPECrater®2017_int_base = 220
SPECrater®2017_int_peak = 229

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Apr-2021
Hardware Availability: Apr-2021
Software Availability: Feb-2021

Platform Notes (Continued)

/proc/cpuinfo cache data
    cache size : 49152 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
    physical chip.
available: 2 nodes (0-1)
    node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46 48 50
    52 54 56 58 60 62
    node 0 size: 248568 MB
    node 0 free: 247241 MB
    node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47 49 51
    53 55 57 59 61 63
    node 1 size: 249052 MB
    node 1 free: 257291 MB
    node distances:
        node 0   1
        0:  10  11
        1:  11  10

From /proc/meminfo
    MemTotal:       527807184 kB
    HugePages_Total:       0
    Hugepagesize:       2048 kB

/sbin/tuned-adm active
    Current active profile: throughput-performance

From /etc/*release*/etc/*version*
    os-release:
        NAME="Red Hat Enterprise Linux"
        VERSION="8.3 (Ootpa)"
        ID="rhel"
        ID_LIKE="fedora"
        VERSION_ID="8.3"
        PLATFORM_ID="platform:el8"
        PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
        ANSI_COLOR="0;31"
    redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
    system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
    Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
    x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

(Continued on next page)
**SPEC CPU®2017 Integer Rate Result**

**Dell Inc.**

PowerEdge MX750c (Intel Xeon Gold 6314U, 2.30 GHz)

---

<table>
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**SPECrate®2017_int_base = 220**

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---

### Platform Notes (Continued)

- **CVE-2018-12207 (iTLB Multihit):** Not affected
- **CVE-2018-3620 (L1 Terminal Fault):** Not affected
- **Microarchitectural Data Sampling:** Not affected
- **CVE-2017-5754 (Meltdown):** Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- **CVE-2018-3639 (Speculative Store Bypass):** Mitigation: usercopy/swaps barriers and __user pointer sanitation
- **CVE-2017-5753 (Spectre variant 1):** Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
- **CVE-2017-5715 (Spectre variant 2):**
- **CVE-2020-0543 (Special Register Buffer Data Sampling):** Not affected
- **CVE-2019-11135 (TSX Asynchronous Abort):** Not affected

---

```plaintext
run-level 3 Apr 3 19:15 last=5

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.7-ic2021.1
Filesystem  Type      Size  Used Avail Use% Mounted on
            tmpfs        tmpfs  125G   4.4G  121G   4% /mnt/ramdisk
```

From `/sys/devices/virtual/dmi/id`

- **Vendor:** Dell Inc.
- **Product:** PowerEdge MX750c
- **Product Family:** PowerEdge
- **Serial:** 1234567

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

**Memory:**
- 8x 00AD063200AD HMAA8GR7AJR4N-XN 64 GB 2 rank 3200
- 24x Not Specified Not Specified

**BIOS:**
- **BIOS Vendor:** Dell Inc.
- **BIOS Version:** 1.1.0
- **BIOS Date:** 03/25/2021
- **BIOS Revision:** 1.1

(End of data from sysinfo program)
Dell Inc.

PowerEdge MX750c (Intel Xeon Gold 6314U, 2.30 GHz)

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**SPECrated®2017_int_base = 220**

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**Compiler Version Notes**

---

C | 500.perlbench_r(peak) 557.xz_r(peak)
---

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

C | 502.gcc_r(peak)
---

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version
2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
| 525.x264_r(base, peak) 557.xz_r(base)
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
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---

C | 500.perlbench_r(peak) 557.xz_r(peak)
---

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
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C | 502.gcc_r(peak)
---

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version
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C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
| 525.x264_r(base, peak) 557.xz_r(base)
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113

(Continued on next page)
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Software Availability: Feb-2021  

Compiler Version Notes (Continued)

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
| C       | 500.perlbench_r(peak) 557.xz_r(peak)                                |
|-----------------------------------------|
| Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000 |
| Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |
------------------------------------------------------------------------------
| C       | 502.gcc_r(peak)                                         |
|-----------------------------------------|
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113 |
| Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |
------------------------------------------------------------------------------
| C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base) |
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| Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113 |
| Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |
------------------------------------------------------------------------------
| C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak) |
|-----------------------------------------|
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113 |
| Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |
------------------------------------------------------------------------------
| Fortran | 548.exchange2_r(base, peak) |
|-----------------------------------------|
| Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000 |
| Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |
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**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

### Base Compiler Invocation

**C benchmarks:**  
icx

**C++ benchmarks:**  
icpx

**Fortran benchmarks:**  
ifort

### Base Portability Flags

- perlbench_r: $-DSPEC_LP64$ $-DSPEC_LINUX_X64$
- gcc_r: $-DSPEC_LP64$
- mcf_r: $-DSPEC_LP64$
- omnetpp_r: $-DSPEC_LP64$
- xalancbmk_r: $-DSPEC_LP64$ $-DSPEC_LINUX$
- x264_r: $-DSPEC_LP64$
- deepsjeng_r: $-DSPEC_LP64$
- leela_r: $-DSPEC_LP64$
- exchange2_r: $-DSPEC_LP64$
- xz_r: $-DSPEC_LP64$

### Base Optimization Flags

**C benchmarks:**  
-w $-std=c11$ $-m64$ $-Wl,-z,muldefs$ $-xCORE-AVX512$ $-O3$ $-ffast-math$  
-flto $-mfpmath=sse$ $-funroll-loops$ $-qopt-mem-layout-trans=4$  
-mbranches-within-32B-boundaries  
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin  
-lqkmalloc

**C++ benchmarks:**  
-w $-m64$ $-Wl,-z,muldefs$ $-xCORE-AVX512$ $-O3$ $-ffast-math$ $-flto$  
-mfpmath=sse $-funroll-loops$ $-qopt-mem-layout-trans=4$  
-mbranches-within-32B-boundaries  
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin  
-lqkmalloc

**Fortran benchmarks:**  
-w $-m64$ $-Wl,-z,muldefs$ $-xCORE-AVX512$ $-O3$ $-ipo$ $-no-prec-div$  
-qopt-mem-layout-trans=4 $-nostandard-realloc-lhs$ $-align array32byte$  
-auto $-mbranches-within-32B-boundaries$

(Continued on next page)
# SPEC CPU®2017 Integer Rate Result

## Dell Inc.

PowerEdge MX750c (Intel Xeon Gold 6314U, 2.30 GHz)

<table>
<thead>
<tr>
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<td>SPECrate®2017_int_peak</td>
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<tr>
<td>Software Availability</td>
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## Base Optimization Flags (Continued)

Fortran benchmarks (continued):
- L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
- lqkmalloc

## Peak Compiler Invocation

C benchmarks (except as noted below):
- icx
- 500.perlbench_r: icc
- 557.xz_r: icc

C++ benchmarks:
- icpx

Fortran benchmarks:
- ifort

## Peak Portability Flags

- 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
- 502.gcc_r: -D_FILE_OFFSET_BITS=64
- 505.mcf_r: -DSPEC_LP64
- 520.omnetpp_r: -DSPEC_LP64
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
- 525.x264_r: -DSPEC_LP64
- 531.deepsjeng_r: -DSPEC_LP64
- 541.leela_r: -DSPEC_LP64
- 548.exchange2_r: -DSPEC_LP64
- 557.xz_r: -DSPEC_LP64

## Peak Optimization Flags

C benchmarks:
- 500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
- -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4 -fno-strict-overflow
- -mbranches-within-32B-boundaries

(Continued on next page)
Peak Optimization Flags (Continued)

500.perlbench_r (continued):
-1/L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-1qkmalloc

502.gcc_r: -m32
-1/L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass1)
-std=gnu89 -Wl,-z,muldefs -fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -gopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -gopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-1qkmalloc

557.xz_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-gopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-1qkmalloc

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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