## SPEC CPU®2017 Integer Speed Result

**New H3C Technologies Co., Ltd.**

### H3C UniServer R6900 G5 (Intel Xeon Gold 5320H)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base = 11.8</th>
<th>SPECspeed®2017_int_peak = 12.2</th>
</tr>
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<tbody>
<tr>
<td>CPU2017 License: 9066</td>
<td>Test Date: Apr-2021</td>
</tr>
<tr>
<td>Test Sponsor: New H3C Technologies Co., Ltd.</td>
<td>Hardware Availability: Sep-2020</td>
</tr>
<tr>
<td>Tested by: New H3C Technologies Co., Ltd.</td>
<td>Software Availability: Dec-2020</td>
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<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
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<tbody>
<tr>
<td>perlbench</td>
<td>80</td>
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<td>8.41</td>
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<tr>
<td>gcc_s</td>
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<td>10.6</td>
<td>11.2</td>
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<tr>
<td>mcf_s</td>
<td>80</td>
<td>9.27</td>
<td>19.7</td>
</tr>
<tr>
<td>omnetpp_s</td>
<td>80</td>
<td>14.7</td>
<td>17.4</td>
</tr>
<tr>
<td>xalancbmk_s</td>
<td>80</td>
<td>6.24</td>
<td>18.0</td>
</tr>
<tr>
<td>x264_s</td>
<td>80</td>
<td>5.29</td>
<td>18.1</td>
</tr>
<tr>
<td>deepsjeng_s</td>
<td>80</td>
<td>6.24</td>
<td>18.1</td>
</tr>
<tr>
<td>leela_s</td>
<td>80</td>
<td>5.29</td>
<td>18.1</td>
</tr>
<tr>
<td>exchange2_s</td>
<td>80</td>
<td>6.24</td>
<td>18.1</td>
</tr>
<tr>
<td>xz_s</td>
<td>80</td>
<td>5.29</td>
<td>18.1</td>
</tr>
</tbody>
</table>

### Hardware

**CPU Name:** Intel Xeon Gold 5320H  
**Max MHz:** 4200  
**Nominal:** 2400  
**Enabled:** 80 cores, 4 chips  
**Orderable:** 1,2,3,4 chips  
**Cache L1:** 32 KB I + 32 KB D on chip per core  
**L2:** 1 MB I+D on chip per core  
**L3:** 27.5 MB I+D on chip per chip  
**Memory:** 768 GB (48 x 16 GB 2Rx8 PC4-3200V-R, running at 2666)  
**Storage:** 1 x 1.0 TB SATA SSD  
**Other:** None

### Software

**OS:** Red Hat Enterprise Linux release 8.2 (Ootpa)  
**Compiler:** C/C++: Version 2021.1 of Intel oneAPI  
**Firmware:** Version 5.15 released Mar-2021 BIOS  

**Parallel:** Yes  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 64-bit  
**Peak Pointers:** 64-bit  
**Power Management:** BIOS set to prefer performance at the cost of additional power usage.
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>80</td>
<td>250</td>
<td>7.09</td>
<td>251</td>
<td>7.07</td>
<td>251</td>
<td>7.08</td>
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<td>602gcc_s</td>
<td>80</td>
<td>379</td>
<td>10.5</td>
<td>375</td>
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<td>605.mcf_s</td>
<td>80</td>
<td>240</td>
<td>19.7</td>
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<td>19.7</td>
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<tr>
<td>620.omnetpp_s</td>
<td>80</td>
<td>173</td>
<td>9.44</td>
<td>176</td>
<td>9.27</td>
<td>177</td>
<td>9.22</td>
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<tr>
<td>623.xalanchmk_s</td>
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<td>99.7</td>
<td>14.2</td>
<td>96.2</td>
<td>14.7</td>
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<td></td>
<td>96.2</td>
<td>14.7</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>80</td>
<td>102</td>
<td>17.3</td>
<td>101</td>
<td>17.4</td>
<td>101</td>
<td>17.4</td>
<td></td>
<td></td>
<td>98.0</td>
<td>18.0</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>80</td>
<td>323</td>
<td>5.29</td>
<td>322</td>
<td>5.29</td>
<td>336</td>
<td>5.07</td>
<td></td>
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<td>322</td>
<td>5.29</td>
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<tr>
<td>648.exchange2_s</td>
<td>80</td>
<td>162</td>
<td>18.1</td>
<td>162</td>
<td>18.1</td>
<td>163</td>
<td>18.0</td>
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<td></td>
<td>162</td>
<td>18.1</td>
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<td>657.xz_s</td>
<td>80</td>
<td>239</td>
<td>25.9</td>
<td>239</td>
<td>25.9</td>
<td>239</td>
<td>25.9</td>
<td></td>
<td></td>
<td>239</td>
<td>25.9</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3>/proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the Redhat Enterprise 7.5, and the system compiler gcc 4.8.5

(Continued on next page)
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SPECspeed\textsuperscript{\textregistered}2017\textunderscore int\_peak = 12.2
SPECspeed\textsuperscript{\textregistered}2017\textunderscore int\_base = 11.8

General Notes (Continued)


Platform Notes

BIOS Settings:
Set Hyper-Threading to Disabled
Set Power Performance Tuning to BIOS Controls EPB
Set Energy Performance BIAS to Performance
Set Patrol Scrub to Disabled

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Fri Apr 23 15:55:36 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5320H CPU @ 2.40GHz
  4 "physical id"s (chips)
  80 "processors"
core, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 20
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 2: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 3: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 80
On-line CPU(s) list: 0-79
Thread(s) per core: 1
Core(s) per socket: 20
Socket(s): 4
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5320H CPU @ 2.40GHz
Stepping: 11

(Continued on next page)
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### Platform Notes (Continued)

```plaintext
CPU MHz:            3522.287  
CPU max MHz:        4200.000  
CPU min MHz:        1000.0000  
BogoMIPS:            4800.00  
Virtualization:     VT-x  
L1d cache:           32K  
L1i cache:           32K  
L2 cache:            1024K  
L3 cache:            28160K  
NUMA node0 CPU(s):   0-19  
NUMA node1 CPU(s):   20-39  
NUMA node2 CPU(s):   40-59  
NUMA node3 CPU(s):   60-79  
Flags:               fpu vme de pse sc sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrnd lahf_lm abm 3dnowprefetch cpuid_fault ebti cat_l3 cdp_l3 invpcid_single intel_pni ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512v1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local avx512_bf16d xsaveopt xsavec xsaveopt xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local avx512_bf16d xsaveopt xsavec xsaveopt xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local avx512_bf16d xsaveopt xsavec xsaveopt xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local avx512_bf16d xsaveopt xsavec xsaveopt xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local avx512_bf16d xsaveopt xsavec xsaveopt xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local avx512_bf16d xsaveopt xsavec xsaveopt xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local avx512_bf16d xsaveopt xsavec xsaveopt xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local avx512_bf16d xsaveopt xsavec xsaveopt xsaves cqm_llc cqm_occup_llc cqm_mbb_total
data
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19
node 0 size: 191856 MB
node 0 free: 191024 MB
node 1 cpus: 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39
node 1 size: 193503 MB
node 1 free: 193229 MB
node 2 cpus: 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59
node 2 size: 193531 MB
node 2 free: 193330 MB
node 3 cpus: 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79
node 3 size: 193530 MB
node 3 free: 193171 MB
node distances:
node 0 1 2 3
0: 10 20 20 20
```
## Platform Notes (Continued)

1:  20  10  20  20  20
2:  20  20  10  20
3:  20  20  20  10

From /proc/meminfo
- MemTotal: 790960636 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

/sbin/tuned-adm active
- Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
- os-release:
  - NAME="Red Hat Enterprise Linux"
  - VERSION="8.2 (Ootpa)"
  - ID=rhel
  - ID_LIKE="fedora"
  - VERSION_ID="8.2"
  - PLATFORM_ID="platform:el8"
  - PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
  - ANSI_COLOR="0;31"
  - redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
  - system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
  - system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
- Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
  - x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

- CVE-2018-12207 (iTLB Multihit): Not affected
- CVE-2018-3620 (L1 Terminal Fault): Not affected
- Microarchitectural Data Sampling: Not affected
- CVE-2017-5754 (Meltdown): Not affected
- CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and __user pointer sanitization
- CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

(Continued on next page)
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Platform Notes (Continued)

CVE-2020-0543 (Special Register Buffer Data Sampling): No status reported
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Apr 23 15:54

SPEC is set to: /home/spec.cpu
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 876G 263G 614G 30% /home

From /sys/devices/virtual/dmi/id
Product Family: SYSTEM_FAMILY

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory: 48x Micron 18ASF2G72PDZ-3G2E1 16 GB 2 rank 3200, configured at 2666

BIOS:
BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 5.15
BIOS Date: 03/01/2021
BIOS Revision: 5.19

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C | 600.perlbench_s(peak)
==============================================================================
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
| 625.x264_s(base, peak) 657.xz_s(base, peak)
==============================================================================
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

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<th>Benchmark(s)</th>
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<tbody>
<tr>
<td>C</td>
<td>600.perlbench_s(peak)</td>
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</table>
|          | Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |
| C        | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak) |
|          | Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |
| C++      | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak) |
|          | Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |
| Fortran  | 648.exchange2_s(base, peak) |
|          | Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |

### Base Compiler Invocation

**C benchmarks:**
- icx

**C++ benchmarks:**
- icpx

**Fortran benchmarks:**
- ifort
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Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-03 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-lqkmalloc

Fortran benchmarks:
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries

Peak Compiler Invocation

C benchmarks (except as noted below):
icx

600.perlbench_s: icc

C++ benchmarks:
icpx

(Continued on next page)
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**Peak Compiler Invocation (Continued)**

**Fortran benchmarks:**

```plaintext
ifort
```

**Peak Portability Flags**

Same as Base Portability Flags

**Peak Optimization Flags**

**C benchmarks:**

```plaintext
600.perlb benchmark_s: -Wl,-z, muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z, muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z, muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes
```

**C++ benchmarks:**

```plaintext
620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes
```

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

New H3C Technologies Co., Ltd.

H3C UniServer R6900 G5 (Intel Xeon Gold 5320H)

SPECspeed®2017_int_base = 11.8
SPECspeed®2017_int_peak = 12.2

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Test Date: Apr-2021
Tested by: New H3C Technologies Co., Ltd.
Hardware Availability: Sep-2020
Tested by: New H3C Technologies Co., Ltd.
Software Availability: Dec-2020

Peak Optimization Flags (Continued)

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-CPX-RevC.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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