# SPEC CPU®2017 Floating Point Speed Result

## Dell Inc.
**PowerEdge MX750c (Intel Xeon Platinum 8352V, 2.10 GHz)**

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base</th>
<th>124</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_fp_peak</td>
<td>127</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55  
**Test Date:** Apr-2021  
**Test Sponsor:** Dell Inc.  
**Hardware Availability:** Apr-2021  
**Tested by:** Dell Inc.  
**Software Availability:** Dec-2020

### Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_fp_base</th>
<th>SPECspeed®2017_fp_peak</th>
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<tr>
<td>603.bwaves_s</td>
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</tr>
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</tr>
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<td>638.imagick_s</td>
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</tr>
<tr>
<td>644.nab_s</td>
<td>36</td>
<td></td>
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</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>36</td>
<td></td>
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</tr>
<tr>
<td>654.roms_s</td>
<td>36</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### Hardware

**CPU Name:** Intel Xeon Platinum 8352V  
**Max MHz:** 3500  
**Nominal:** 2100  
**Enabled:** 36 cores, 1 chip  
**Orderable:** 1 chip  
**Cache L1:** 32 KB I + 48 KB D on chip per core  
**L2:** 1.25 MB I+D on chip per core  
**L3:** 54 MB I+D on chip per chip  
**Other:** None  
**Memory:** 512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R, running at 2933)  
**Storage:** 125 GB on tmpfs  
**Other:** None

### Software

**OS:** Red Hat Enterprise Linux 8.2 (Ootpa)  
**Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux

**Parallel:** Yes  
**Firmware:** Version 1.1.2 released Apr-2021  
**File System:** tmpfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 64-bit  
**Peak Pointers:** 64-bit  
**Other:** jemalloc memory allocator V5.0.1  
**Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Seconds</th>
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<td>71.5</td>
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</tbody>
</table>

SPECspeed\textsuperscript{2017\_fp\_base} = 124
SPECspeed\textsuperscript{2017\_fp\_peak} = 127

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

---

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

- KMP\_AFFINITY = "granularity=fine,compact"
- LD\_LIBRARY\_PATH = 
  
  mnt/ramdisk/cpu2017-1.1.7-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/je5.0.1-64

- MALLOCS\_CONF = "retain:true"
- OMP\_STACKSIZE = "192M"

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop_caches


(Continued on next page)
Dell Inc.
PowerEdge MX750c (Intel Xeon Platinum 8352V, 2.10 GHz)

General Notes (Continued)

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:
Logical Processor : Disabled
Virtualization Technology : Disabled

System Profile : Custom
CPU Power Management : Maximum Performance
CIE : Disabled
C States : Autonomous
Memory Patrol Scrub : Disabled
Energy Efficiency Policy : Performance
CPU Interconnect Bus Link
Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.7-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Mon Apr 26 19:59:44 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8352V CPU @ 2.10GHz
  1 "physical id"s (chips)
  36 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 36
siblings : 36
physical 0: cores 0 1 2 3 4 5 6 7 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35

From lscpu:
Architecture: x86_64

(Continued on next page)
Dell Inc.
PowerEdge MX750c (Intel Xeon Platinum 8352V, 2.10 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

SPECspeed®2017_fp_base = 124
SPECspeed®2017_fp_peak = 127

CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 36
On-line CPU(s) list: 0-35
Thread(s) per core: 1
Core(s) per socket: 36
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Platinum 8352V CPU @ 2.10GHz
Stepping: 6
CPU MHz: 2599.099
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 55296K
NUMA node0 CPU(s): 0-35

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrp pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd
mba ibrs ibpb stibp ibrs Enhanced tpr_shadow vmni flexpriority ept vpid fsbsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erm sipvcd single ssbd mba ibrs ibpb stibp ibrs Enhanced tpr_shadow vmni flexpriority ept vpid fsbsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erm sipvcd single ssbd mba ibrs ibpb stibp ibrs Enhanced tpr_shadow vmni flexpriority ept vpid fsbsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erm sipvcd single ssbd mba ibrs ibpb stibp ibrs Enhanced tpr_shadow vmni flexpriority ept vpid fsbsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erm sipvcd single ssbd mba ibrs ibpb stibp ibrs Enhanced tpr_shadow vmni flexpriority ept vpid fsbsbase

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 1 nodes (0)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
28 29 30 31 32 33 34 35
node 0 size: 515452 MB
node 0 free: 499137 MB
node distances:
node 0

(Continued on next page)
**SPEC CPU®2017 Floating Point Speed Result**

**Dell Inc.**

PowerEdge MX750c (Intel Xeon Platinum 8352V, 2.10 GHz)

| SPECspeed®2017_fp_base = 124 | SPECspeed®2017_fp_peak = 127 |

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

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**Platform Notes (Continued)**

0: 10

From /proc/meminfo
- MemTotal: 527823464 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

From /etc/*release* /etc/*version*
- os-release:
  - NAME="Red Hat Enterprise Linux"
  - VERSION="8.2 (Ootpa)"
  - ID=rhel
  - ID_LIKE="fedora"
  - VERSION_ID="8.2"
  - PLATFORM_ID="platform:el8"
  - PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
  - ANSI_COLOR="0;31"
- redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
- system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
- system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
- Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

| CVE-2018-12207 (iTLB Multihit): | Not affected |
| CVE-2018-3620 (L1 Terminal Fault): | Not affected |
| Microarchitectural Data Sampling: | Not affected |
| CVE-2017-5754 (Meltdown): | Mitigation: Speculative Store Bypass disabled via prctl and seccomp |
| CVE-2018-3639 (Speculative Store Bypass): | Mitigation: usercopy/swaps barriers and __user pointer sanitization |
| CVE-2017-5753 (Spectre variant 1): | Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling |
| CVE-2017-5715 (Spectre variant 2): | No status reported |
| CVE-2020-0543 (Special Register Buffer Data Sampling): | Not affected |
| CVE-2019-11135 (TSX Asynchronous Abort): | |

run-level 3 Apr 26 17:14

(Continued on next page)
Dell Inc.  
PowerEdge MX750c (Intel Xeon Platinum 8352V, 2.10 GHz)  

SPECspeed®2017_fp_base = 124  
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Platform Notes (Continued)

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.7-ic2021.1  
Filesystem Type Size Used Avail Use% Mounted on  
tmpfs tmpfs 125G 11G 115G 9% /mnt/ramdisk

From /sys/devices/virtual/dmi/id  
Vendor: Dell Inc.  
Product: PowerEdge MX750c  
Product Family: PowerEdge  
Serial: 1234567

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
12x 00AD063200AD HMA84GR7CJR4N-XN 32 GB 2 rank 3200, configured at 2933  
4x 00AD063200AD HMAA4GR7AJR8N-XN 32 GB 2 rank 3200, configured at 2933  
16x Not Specified Not Specified

BIOS:  
BIOS Vendor: Dell Inc.  
BIOS Version: 1.1.2  
BIOS Date: 04/09/2021  
BIOS Revision: 1.1

(End of data from sysinfo program)

Compiler Version Notes

C | 619.lbm_s(base, peak) 638.imagick_s(base, peak)  
644.nab_s(base)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)  
64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C | 644.nab_s(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

(Continued on next page)
Compiler Version Notes (Continued)

```
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base)

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Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---------------------------------------------
C               | 644.nab_s(peak)

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---------------------------------------------
C++, C, Fortran | 607.cactuBSSN_s(base, peak)

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Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---------------------------------------------
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)

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Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)

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Copyright 2017-2021 Standard Performance Evaluation Corporation

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Compiler Version Notes (Continued)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries

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---

### Base Optimization Flags (Continued)

Fortran benchmarks:
- `-m64`  
- `-Wl,-z,muldefs`  
- `-DSPEC_OPENMP`  
- `-xCORE-AVX512`  
- `-ipo`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-mbranches-within-32B-boundaries`  
- `-L/usr/local/jemalloc64-5.0.1/lib`  
- `-ljemalloc`

Benchmarks using both Fortran and C:
- `-m64`  
- `-std=c11`  
- `-Wl,-z,muldefs`  
- `-xCORE-AVX512`  
- `-ipo`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=4`  
- `-qopenmp`  
- `-DSPEC_OPENMP`  
- `-mbranches-within-32B-boundaries`  
- `-nostandard-realloc-lhs`  
- `-L/usr/local/jemalloc64-5.0.1/lib`  
- `-ljemalloc`

Benchmarks using Fortran, C, and C++:
- `-m64`  
- `-std=c11`  
- `-Wl,-z,muldefs`  
- `-xCORE-AVX512`  
- `-ipo`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=4`  
- `-qopenmp`  
- `-DSPEC_OPENMP`  
- `-mbranches-within-32B-boundaries`  
- `-nostandard-realloc-lhs`  
- `-L/usr/local/jemalloc64-5.0.1/lib`  
- `-ljemalloc`

---

### Peak Compiler Invocation

C benchmarks (except as noted below):
- `icc`

644.nab_s: `icx`

Fortran benchmarks:
- `ifort`

Benchmarks using both Fortran and C:
- `ifort icc`

Benchmarks using Fortran, C, and C++:
- `icpc icc ifort`

---

### Peak Portability Flags

Same as Base Portability Flags
Dell Inc.
PowerEdge MX750c (Intel Xeon Platinum 8352V, 2.10 GHz)

SPECspeed\textsuperscript{\textregistered}2017\_fp\_base = 124
SPECspeed\textsuperscript{\textregistered}2017\_fp\_peak = 127

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Apr-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

Peak Optimization Flags

C benchmarks:

619.lbm\_s: basepeak = yes

638.imagick\_s: basepeak = yes


Fortran benchmarks:

603.bwaves\_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -DSPEC\_SUPPRESS\_OPENMP -DSPEC\_OPENMP -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs -mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

649.fotonik3d\_s: Same as 603.bwaves\_s

654.roms\_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf\_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -DSPEC\_SUPPRESS\_OPENMP -qopenmp -DSPEC\_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

627.cam4\_s: basepeak = yes

628.pop2\_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN\_s: basepeak = yes
## SPEC CPU®2017 Floating Point Speed Result

### Dell Inc.

PowerEdge MX750c (Intel Xeon Platinum 8352V, 2.10 GHz)

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_peak</th>
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<tr>
<td>SPECspeed®2017_fp_base</td>
<td>124</td>
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</tbody>
</table>

**Dell Inc.**

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Apr-2021  
**Hardware Availability:** Apr-2021  
**Software Availability:** Dec-2020

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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