# SPEC CPU®2017 Floating Point Speed Result

## Dell Inc.

PowerEdge C6520 (Intel Xeon Gold 6342, 2.80 GHz)

<table>
<thead>
<tr>
<th>SPECspeed^2017_fp_base</th>
<th>SPECspeed^2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>191</td>
<td>195</td>
</tr>
</tbody>
</table>

### CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

### Test Date: May-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

### Threads

<table>
<thead>
<tr>
<th>SPECspeed^2017_fp_base</th>
<th>SPECspeed^2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td></td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td></td>
</tr>
<tr>
<td>619.lbm_s</td>
<td></td>
</tr>
<tr>
<td>621.wrf_s</td>
<td></td>
</tr>
<tr>
<td>627.cam4_s</td>
<td></td>
</tr>
<tr>
<td>628.pop2_s</td>
<td></td>
</tr>
<tr>
<td>638.imagick_s</td>
<td></td>
</tr>
<tr>
<td>644.nab_s</td>
<td></td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td></td>
</tr>
<tr>
<td>654.roms_s</td>
<td></td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>CPU Name: Intel Xeon Gold 6342</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz: 3500</td>
</tr>
<tr>
<td>Nominal: 2800</td>
</tr>
<tr>
<td>Enabled: 48 cores, 2 chips</td>
</tr>
<tr>
<td>Orderable: 1.2 chips</td>
</tr>
<tr>
<td>Cache L1: 32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td>L2: 1.25 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3: 36 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other: None</td>
</tr>
<tr>
<td>Memory: 512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R)</td>
</tr>
<tr>
<td>Storage: 125 GB on tmpfs</td>
</tr>
<tr>
<td>Other: None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>OS: Red Hat Enterprise Linux 8.3 (Ootpa)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201112 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux</td>
</tr>
<tr>
<td>Parallel: Yes</td>
</tr>
<tr>
<td>Firmware: Version 1.1.2 released Apr-2021</td>
</tr>
<tr>
<td>File System: tmpfs</td>
</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>Peak Pointers: 64-bit</td>
</tr>
<tr>
<td>Other: jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management: BIOS and OS set to prefer performance at the cost of additional power usage</td>
</tr>
</tbody>
</table>
Dell Inc. PowerEdge C6520 (Intel Xeon Gold 6342, 2.80 GHz)

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### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
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<th>Seconds</th>
<th>Ratio</th>
</tr>
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<tbody>
<tr>
<td>603.bwaves_s</td>
<td>48</td>
<td>89.0</td>
<td>663</td>
<td>48</td>
<td>88.6</td>
<td>669</td>
<td>48</td>
<td>88.7</td>
<td>665</td>
<td>48</td>
<td>88.6</td>
<td>669</td>
<td>48</td>
<td>88.7</td>
<td>665</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>48</td>
<td>67.7</td>
<td>246</td>
<td>48</td>
<td>67.7</td>
<td>246</td>
<td>48</td>
<td>68.0</td>
<td>245</td>
<td>48</td>
<td>67.7</td>
<td>246</td>
<td>48</td>
<td>68.0</td>
<td>245</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>48</td>
<td>39.2</td>
<td>134</td>
<td>48</td>
<td>39.2</td>
<td>134</td>
<td>48</td>
<td>41.1</td>
<td>127</td>
<td>48</td>
<td>39.2</td>
<td>134</td>
<td>48</td>
<td>41.1</td>
<td>127</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>48</td>
<td>80.5</td>
<td>164</td>
<td>48</td>
<td>76.8</td>
<td>172</td>
<td>48</td>
<td>76.4</td>
<td>173</td>
<td>48</td>
<td>76.8</td>
<td>172</td>
<td>48</td>
<td>76.4</td>
<td>173</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>48</td>
<td>64.1</td>
<td>138</td>
<td>48</td>
<td>64.1</td>
<td>138</td>
<td>48</td>
<td>63.2</td>
<td>140</td>
<td>48</td>
<td>64.1</td>
<td>138</td>
<td>48</td>
<td>63.2</td>
<td>140</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>48</td>
<td>141</td>
<td>84.4</td>
<td>48</td>
<td>141</td>
<td>84.4</td>
<td>48</td>
<td>140</td>
<td>84.9</td>
<td>48</td>
<td>141</td>
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<td>48</td>
<td>140</td>
<td>84.9</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>48</td>
<td>81.2</td>
<td>178</td>
<td>48</td>
<td>81.2</td>
<td>178</td>
<td>48</td>
<td>81.1</td>
<td>178</td>
<td>48</td>
<td>81.2</td>
<td>178</td>
<td>48</td>
<td>81.1</td>
<td>178</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>48</td>
<td>48.7</td>
<td>359</td>
<td>48</td>
<td>43.2</td>
<td>405</td>
<td>48</td>
<td>43.1</td>
<td>405</td>
<td>48</td>
<td>43.2</td>
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<td>80.4</td>
<td>113</td>
<td>48</td>
<td>79.9</td>
<td>114</td>
<td>48</td>
<td>79.9</td>
<td>114</td>
<td>48</td>
<td>79.9</td>
<td>114</td>
<td>48</td>
<td>79.9</td>
<td>114</td>
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<td>48</td>
<td>68.9</td>
<td>229</td>
<td>48</td>
<td>68.9</td>
<td>229</td>
<td>48</td>
<td>69.4</td>
<td>227</td>
<td>48</td>
<td>68.9</td>
<td>229</td>
<td>48</td>
<td>69.4</td>
<td>227</td>
</tr>
</tbody>
</table>

SPECspeed2017_fp_base = 191
SPECspeed2017_fp_peak = 195

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,compact"
- LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/je5.0.1-64"
- MALLOC_CONF = "retain:true"
- OMP_STACKSIZE = "192M"

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the Redhat Enterprise 7.5, and the system compiler gcc 4.8.5

(Continued on next page)
**SPEC CPU®2017 Floating Point Speed Result**

**Dell Inc.**  
PowerEdge C6520 (Intel Xeon Gold 6342, 2.80 GHz)  

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<td>SPECspeed®2017_fp_base</td>
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<td>Dell Inc.</td>
</tr>
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<td>Tested by:</td>
<td>Dell Inc.</td>
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<td>Test Date:</td>
<td>May-2021</td>
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<td>Apr-2021</td>
</tr>
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<td>Software Availability:</td>
<td>Dec-2020</td>
</tr>
</tbody>
</table>

**General Notes (Continued)**

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

**Platform Notes**

<table>
<thead>
<tr>
<th>BIOS Settings:</th>
</tr>
</thead>
<tbody>
<tr>
<td>Logical Processor : Disabled</td>
</tr>
<tr>
<td>Virtualization Technology : Disabled</td>
</tr>
<tr>
<td>System Profile : Custom</td>
</tr>
<tr>
<td>CPU Power Management : Maximum Performance</td>
</tr>
<tr>
<td>C1E : Disabled</td>
</tr>
<tr>
<td>C States : Autonomous</td>
</tr>
<tr>
<td>Memory Patrol Scrub : Disabled</td>
</tr>
<tr>
<td>Energy Efficiency Policy : Performance</td>
</tr>
<tr>
<td>CPU Interconnect Bus Link Power Management : Disabled</td>
</tr>
</tbody>
</table>

Sysinfo program /mnt/ramdisk/cpu2017-1.1.7-ic2021.1/bin/sysinfo  
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c  
running on localhost.localdomain Wed May  5 13:11:37 2021

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo  
```
model name : Intel(R) Xeon(R) Gold 6342 CPU @ 2.80GHz
2  "physical id"s (chips)
48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 24
physical 0: cores 0 1 10 11 12 13 14 15 16 17 18 19 20 21 22 23
physical 1: cores 0 1 10 11 12 13 14 15 16 17 18 19 20 21 22 23
```

From lscpu:  
```
Architecture: x86_64
```
Platform Notes (Continued)

CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 1
Core(s) per socket: 24
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6342 CPU @ 2.80GHz
Stepping: 6
CPU MHz: 2226.491
BogoMIPS: 5600.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 36864K
NUMA node0 CPU(s): 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46
NUMA node1 CPU(s): 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdemsg rdtscp lm constant_time art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitoring ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrunc pdcm pccid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault ebpx cat_l3 invpcid_single intel_p sui sbd mba ibrs ibpb stibp ibrs_enhanced fsgsbse tsc_adjust bmi1 hle avx2 smep bmi2 3msr invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl xsaveopt xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local split_lock_detect wbinvd dtherm ida arat pnr pms avx512vbmi umip pku ospke avx512_vbmi2 gfnl vaes vpclmulqd avx512_vnni avx512_binary tme avx512_vpopt swap option 1a57 rpdpd md_clear pconfig flush_lld arch_capabilities

/proc/cpuinfo cache data
Cache size: 36864 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46
node 0 size: 247179 MB
node 0 free: 254259 MB

(Continued on next page)
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Dell Inc. PowerEdge C6520 (Intel Xeon Gold 6342, 2.80 GHz)

SPECspeed®2017_fp_base = 191
SPECspeed®2017_fp_peak = 195

CPU2017 License: 55
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Platform Notes (Continued)
	node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47
	node 1 size: 248695 MB
	node 1 free: 244880 MB
	node distances:

0: 10 20
1: 20 10

From /proc/meminfo

MemTotal: 527809648 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active

Current active profile: throughput-performance

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.3 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swapsgs barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1): Mitigation: Enhanced IBRS, IBPB:
## Platform Notes (Continued)

- CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
- CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 May 5 10:31

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.7-ic2021.1

```
Filesystem   Type     Size  Used  Avail Use% Mounted on
tmpfs        tmpfs    125G   11G   115G   9%  /mnt/ramdisk
```

From /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge C6520
Product Family: PowerEdge

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
- 6x 00AD063200AD HMA84GR7CJR4N-XN 32 GB 2 rank 3200
- 10x 00AD063200AD HMAA4GR7AJR8N-XN 32 GB 2 rank 3200

BIOS:
- BIOS Vendor: Dell Inc.
- BIOS Version: 1.1.2
- BIOS Date: 04/09/2021
- BIOS Revision: 1.1

(End of data from sysinfo program)

## Compiler Version Notes

```
C                    | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base)
```

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

```
C                    | 644.nab_s(peak)
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Dell Inc.

PowerEdge C6520 (Intel Xeon Gold 6342, 2.80 GHz)

---

**Compiler Version Notes (Continued)**

Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base)
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C               | 644.nab_s(peak)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
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Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
(Continued on next page)
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**Compiler Version Notes (Continued)**

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)  
64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

**Base Compiler Invocation**

C benchmarks:  
icc

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
ifort icc

Benchmarks using Fortran, C, and C++:  
icpc icc ifort

**Base Portability Flags**

603.bwaves_s: -DSPEC_LP64  
607.cactuBSSN_s: -DSPEC_LP64  
619.lbm_s: -DSPEC_LP64  
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian  
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG  
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl  
638.imagick_s: -DSPEC_LP64  
644.nab_s: -DSPEC_LP64  
649.fotonik3d_s: -DSPEC_LP64  
654.roms_s: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:  
-m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

(Continued on next page)
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Base Optimization Flags (Continued)

C benchmarks (continued):
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries

Fortran benchmarks:
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
-mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

Benchmarks using both Fortran and C:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

644.nab_s: icx

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort
SPEC CPU®2017 Floating Point Speed Result

Dell Inc.
PowerEdge C6520 (Intel Xeon Gold 6342, 2.80 GHz)

SPECspeed®2017_fp_base = 191
SPECspeed®2017_fp_peak = 195

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: May-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
619.lbm_s: basepeak = yes
638.imagick_s: basepeak = yes
644.nab_s: -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -fiopenmp
-DSPEC_OPENMP -qopt-mem-layout-trans=4
-fimf-accuracy-bits=14:sqrt
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:
603.bwaves_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX512
-03 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
649.fotonik3d_s: Same as 603.bwaves_s
654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:
621.wrf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)
-prof-use(pass 2) -ipo -xCORE-AVX512 -03 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
627.cam4_s: basepeak = yes
628.pop2_s: basepeak = yes

(Continued on next page)
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Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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