### SPEC CPU®2017 Integer Speed Result

**New H3C Technologies Co., Ltd.**

**H3C UniServer R4900 G5 (Intel Xeon Gold 5320)**

**SPECspeed®2017_int_base = 9.75**

**SPECspeed®2017_int_peak = 9.96**

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed®2017_int_base (9.75)</th>
<th>SPECspeed®2017_int_peak (9.96)</th>
</tr>
</thead>
</table>

#### Hardware

- **CPU Name:** Intel Xeon Gold 5320
- **Max MHz:** 3400
- **Nominal:** 2200
- **Enabled:** 52 cores, 2 chips
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 48 KB D on chip per core
- **L2:** 1.25 MB I+D on chip per core
- **L3:** 39 MB I+D on chip per core
- **Other:** None
- **Memory:** 1 TB (32 x 32 GB 2Rx8 PC4-3200V-R, running at 2933)
- **Storage:** 1 x 480 GB SATA SSD
- **Other:** None

#### Software

- **OS:** Red Hat Enterprise Linux release 8.3 (Ootpa) 4.18.0-240.el8.x86_64
- **Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;
  C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux;
- **Parallel:** Yes
- **Firmware:** Version 5.23 released Apr-2021
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage
New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G5 (Intel Xeon Gold 5320)

CPU2017 License: 9066  
Test Sponsor: New H3C Technologies Co., Ltd.  
Tested by: New H3C Technologies Co., Ltd.

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>600.perlbench_s</td>
<td>52</td>
<td>304</td>
<td>5.83</td>
<td>305</td>
<td>5.83</td>
<td>306</td>
<td>5.81</td>
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<td>264</td>
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<td>602.gcc_s</td>
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<td>432</td>
<td>9.21</td>
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<td>16.7</td>
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<td>16.6</td>
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<td>620.omnetpp_s</td>
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<td>164</td>
<td>9.96</td>
<td>166</td>
<td>9.84</td>
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<td>164</td>
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<td>623.xalanchmk_s</td>
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<td>10.8</td>
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<td>13.8</td>
<td>128</td>
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<td>13.8</td>
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<td>122</td>
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<td>641.leela_s</td>
<td>52</td>
<td>439</td>
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<td>648.exchange2_s</td>
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<td>191</td>
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<td>191</td>
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<td>657.xz_s</td>
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<td>20.3</td>
<td>304</td>
<td>20.3</td>
<td>52</td>
<td>304</td>
<td>20.3</td>
<td>304</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/spec2017/lib/intel64:/home/spec2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
```
sync; echo 3>/proc/sys/vm/drop_caches
```
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

(Continued on next page)
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Test Date: May-2021
Tested by: New H3C Technologies Co., Ltd.
Hardware Availability: Jun-2021
Software Availability: Dec-2020

general notes (continued)

platform notes

bios settings:
set hyper-threading to disabled
set patrol scrub to disabled

sysinfo program /home/spec2017/bin/sysinfo
rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aca56c6d
running on localhost.localdomain Sat May 22 14:40:38 2021

sut (system under test) info as seen by some common utilities.
for more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

from /proc/cpuinfo
model name: Intel(R) Xeon(R) Gold 5320 CPU @ 2.20GHz
  2 "physical id"s (chips)
  52 "processors"
cores, siblings (caution: counting these is hw and system dependent. the following
excerpts from /proc/cpuinfo might not be reliable. use with caution.)
cpu cores: 26
siblings : 26
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25

from lscpu from util-linux 2.32.1:
architecture: x86_64
cpu op-mode(s): 32-bit, 64-bit
byte order: little endian
cpu(s): 52
on-line cpu(s) list: 0-51
thread(s) per core: 1
core(s) per socket: 26
socket(s): 2
numa node(s): 2
vendor id: genuineintel
cpu family: 6
model: 106
model name: Intel(R) Xeon(R) Gold 5320 CPU @ 2.20GHz
stepping: 6
cpu mhz: 2800.000
cpu max mhz: 3400.0000
(continued on next page)
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Platform Notes (Continued)

CPU min MHz: 800.0000
BogoMIPS: 4400.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 39936K
NUMA node0 CPU(s): 0-25
NUMA node1 CPU(s): 26-51
Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 cfe flush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrm pdcm pcd dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single intel_pni ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad fsbg base tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl xsaveopt xsave xaxev xgetbv1 xsave v gfni vaes vpmulldq avx512_vnni avx512_vbmi umip pk u ospk avx512_vbmi2 gfn vaes vpmulldq avx512_vnni avx512_vbmi2 xsave avx512_vpopcntdq la57 rdpid md_clear pconfig flush_lld arch_capabilities

/cache/data

WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
node 0 size: 494924 MB
node 0 free: 514296 MB
node 1 cpus: 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51
node 1 size: 496659 MB
node 1 free: 515263 MB
node distances:
node 0 1
0: 10 20
1: 20 10

From /proc/meminfo
MemTotal: 1056229364 KB
HugePages_Total: 0
Hugepagesize: 2048 KB

/sbin/tuned-adm active

(Continued on next page)
Platform Notes (Continued)

It seems that tuned daemon is not running, preset profile is not activated.

Preset profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.3 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga
uname -a:
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):
Not affected
CVE-2018-3620 (L1 Terminal Fault):
Not affected
Microarchitectural Data Sampling:
Not affected
CVE-2017-5754 (Meltdown):
Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass):
Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1):
Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2017-5715 (Spectre variant 2):
Not affected
CVE-2020-0543 (Special Register Buffer Data Sampling):
Not affected
CVE-2019-11135 (TSX Asynchronous Abort):
Not affected

run-level 3 May 22 14:39

SPEC is set to: /home/spec2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 372G 95G 278G 26% /home

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**Platform Notes (Continued)**

From /sys/devices/virtual/dmi/id

| Vendor: New H3C Technologies Co., Ltd. |
| Product: H3C UniServer R4900 G5 |
| Product Family: Rack |
| Serial: 210235A2RBH214000004 |

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
32x Micron 36ASF4G72PZ-3G2E7 32 GB 2 rank 3200, configured at 2933

BIOS:
| BIOS Vendor: American Megatrends International, LLC. |
| BIOS Version: 5.23 |
| BIOS Date: 04/23/2021 |
| BIOS Revision: 5.21 |

(End of data from sysinfo program)

---

**Compiler Version Notes**

```plaintext
C       | 600.perlbench_s(peak)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
| 625.x264_s(base, peak) 657.xz_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C       | 600.perlbench_s(peak)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

(Continued on next page)
Compiler Version Notes (Continued)

------------------------------------------------------------------------------
| C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) |
|         | 625.x264_s(base, peak) 657.xz_s(base, peak)                        |
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
| C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) |
|         | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)       |
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
| Fortran | 648.exchange2_s(base, peak) |
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Base Portability Flags

600.perlbench_s: -DSPC_LP64 -DSPC_LINUX_X64
602.gcc_s: -DSPC_LP64

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**Base Portability Flags (Continued)**

605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

---

**Base Optimization Flags**

C benchmarks:
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-1/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-1/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-1qkmalloc

Fortran benchmarks:
-m64 -xcORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries

---

**Peak Compiler Invocation**

C benchmarks (except as noted below):
-icx

600.perlbench_s: icc

C++ benchmarks:
-icpx

Fortran benchmarks:
-ifort
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<th>Test Date</th>
<th>May-2021</th>
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<td>Hardware Availability</td>
<td>Jun-2021</td>
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<tr>
<td>Software Availability</td>
<td>Dec-2020</td>
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</table>

### Peak Portability Flags

Same as Base Portability Flags

### Peak Optimization Flags

#### C benchmarks:

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
</thead>
</table>
| 600.perlbench_s | `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)`  
`-xCORE-AVX512 -ipo -O3 -no-prec-div`  
`-qopt-mem-layout-trans=4 -fno-strict-overflow`  
`-mbranches-within-32B-boundsaries`  
`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc` |
| 602.gcc_s | `-m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)`  
`-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto`  
`-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4`  
`-mbranches-within-32B-boundsaries`  
`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc` |
| 605.mcf_s | `basepeak = yes` |
| 625.x264_s | `-DSPEC_OPENMP -fopenmp -std=c11 -m64 -Wl,-z,muldefs`  
`-xCORE-AVX512 -flto -O3 -ffast-math`  
`-qopt-mem-layout-trans=4 -fno-alias`  
`-mbranches-within-32B-boundsaries`  
`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc` |
| 657.xz_s | `basepeak = yes` |

#### C++ benchmarks:

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<td><code>basepeak = yes</code></td>
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<td><code>basepeak = yes</code></td>
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<td><code>basepeak = yes</code></td>
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<td>641.leela_s</td>
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#### Fortran benchmarks:

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<td>648.exchange2_s</td>
<td><code>basepeak = yes</code></td>
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The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2021-05-22 02:40:37-0400.
Originally published on 2021-06-09.