Dell Inc.

PowerEdge R650 (Intel Xeon Gold 6330, 2.00 GHz)

SPECspeed®2017_int_base = 10.7
SPECspeed®2017_int_peak = 10.9

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Dell Inc.
PowerEdge R650 (Intel Xeon Gold 6330, 2.00 GHz)

Copyright 2017-2021 Standard Performance Evaluation Corporation

Test Date: May-2021
Hardware Availability: May-2021
Software Availability: Feb-2021

Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
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<tr>
<td>600.perlbench_s</td>
<td>6.42</td>
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<tr>
<td>602.gcc_s</td>
<td>9.86</td>
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<td>605.mcf_s</td>
<td>10.8</td>
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<tr>
<td>620.omnetpp_s</td>
<td></td>
<td>18.0</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td></td>
<td>12.1</td>
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<tr>
<td>625.x264_s</td>
<td></td>
<td>15.2</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
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<td>13.9</td>
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<tr>
<td>641.leela_s</td>
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<td>17.1</td>
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<tr>
<td>648.exchange2_s</td>
<td></td>
<td>21.5</td>
</tr>
<tr>
<td>657.xz_s</td>
<td></td>
<td>21.5</td>
</tr>
</tbody>
</table>

SPECspeed®2017_int_base (10.7)
SPECspeed®2017_int_peak (10.9)

Hardware

CPU Name: Intel Xeon Gold 6330
Max MHz: 3100
Nominal: 2000
Enabled: 56 cores, 2 chips
Orderable: 1.2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1.25 MB I+D on chip per core
L3: 42 MB I+D on chip per chip
Other: None
Memory: 512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R, running at 2933)
Storage: 225 GB on tmpfs
Other: None

Software

OS: Red Hat Enterprise Linux 8.3 (Ootpa)
Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++
Compiler Build 20201113 for Linux;
Fortran: Version 2021.1 of Intel Fortran Compiler
Classic Build 20201112 for Linux;
C/C++: Version 2021.1 of Intel C/C++ Compiler
Classic Build 20201112 for Linux
Parallel: Yes
Firmware: Version 1.1.2 released Apr-2021
File System: tmpfs
System State: Run level 5 (graphical multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.

jemalloc memory allocator V5.0.1
Dell Inc.

PowerEdge R650 (Intel Xeon Gold 6330, 2.00 GHz)

SPECspeed®2017_int_base = 10.7
SPECspeed®2017_int_peak = 10.9

Results Table

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<tr>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>10.8</td>
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<td>10.9</td>
<td>56</td>
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<td>10.8</td>
<td>152</td>
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<td>10.9</td>
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<tr>
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<td>270</td>
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<td>172</td>
<td>17.1</td>
<td>171</td>
<td>17.2</td>
<td>172</td>
<td>17.1</td>
<td>56</td>
<td>172</td>
<td>17.1</td>
<td>171</td>
<td>17.2</td>
<td>172</td>
<td>17.1</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH =
"/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/je5.0.1-64"
MALLOCP_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
Dell Inc.

PowerEdge R650 (Intel Xeon Gold 6330, 2.00 GHz)

**General Notes (Continued)**

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 225 GB ramdisk created with the cmd: "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"

**Platform Notes**

BIOS Settings:
- Logical Processor : Disabled
- Virtualization Technology : Disabled

- System Profile : Custom
- CPU Power Management : Maximum Performance
- C1E : Disabled
- C States : Autonomous
- Memory Patrol Scrub : Disabled
- Energy Efficiency Policy : Performance
- CPU Interconnect Bus Link
- Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.5-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080a89d4b38e2f1c
running on localhost.localdomain Tue May 11 05:11:41 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) Gold 6330 CPU @ 2.00GHz
- 2 "physical id"s (chips)
- 56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 28
  - siblings : 28
  - physical 0: cores 0 1 2 3 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
  - physical 1: cores 0 1 2 3 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian

(Continued on next page)
# SPEC CPU®2017 Integer Speed Result

### Dell Inc.

**PowerEdge R650 (Intel Xeon Gold 6330, 2.00 GHz)**

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</tbody>
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---

### SPECspeed®2017_int_base = 10.7

### SPECspeed®2017_int_peak = 10.9

---

## Platform Notes (Continued)

```
CPU(s):              56
On-line CPU(s) list: 0-55
Thread(s) per core:  1
Core(s) per socket:  28
Socket(s):           2
NUMA node(s):        2
Vendor ID:           GenuineIntel
CPU family:          6
Model:               106
Model name:          Intel(R) Xeon(R) Gold 6330 CPU @ 2.00GHz
Stepping:            6
CPU MHz:             1341.839
BogoMIPS:            4000.00
Virtualization:      VT-x
L1d cache:           48K
L1i cache:           32K
L2 cache:            1280K
L3 cache:            43008K
NUMA node0 CPU(s):   0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54
NUMA node1 CPU(s):   1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55
Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                      pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                      lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
                      aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma cx16
                      xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
                      avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single
                      intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bm11 hle avx2
                      smep bmi2 erms invpcid cmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                      clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsave xsetbv
                      xsaves cmq_llc cmq_occachl lc cmq_mbb_total cmq_mbb_local split_lock_detect wbinvd
                      dtherm ida arat pln pts avx512vmbi umip pkt ompe avx512vpavmt avx512ifv 512
                      vni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_lld
                      arch_capabilities

/proc/cpuinfo cache data
   cache size : 43008 KB
```

From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus:  0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46 48 50 52 54
node 0 size: 245824 MB
node 0 free: 241685 MB
node 1 cpus:  1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47 49 51

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Dell Inc.

PowerEdge R650 (Intel Xeon Gold 6330, 2.00 GHz)

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<td>10.9</td>
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</tbody>
</table>

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Platform Notes ( Continued )

53 55
node 1 size: 246831 MB
node 1 free: 256472 MB
node distances:
node 0 1
0: 10 20
1: 20 10

From /proc/meminfo
MemTotal: 527810380 kB
 HugePages_Total: 0
 Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.3 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
Linux localhost.localdomain 4.18.0-240.15.1.el8_3.x86_64 #1 SMP Wed Feb 3 03:12:15 EST 2021 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1): Mitigation: Enhanced IBRS, IBPB:
CVE-2017-5715 (Spectre variant 2):
Dell Inc. PowerEdge R650 (Intel Xeon Gold 6330, 2.00 GHz)  
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CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Test Date: May-2021  
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Software Availability: Feb-2021

Platform Notes (Continued)

conditional, RSB filling  
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected  
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 5 May 11 05:09

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.5-ic2021.1

Filesystem   Type   Size  Used Avail Use% Mounted on
tmpfs        tmpfs  225G  6.9G  219G   4% /mnt/ramdisk

From /sys/devices/virtual/dmi/id
Vendor:        Dell Inc.  
Product:       PowerEdge R650  
Product Family: PowerEdge  
Serial:        1234567

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
7x 00AD00B300AD HMAA4GR7AJR8N-XN 32 GB 2 rank 3200, configured at 2933  
9x 00AD063200AD HMAA4GR7AJR8N-XN 32 GB 2 rank 3200, configured at 2933  
16x Not Specified Not Specified

BIOS:
BIOS Vendor: Dell Inc.  
BIOS Version: 1.1.2  
BIOS Date: 04/09/2021  
BIOS Revision: 1.1

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 600.perlbench_s(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)  
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)  
625.x264_s(base, peak) 657.xz_s(base, peak)

(Continued on next page)
# SPEC CPU®2017 Integer Speed Result

## Dell Inc.

PowerEdge R650 (Intel Xeon Gold 6330, 2.00 GHz)

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### Compiler Version Notes (Continued)

---

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

C               | 600.perlbench_s(peak)
---               |-----------------------

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

C               | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
|----------------|-----------------------|
|                | 625.x264_s(base, peak) 657.xz_s(base, peak)

---

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

C++             | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)
|----------------|-----------------------|
|                | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)

---

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

Fortran         | 648.exchange2_s(base, peak)
---              |-----------------------

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

Base Compiler Invocation

C benchmarks:
- icx
Base Compiler Invocation (Continued)

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-DSPEC_OPENMP -std=c11 -m64 -fopenmp -Wl,-z,muldefs -xCORE-AVX512
-O3 -ffast-math -flto -ffpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -ffpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-lqkmalloc

Fortran benchmarks:
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries
Dell Inc.  
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Peak Compiler Invocation

C benchmarks (except as noted below):
icx

600.perlbench_s: icc

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)  
-xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -fno-strict-overflow  
-mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto  
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4  
-mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs  
-xCORE-AVX512 -flto -O3 -ffast-math  
-qopt-mem-layout-trans=4 -fno-alias  
-mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

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Tested by: Dell Inc.
Hardware Availability: May-2021
Software Availability: Feb-2021

Peak Optimization Flags (Continued)

C++ benchmarks:

620.omnetpp_s: basepeak = yes
623.xalancbmk_s: basepeak = yes
631.deepsjeng_s: basepeak = yes
641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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