# SPEC CPU®2017 Floating Point Speed Result

## Lenovo Global Technology

**ThinkSystem ST650 V2**  
(3.10 GHz, Intel Xeon Gold 6346)

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** May-2021  
**Hardware Availability:** Jul-2021  
**Software Availability:** Feb-2021  

### SPECspeed®2017 fp_base = 178

### SPECspeed®2017 fp_peak = Not Run

### Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>32</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>32</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>32</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>32</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>32</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>32</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>32</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>32</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>32</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>32</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 6346  
- **Max MHz:** 3600  
- **Nominal:** 3100  
- **Enabled:** 32 cores, 2 chips  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 48 KB D on chip per core  
- **L2:** 1.25 MB I+D on chip per core  
- **L3:** 36 MB I+D on chip per core  
- **Other:** None  
- **Memory:** 1 TB (32 x 32 GB 2Rx8 PC4-3200AA-R)  
- **Storage:** 1 x 960 GB SATA SSD  
- **Other:** None  

### Software

- **OS:** Red Hat Enterprise Linux 8.3 (Ootpa)  
  Kernel 4.18.0-240.el8.x86_64  
- **Compiler:** C/C++, Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;  
  Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;  
  C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux  
- **Parallel:** Yes  
- **Firmware:** Lenovo BIOS Version U8E109PT1 1.01 released Apr-2021  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** Not Applicable  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>32</td>
<td>89.7</td>
<td>658</td>
<td>90.5</td>
<td>652</td>
<td>90.5</td>
<td>652</td>
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<tr>
<td>607.cactuBSSN_s</td>
<td>32</td>
<td>77.9</td>
<td>214</td>
<td>78.9</td>
<td>211</td>
<td>80.3</td>
<td>208</td>
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<tr>
<td>619.lbm_s</td>
<td>32</td>
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<td>40.6</td>
<td>129</td>
<td>40.3</td>
<td>130</td>
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<tr>
<td>621.wrf_s</td>
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<td>80.5</td>
<td>164</td>
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<td>163</td>
<td>81.5</td>
<td>162</td>
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<tr>
<td>627.cam4_s</td>
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<td>78.1</td>
<td>113</td>
<td>78.1</td>
<td>113</td>
<td>77.9</td>
<td>114</td>
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<tr>
<td>628.pop2_s</td>
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<td>139</td>
<td>85.3</td>
<td>139</td>
<td>85.1</td>
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<tr>
<td>638.imagick_s</td>
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<td>87.5</td>
<td>165</td>
<td>87.8</td>
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<td>87.6</td>
<td>165</td>
</tr>
<tr>
<td>644.nab_s</td>
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<td>280</td>
<td>62.4</td>
<td>280</td>
<td>62.4</td>
<td>280</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>32</td>
<td>80.7</td>
<td>113</td>
<td>79.8</td>
<td>114</td>
<td>80.7</td>
<td>113</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>32</td>
<td>73.3</td>
<td>215</td>
<td>73.4</td>
<td>214</td>
<td>73.4</td>
<td>215</td>
</tr>
</tbody>
</table>

SPECspeed®2017_fp_base = 178
SPECspeed®2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017-1.1.5-ic2021.1-revB/lib/intel64:/home/cpu2017-1.1.5-ic2021.1-revB/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3> /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)
General Notes (Continued)

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode
C-States set to Legacy
Adjacent Cache Prefetch set to Disabled
Hyper-Threading set to Disabled

Sysinfo program /home/cpu2017-1.1.5-ic2021.1-revB/bin/sysinfo
Rev: r6538 of 2020-09-24 e664e66dd27080afeaa89d4b38e2f1c
running on localhost.localdomain Fri May 21 02:08:41 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6346 CPU @ 3.10GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 16
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 1
Core(s) per socket: 16
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6346 CPU @ 3.10GHz
Stepping: 6

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Platform Notes (Continued)

CPU MHz: 3423.335
BogoMIPS: 6200.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 36864K
NUMA node0 CPU(s): 0-15
NUMA node1 CPU(s): 16-31
Flags: fpu vme de pse ts c3 sm tss lms pae mce cmov
type mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb
dts scs cope movd popcnt vdir pdcm pclid dca sse4_1 sse4_2
x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3
invpcid_single intel_ppln ssbd mba ibrs ibpb stibp ibrs_enhanced
tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase
tsc_adjust bmi1 hle avx2 smep bmi2 ibrms invpcid cmov
dtimes adx avx512f avx512dq rdseed adx smap avx512ifma
clflushopt clwb intel_pt avx512cd sha ni
avx512bw avx512vl xsaveopt xsave xsetbv1 xsavec
From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
From /proc/meminfo
MemTotal: 1056496128 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
/sbin/tuned-adm active
Current active profile: throughput-performance

(Continued on next page)
Platform Notes (Continued)

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.3 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.3"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
    ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
  system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
    Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
    x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass):
  Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):
  Mitigation: usercopy/swaps barriers and __user pointer sanitation
CVE-2017-5715 (Spectre variant 2):
  Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling):
CVE-2019-11135 (TSX Asynchronous Abort):

run-level 3 May 21 02:07

SPEC is set to: /home/cpu2017-1.1.5-ic2021.1-revB
  Filesystem   Type  Size  Used  Avail  Use% Mounted on
  /dev/sda4     xfs  818G  50G  768G   7% /home

From /sys/devices/virtual/dmi/id
  Vendor: Lenovo
  Product: ThinkSystem ST650V2
  Product Family: ThinkSystem
  Serial: 1234567890

(Continued on next page)
Platform Notes (Continued)

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
32x Samsung M393A4G43AB3-CWE 32 GB 2 rank 3200

BIOS:
  BIOS Vendor: Lenovo
  BIOS Version: U8E109PT1-1.01
  BIOS Date: 04/28/2021
  BIOS Revision: 1.1
  Firmware Revision: 1.20

(End of data from sysinfo program)

Compiler Version Notes

C | 619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)
-----------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
  64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------
C++, C, Fortran | 607.cactuBSSN_s(base)
-----------------------------------------
Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on
  Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
  64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
  Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------
Fortran | 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
-----------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
  Intel(R) 64, Version 2021.1 Build 20201112_000000
(Continued on next page)
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Compiler Version Notes (Continued)
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

----------------------------------------
Fortran, C | 621.wrf_s(base) 627.cam4_s(base) 628.pop2_s(base)
----------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation
C benchmarks:
icc
Fortran benchmarks:
ifort
Benchmarks using both Fortran and C:
ifort icc
Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags
603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byteorder
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
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Base Optimization Flags

C benchmarks:
-m64 -std=c11 -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries

Fortran benchmarks:
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-nostandard-realloc-lhs -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nstandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nstandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-D.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-D.xml
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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