SPEC CPU®2017 Integer Rate Result

Dell Inc.

PowerEdge R650 (Intel Xeon Gold 6312U, 2.40 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

SPECrate®2017_int_base = 181
SPECrate®2017_int_peak = 188

Dell Inc.

PowerEdge R650 (Intel Xeon Gold 6312U, 2.40 GHz)

SPECrate®2017_int_base = 181
SPECrate®2017_int_peak = 188

Hardware

CPU Name: Intel Xeon Gold 6312U
Max MHz: 3600
Nominal: 2400
Enabled: 24 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1.25 MB I+D on chip per core
L3: 36 MB I+D on chip per chip
Other: None
Memory: 256 GB (8 x 32 GB 2Rx8 PC4-3200AA-R)
Storage: 225 GB on tmpfs
Other: None

Software

OS: Red Hat Enterprise Linux 8.3 (Ootpa)
4.18.0-240.15.1.el8_3.x86_64
Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;
Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;
C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux
Parallel: No
Firmware: Version 1.2.2 released May-2021
File System: tmpfs
System State: Run level 5 (graphical multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.
Dell Inc. PowerEdge R650 (Intel Xeon Gold 6312U, 2.40 GHz)  

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>48</td>
<td>610</td>
<td>125</td>
<td>608</td>
<td>126</td>
<td>48</td>
<td>521</td>
<td>147</td>
<td>521</td>
<td>147</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>48</td>
<td>465</td>
<td>146</td>
<td>466</td>
<td>146</td>
<td>48</td>
<td>398</td>
<td>171</td>
<td>396</td>
<td>171</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>48</td>
<td>262</td>
<td>296</td>
<td>259</td>
<td>299</td>
<td>48</td>
<td>262</td>
<td>296</td>
<td>259</td>
<td>299</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>48</td>
<td>557</td>
<td>113</td>
<td>554</td>
<td>114</td>
<td>48</td>
<td>557</td>
<td>113</td>
<td>554</td>
<td>114</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>48</td>
<td>222</td>
<td>228</td>
<td>222</td>
<td>228</td>
<td>48</td>
<td>222</td>
<td>228</td>
<td>222</td>
<td>228</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>525.x264_r</td>
<td>48</td>
<td>222</td>
<td>379</td>
<td>222</td>
<td>379</td>
<td>48</td>
<td>212</td>
<td>396</td>
<td>212</td>
<td>397</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>48</td>
<td>395</td>
<td>139</td>
<td>395</td>
<td>139</td>
<td>48</td>
<td>395</td>
<td>139</td>
<td>395</td>
<td>139</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>541.leela_r</td>
<td>48</td>
<td>583</td>
<td>136</td>
<td>583</td>
<td>136</td>
<td>48</td>
<td>583</td>
<td>136</td>
<td>583</td>
<td>136</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>48</td>
<td>333</td>
<td>377</td>
<td>333</td>
<td>377</td>
<td>48</td>
<td>333</td>
<td>377</td>
<td>333</td>
<td>377</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>557.xz_r</td>
<td>48</td>
<td>513</td>
<td>101</td>
<td>513</td>
<td>101</td>
<td>48</td>
<td>513</td>
<td>101</td>
<td>513</td>
<td>101</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results Table

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH =
"/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/lib/ia32:/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/je5.0.1-32"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

(Continued on next page)
**General Notes (Continued)**

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Benchmark run from a 225 GB ramdisk created with the cmd: "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"

**Platform Notes**

BIOS Settings:
- Sub NUMA Cluster : 2-Way Clustering
- Virtualization Technology : Disabled

System Profile : Custom
- CPU Power Management : Maximum Performance
- C1E : Disabled
- C States : Autonomous
- Memory Patrol Scrub : Disabled
- Energy Efficiency Policy : Performance
- CPU Interconnect Bus Link
  - Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.5-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Tue May 18 11:04:55 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
  https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 6312U CPU @ 2.40GHz
  1 "physical id"s (chips)
  48 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following
  excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 24

(Continued on next page)
**SPEC CPU®2017 Integer Rate Result**

**Dell Inc.**

PowerEdge R650 (Intel Xeon Gold 6312U, 2.40 GHz)

**SPECrate®2017_int_base = 181**

**SPECrate®2017_int_peak = 188**

**Dell Inc.**

**CPU2017 License:** 55

**Test Sponsor:** Dell Inc.

**Tested by:** Dell Inc.

**Test Date:** May-2021

**Hardware Availability:** May-2021

**Software Availability:** Feb-2021

---

**Platform Notes (Continued)**

siblings : 48

genuineintell

From lscpu:

Architecture: x86_64

CPU op-mode(s): 32-bit, 64-bit

Byte Order: Little Endian

CPU(s): 48

On-line CPU(s) list: 0-47

Thread(s) per core: 2

Core(s) per socket: 24

Socket(s): 1

NUMA node(s): 2

Vendor ID: GenuineIntel

CPU family: 6

Model: 106

Model name: Intel(R) Xeon(R) Gold 6312U CPU @ 2.40GHz

Stepping: 6

CPU MHz: 2623.829

BogoMIPS: 4800.00

Virtualization: VT-x

L1d cache: 48K

L1i cache: 32K

L2 cache: 1280K

L3 cache: 36864K

NUMA node0 CPU(s):

0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46

NUMA node1 CPU(s):

1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single intel_pinn ssbd mba ibrs ibpb stibp ibrs跛到 fsbgbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_hni avx512bw avx512vl xsavexct xgetbv1 xsave cmm llc cmq_occup_l1c cmq_mbb_total cmq_mbb_local split_lock_detect wbninvd dtherm ida arat pln pts avx512vmbi umip pku ospke avx512_vbmi2 qfni vaes vpcmldqd avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d arch_capabilities

/proc/cpuinfo cache data

cache size : 36864 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a...
Dell Inc.

PowerEdge R650 (Intel Xeon Gold 6312U, 2.40 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

SPECratre®2017_int_base = 181
SPECratre®2017_int_peak = 188

Test Date: May-2021
Hardware Availability: May-2021
Software Availability: Feb-2021

Platform Notes (Continued)

physical chip.
   available: 2 nodes (0-1)
   node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46
   node 0 size: 124863 MB
   node 0 free: 112679 MB
   node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47
   node 1 size: 125128 MB
   node 1 free: 127452 MB
   node distances:
      node 0   1
      0:  10  11
      1:  11  10

From /proc/meminfo
   MemTotal:       263571256 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/sbin/tuned-adm active
   Current active profile: throughput-performance

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux"
      VERSION="8.3 (Ootpa)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="8.3"
      PLATFORM_ID="platform:el8"
      PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
      ANSI_COLOR="0;31"
      redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
      system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
      system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
   Linux localhost.localdomain 4.18.0-240.15.1.el8_3.x86_64 #1 SMP Wed Feb 3 03:12:15 EST 2021 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Dell Inc.

PowerEdge R650 (Intel Xeon Gold 6312U, 2.40 GHz)

SPECrate®2017_int_base = 181
SPECrate®2017_int_peak = 188

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.
Test Date: May-2021
Hardware Availability: May-2021
Software Availability: Feb-2021

Platform Notes (Continued)

CVE-2017-5753 (Spectre variant 1):
Mitigation: usercopy/swapgs barriers and __user pointer sanitization

CVE-2017-5715 (Spectre variant 2):
Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling):
Not affected

CVE-2019-11135 (TSX Asynchronous Abort):
Not affected

run-level 5 May 18 11:03

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.5-ic2021.1
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 225G 6.9G 219G 4% /mnt/ramdisk

From /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge R650
Product Family: PowerEdge
Serial: 1234567

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
2x 00AD00B300AD HMMA4GR7A JR8N-XN 32 GB 2 rank 3200
6x 00AD063200AD HMMA4GR7A JR8N-XN 32 GB 2 rank 3200
24x Not Specified Not Specified

BIOS:
BIOS Vendor: Dell Inc.
BIOS Version: 1.2.2
BIOS Date: 05/14/2021
BIOS Revision: 1.2

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C           | 500.perlbench_r(peak)
==============================================================================
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
(Continued on next page)
SPECCPU®2017 Integer Rate Result

Dell Inc.

PowerEdge R650 (Intel Xeon Gold 6312U, 2.40 GHz)

SPECraté®2017_int_base = 181

SPECraté®2017_int_peak = 188

CPU2017 License: 55
Test Date: May-2021

Test Sponsor: Dell Inc.
Hardware Availability: May-2021

Tested by: Dell Inc.
Software Availability: Feb-2021

---

**Compiler Version Notes (Continued)**

-------------------------------------------------------------------
C | 502.gcc_r(peak)
-------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-------------------------------------------------------------------

-------------------------------------------------------------------
C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
   | 525.x264_r(base, peak) 557.xz_r(base, peak)
-------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-------------------------------------------------------------------

-------------------------------------------------------------------
C | 500.perlbench_r(peak)
-------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-------------------------------------------------------------------

-------------------------------------------------------------------
C | 502.gcc_r(peak)
-------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-------------------------------------------------------------------

-------------------------------------------------------------------
C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
   | 525.x264_r(base, peak) 557.xz_r(base, peak)
-------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-------------------------------------------------------------------

-------------------------------------------------------------------
C | 500.perlbench_r(peak)
-------------------------------------------------------------------
(Continued on next page)
Compiler Version Notes (Continued)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 2021112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C   | 502.gcc_r(peak)
==============================================================================

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version
2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
         | 525.x264_r(base, peak) 557.xz_r(base, peak)
==============================================================================

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak)
         | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
==============================================================================

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
Fortran | 548.exchange2_r(base, peak)
==============================================================================

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 2021112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

(Continued on next page)
**SPEC CPU®2017 Integer Rate Result**

**Dell Inc.**

PowerEdge R650 (Intel Xeon Gold 6312U, 2.40 GHz)

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base</th>
<th>181</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_int_peak</td>
<td>188</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>55</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Tested by</td>
<td>Dell Inc.</td>
</tr>
</tbody>
</table>

**Test Date:** May-2021  
**Hardware Availability:** May-2021  
**Software Availability:** Feb-2021

### Base Compiler Invocation (Continued)

Fortran benchmarks:

- ifort

### Base Portability Flags

- 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
- 502.gcc_r: -DSPEC_LP64
- 505.mcf_r: -DSPEC_LP64
- 520.omnetpp_r: -DSPEC_LP64
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
- 525.x264_r: -DSPEC_LP64
- 531.deepsjeng_r: -DSPEC_LP64
- 541.leela_r: -DSPEC_LP64
- 548.exchange2_r: -DSPEC_LP64
- 557.xz_r: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**

- -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries 
- -L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin 
- -lqkmalloc

**C++ benchmarks:**

- -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries 
- -L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin 
- -lqkmalloc

**Fortran benchmarks:**

- -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte -auto -mbranches-within-32B-boundaries 
- -L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin 
- -lqkmalloc
### Dell Inc.

PowerEdge R650 (Intel Xeon Gold 6312U, 2.40 GHz)

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base</th>
<th>Dell Inc.</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_int_peak</td>
<td>Dell Inc.</td>
</tr>
</tbody>
</table>

- **CPU2017 License:** 55
- **Test Sponsor:** Dell Inc.
- **Tested by:** Dell Inc.
- **Test Date:** May-2021
- **Hardware Availability:** May-2021
- **Software Availability:** Feb-2021

### Peak Compiler Invocation

C benchmarks (except as noted below):

- icx
- 500.perlbench_r: icc

C++ benchmarks:

- icpx

Fortran benchmarks:

- ifort

### Peak Portability Flags

- 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
- 502.gcc_r: -D_FILE_OFFSET_BITS=64
- 505.mcf_r: -DSPEC_LP64
- 520.omnetpp_r: -DSPEC_LP64
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
- 525.x264_r: -DSPEC_LP64
- 531.deepsjeng_r: -DSPEC_LP64
- 541.leela_r: -DSPEC_LP64
- 548.exchange2_r: -DSPEC_LP64
- 557.xz_r: -DSPEC_LP64

### Peak Optimization Flags

C benchmarks:

- 500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
- -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4 -fno-strict-overflow
- -mbranches-within-32B-boundaries
- -L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
- -lqkmalloc
- 502.gcc_r: -m32
- -L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/ia32_lin
- -std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
- -fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
- -Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
- -mbranches-within-32B-boundaries
- -L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

(Continued on next page)
## SPEC CPU®2017 Integer Rate Result

### Dell Inc.

**PowerEdge R650 (Intel Xeon Gold 6312U, 2.40 GHz)**

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>181</td>
<td>188</td>
</tr>
</tbody>
</table>

| CPU2017 License: | 55 |
| Test Sponsor:   | Dell Inc. |
| Tested by:      | Dell Inc. |
| Test Date:      | May-2021 |
| Hardware Availability: | May-2021 |
| Software Availability: | Feb-2021 |

### Peak Optimization Flags (Continued)

505.mcf_r: basepeak = yes


557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml


---

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.5 on 2021-05-18 12:04:55-0400.

Report generated on 2021-07-08 13:29:03 by CPU2017 PDF formatter v6442.

Originally published on 2021-07-06.