Dell Inc.  
PowerEdge R750xa (Intel Xeon Gold 5320, 2.20 GHz)

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Jun-2021  
**Hardware Availability:** May-2021  
**Software Availability:** Feb-2021

<table>
<thead>
<tr>
<th>Benchmarks</th>
<th>Copies</th>
<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
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<tbody>
<tr>
<td>perlbench_r</td>
<td>104</td>
<td>246</td>
<td>287</td>
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<tr>
<td>gcc_r</td>
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<td>282</td>
<td>335</td>
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<td>mcf_r</td>
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<td>omnetpp_r</td>
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<tr>
<td>xalancbmk_r</td>
<td>104</td>
<td>438</td>
<td>730</td>
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<tr>
<td>x264_r</td>
<td>104</td>
<td></td>
<td>768</td>
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<tr>
<td>deepsjeng_r</td>
<td>104</td>
<td>272</td>
<td></td>
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<tr>
<td>leela_r</td>
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<tr>
<td>exchange2_r</td>
<td>104</td>
<td>731</td>
<td></td>
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<tr>
<td>xz_r</td>
<td>104</td>
<td>200</td>
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**Hardware**

<table>
<thead>
<tr>
<th>Feature</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon Gold 5320</td>
</tr>
<tr>
<td>Max MHz</td>
<td>3400</td>
</tr>
<tr>
<td>Nominal</td>
<td>2200</td>
</tr>
<tr>
<td>Enabled</td>
<td>52 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable</td>
<td>1.2 chips</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td>Cache L2</td>
<td>1.25 MB I+D on chip per core</td>
</tr>
<tr>
<td>Cache L3</td>
<td>39 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R, running at 2933)</td>
</tr>
<tr>
<td>Storage</td>
<td>225 GB on tmpfs</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

**Software**

<table>
<thead>
<tr>
<th>Feature</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS</td>
<td>Red Hat Enterprise Linux 8.3 (Ootpa)</td>
</tr>
</tbody>
</table>
| Compiler                 | C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;  
|                          | Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;  
|                          | C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux; |
| Parallel                 | No                                                                          |
| Firmware                 | Version 1.2.4 released May-2021                                             |
| File System              | tmpfs                                                                       |
| System State             | Run level 5 (graphical multi-user)                                          |
| Base Pointers            | 64-bit                                                                      |
| Peak Pointers            | 32/64-bit                                                                   |
| Other                    | jemalloc memory allocator V5.0.1                                            |
| Power Management         | BIOS and OS set to prefer performance at the cost of additional power usage. |

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**SPEC CPU®2017 Integer Rate Result**

**SPECrate®2017_int_base = 352**  
**SPECrate®2017_int_peak = 366**
## SPEC CPU® 2017 Integer Rate Result

**Dell Inc.**

PowerEdge R750xa (Intel Xeon Gold 5320, 2.20 GHz)

SPECrate® 2017 int_base = 352

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<tr>
<th>Benchmark</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Segments</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Segments</th>
<th>Seconds</th>
<th>Ratio</th>
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<tr>
<td>500.perlbench_r</td>
<td>104</td>
<td>672</td>
<td>246</td>
<td>674</td>
<td>246</td>
<td>104</td>
<td>578</td>
<td>287</td>
<td>577</td>
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<tr>
<td>502.gcc_r</td>
<td>104</td>
<td>522</td>
<td>282</td>
<td>520</td>
<td>283</td>
<td>104</td>
<td>439</td>
<td>335</td>
<td>439</td>
<td>335</td>
<td></td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>104</td>
<td>290</td>
<td>579</td>
<td>289</td>
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<td>104</td>
<td>290</td>
<td>579</td>
<td>289</td>
<td>581</td>
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<td>520.omnetpp_r</td>
<td>104</td>
<td>630</td>
<td>217</td>
<td>630</td>
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<td>104</td>
<td>630</td>
<td>217</td>
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<tr>
<td>523.xalancbmk_r</td>
<td>104</td>
<td>250</td>
<td>438</td>
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<td>438</td>
<td>251</td>
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<tr>
<td>525.x264_r</td>
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<td>771</td>
<td>237</td>
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<tr>
<td>548.exchange2_r</td>
<td>104</td>
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<td>731</td>
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<td>731</td>
<td>371</td>
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</tr>
<tr>
<td>557.xz_r</td>
<td>104</td>
<td>561</td>
<td>200</td>
<td>561</td>
<td>200</td>
<td>104</td>
<td>561</td>
<td>200</td>
<td>561</td>
<td>200</td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = 
"/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/lib/ia32:/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/je5.0.1-32"
MALLOC_CONF = "retain:true"
```

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3 > /proc/sys/vm/drop_caches
```

(Continued on next page)
Dell Inc.  
PowerEdge R750xa (Intel Xeon Gold 5320, 2.20 GHz)  

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

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---

**General Notes (Continued)**

runcpu command invoked through numacl i.e.:  
numacl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)  
is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)  
is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)  
is mitigated in the system as tested and documented.

Benchmark run from a 225 GB ramdisk created with the cmd: "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"

---

**Platform Notes**

**BIOS Settings:**  
Sub NUMA Cluster : 2-Way Clustering  
Virtualization Technology : Disabled

System Profile : Custom  
CPU Power Management : Maximum Performance  
C1E : Disabled  
C States : Autonomous  
Memory Patrol Scrub : Disabled  
Energy Efficiency Policy : Performance  
CPU Interconnect Bus Link  
Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.5-ic2021.1/bin/sysinfo  
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c  
running on localhost.localdomain Fri Jun 4 15:05:58 2021

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 5320 CPU @ 2.20GHz  
2 "physical id"s (chips)  
104 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 26

(Continued on next page)
Platform Notes (Continued)

siblings : 52
physical 0: cores 0 1 2 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
physical 1: cores 0 1 2 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 104
On-line CPU(s) list: 0-103
Thread(s) per core: 2
Core(s) per socket: 26
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 5320 CPU @ 2.20GHz
Stepping: 6
CPU MHz: 2925.129
BogoMIPS: 4400.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 39936K
NUMA node0 CPU(s): 0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68,72,76,80,84,88,92,96,100
NUMA node1 CPU(s): 2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70,74,78,82,86,90,94,98,102
NUMA node2 CPU(s): 1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69,73,77,81,85,89,93,97,101
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtr pdcmd cldc dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single
intel_patin ssbd mbx ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle avx2
smep bmi2 erts invpctid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xsavec
xsavec cqm_llc cqm_occucc_1lc cqm_mbb_total cqm_mbb_local split_lock detect wboinvd
fdt ftdm ida arat pln pts avx512v bmi umip pk uospke avx512v bmi gfnl vaes vclmulq
fpga axv512_vnni axv512_bitalg tme axv512_vpopcntd q la57 rpfd md_clear pconfug flush_l1d
arch_capabilities

(Continued on next page)
Dell Inc.

PowerEdge R750xa (Intel Xeon Gold 5320, 2.20 GHz)

SPEC CPU®2017 Integer Rate Result

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Dell Inc. SPECrate®2017_int_base = 352
PowerEdge R750xa (Intel Xeon Gold 5320, 2.20 GHz) SPECrate®2017_int_peak = 366

CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: Jun-2021
Tested by: Dell Inc.
Hardware Availability: May-2021
Software Availability: Feb-2021

Platform Notes (Continued)

/proc/cpuinfo cache data
cache size : 39936 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60 64 68 72 76 80 84 88 92 96
100
node 0 size: 125564 MB
node 0 free: 127439 MB
node 1 cpus: 2 6 10 14 18 22 26 30 34 38 42 46 50 54 58 62 66 70 74 78 82 86 90 94 98
102
node 1 size: 126162 MB
node 1 free: 128598 MB
node 2 cpus: 1 5 9 13 17 21 25 29 33 37 41 45 49 53 57 61 65 69 73 77 81 85 89 93 97
101
node 2 size: 126105 MB
node 2 free: 124551 MB
node 3 cpus: 3 7 11 15 19 23 27 31 35 39 43 47 51 55 59 63 67 71 75 79 83 87 91 95 99
103
node 3 size: 126240 MB
node 3 free: 105415 MB
node distances:
node 0 1 2 3
0: 10 11 20 20
1: 11 10 20 20
2: 20 20 10 11
3: 20 20 11 10

From /proc/meminfo
MemTotal: 527799160 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
/sbin/tuned-adm active
Current active profile: throughput-performance

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.3 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"

(Continued on next page)
Platform Notes (Continued)

ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
Linux localhost.localdomain 4.18.0-240.15.1.el8_3.x86_64 #1 SMP Wed Feb 3 03:12:15 EST 2021 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapsgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 5 Jun 4 15:04

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.5-ic2021.1
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 225G 13G 213G 6% /mnt/ramdisk

From /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge R750xa
Product Family: PowerEdge
Serial: 1234567

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SM BIOS" standard.

Memory:
16x 002C069D002C 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200, configured at 2933
16x Not Specified Not Specified

BIOS: (Continued on next page)
spec

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Platform Notes (Continued)

BIOS Vendor: Dell Inc.
BIOS Version: 1.2.4
BIOS Date: 05/28/2021
BIOS Revision: 1.2

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C | 500.perlbench_r(peak)
---|-----------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C | 502.gcc_r(peak)
---|-----------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
| 525.x264_r(base, peak) 557.xz_r(base, peak)
---|-----------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C | 500.perlbench_r(peak)
---|-----------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

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C | 502.gcc_r(peak)
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version

(Continued on next page)
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Compiler Version Notes (Continued)

2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
 | 525.x264_r(base, peak) 557.xz_r(base, peak)
-----------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
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-----------------------------------------------------------------------------
C | 500.perlbench_r(peak)
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64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
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-----------------------------------------------------------------------------
C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak)
 | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
-----------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
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Dell Inc.
PowerEdge R750xa (Intel Xeon Gold 5320, 2.20 GHz)

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CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Jun-2021
Hardware Availability: May-2021
Software Availability: Feb-2021

Compiler Version Notes (Continued)

Fortran | 548.exchange2_r(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-fflt握手 -fmad=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

(Continued on next page)
Dell Inc.

PowerEdge R750xa (Intel Xeon Gold 5320, 2.20 GHz)

<table>
<thead>
<tr>
<th>SPECrate®2017_int_peak = 366</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_int_base = 352</td>
</tr>
</tbody>
</table>

| CPU2017 License:      | 55 |
| Test Sponsor:         | Dell Inc. |
| Tested by:            | Dell Inc. |
| Test Date:            | Jun-2021 |
| Hardware Availability:| May-2021 |
| Software Availability:| Feb-2021 |

**Base Optimization Flags (Continued)**

C++ benchmarks:
- `-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto`
- `-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`
- `-mbranches-within-32B-boundaries`
- `-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin`
- `-lqkmalloc`

Fortran benchmarks:
- `-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div`
- `-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte`
- `-auto -mbranches-within-32B-boundaries`
- `-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin`
- `-lqkmalloc`

**Peak Compiler Invocation**

C benchmarks (except as noted below):
- `icx`

500.perlbench_r: `icc`

C++ benchmarks:
- `icpx`

Fortran benchmarks:
- `ifort`

**Peak Portability Flags**

500.perlbench_r: `-DSPEC_LP64` `-DSPEC_LINUX_X64`
502.gcc_r: `-D_FILE_OFFSET_BITS=64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LP64` `-DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`
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SPEC CPU®2017 Integer Rate Result
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Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -flto
-03 -ffast-math -qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at
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You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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