# SPEC CPU®2017 Integer Speed Result

**Dell Inc.**

PowerEdge MX750c (Intel Xeon Silver 4314, 2.40 GHz)

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench_s</td>
<td>32</td>
<td>6.98</td>
<td>8.07</td>
</tr>
<tr>
<td>gcc_s</td>
<td>32</td>
<td>10.4</td>
<td>10.9</td>
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<tr>
<td>mcf_s</td>
<td>32</td>
<td>9.75</td>
<td>19.2</td>
</tr>
<tr>
<td>omnetpp_s</td>
<td>32</td>
<td>13.2</td>
<td>16.4</td>
</tr>
<tr>
<td>xalancbmk_s</td>
<td>32</td>
<td>5.75</td>
<td>17.1</td>
</tr>
<tr>
<td>x264_s</td>
<td>32</td>
<td>4.72</td>
<td></td>
</tr>
<tr>
<td>deepsjeng_s</td>
<td>32</td>
<td></td>
<td></td>
</tr>
<tr>
<td>leela_s</td>
<td>32</td>
<td></td>
<td></td>
</tr>
<tr>
<td>exchange2_s</td>
<td>32</td>
<td>18.7</td>
<td></td>
</tr>
<tr>
<td>xz_s</td>
<td>32</td>
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**CPU2017 License:** 55

**Test Sponsor:** Dell Inc.

**Test Date:** Apr-2021

**Hardware Availability:** Apr-2021

**Tested by:** Dell Inc.

**Software Availability:** Dec-2020

---

## Hardware

**CPU Name:** Intel Xeon Silver 4314

- **Max MHz:** 3400
- **Nominal:** 2400
- **Enabled:** 32 cores, 2 chips
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 48 KB D on chip per core
- **L2:** 1.25 MB I+D on chip per core
- **L3:** 24 MB I+D on chip per chip
- **Other:** None

**Memory:** 512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R, running at 2666)

**Storage:** 125 GB on tmpfs

**Other:** None

---

## Software

**OS:** Red Hat Enterprise Linux 8.3 (Ootpa) 4.18.0-240.el8.x86_64

**Compiler:**
- C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;
- Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;
- C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux

**Parallel:** Yes

**Firmware:** Version 1.1.2 released Apr-2021

**File System:** tmpfs

**System State:** Run level 3 (multi-user)

**Base Pointers:** 64-bit

**Peak Pointers:** 64-bit

**Other:** Jemalloc memory allocator V5.0.1

**Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
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SPECspeed®2017_int_base = 11.2
SPECspeed®2017_int_peak = 11.5

Results Table

<table>
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<tr>
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<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
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<th>Ratio</th>
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<td>600.perlbench_s</td>
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<td>620.omnetpp_s</td>
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<td>9.78</td>
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<tr>
<td>623.xalancbmk_s</td>
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<td>108</td>
<td>13.2</td>
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<td>108</td>
<td>13.2</td>
<td>108</td>
<td>13.2</td>
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<td>17.1</td>
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<td>17.1</td>
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<td>249</td>
<td>5.76</td>
<td>32</td>
<td>249</td>
<td>5.76</td>
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<td>5.75</td>
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<td>641.leela_s</td>
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<td>362</td>
<td>4.72</td>
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<td>4.72</td>
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<tr>
<td>648.exchange2_s</td>
<td>32</td>
<td>157</td>
<td>18.7</td>
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<td>18.7</td>
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<td>18.7</td>
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<td>657.xz_s</td>
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<td>21.7</td>
<td>285</td>
<td>21.7</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default
Prior to runcpu invocation

(Continued on next page)
### General Notes (Continued)

Filesystem page cache synced and cleared with:
```
sync; echo 3>/proc/sys/vm/drop_caches
```

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5


Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

### Platform Notes

BIOS Settings:
- Logical Processor: Disabled
- Virtualization Technology: Disabled
- System Profile: Custom
- CPU Power Management: Maximum Performance
- C1E: Disabled
- C States: Autonomous
- Memory Patrol Scrub: Disabled
- Energy Efficiency Policy: Performance
- CPU Interconnect Bus Link: Power Management: Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.7-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Fri Apr 30 02:09:05 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see [https://www.spec.org/cpu2017/Docs/config.html#sysinfo](https://www.spec.org/cpu2017/Docs/config.html#sysinfo)

From /proc/cpuinfo
- model name: Intel(R) Xeon(R) Silver 4314 CPU @ 2.40GHz
  - 2 "physical ids" (chips)
  - 32 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores: 16
  - siblings: 16
  - physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  - physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit

---

(Continued on next page)
### Platform Notes (Continued)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Byte Order:</td>
<td>Little Endian</td>
</tr>
<tr>
<td>CPU(s):</td>
<td>32</td>
</tr>
<tr>
<td>On-line CPU(s) list:</td>
<td>0-31</td>
</tr>
<tr>
<td>Thread(s) per core:</td>
<td>1</td>
</tr>
<tr>
<td>Core(s) per socket:</td>
<td>16</td>
</tr>
<tr>
<td>Socket(s):</td>
<td>2</td>
</tr>
<tr>
<td>NUMA node(s):</td>
<td>2</td>
</tr>
<tr>
<td>Vendor ID:</td>
<td>GenuineIntel</td>
</tr>
<tr>
<td>CPU family:</td>
<td>6</td>
</tr>
<tr>
<td>Model:</td>
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</tr>
<tr>
<td>Model name:</td>
<td>Intel(R) Xeon(R) Silver 4314 CPU @ 2.40GHz</td>
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<tr>
<td>Stepping:</td>
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<tr>
<td>CPU MHz:</td>
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<td>BogoMIPS:</td>
<td>4800.00</td>
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<td>Virtualization:</td>
<td>VT-x</td>
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<tr>
<td>L1d cache:</td>
<td>48K</td>
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<tr>
<td>L1i cache:</td>
<td>32K</td>
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<tr>
<td>L2 cache:</td>
<td>1280K</td>
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<tr>
<td>L3 cache:</td>
<td>24576K</td>
</tr>
<tr>
<td>NUMA node0 CPU(s):</td>
<td>0-15</td>
</tr>
<tr>
<td>NUMA nodel CPU(s):</td>
<td>16-31</td>
</tr>
<tr>
<td>Flags:</td>
<td>fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acp1 mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrm pdcm pcmid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 invpcid_single intel_puin ssbd mba ibrs ibp bsts ibrs_menhasted fsasbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cmip rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl xsaveopt xsaveopt xsave xsetbx1 xsaves cmqm llc cmq_occup_llc cmq_mbm_total cmq_mbm_local split_lock_detect hwmon runvd dtherm ida arat pln pts avx512vbmi umip pku ospke avx512_vbmi2 gfni vae vclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq lal57 rdpid md_clear pconfig flush_l1d arch_capabilities</td>
</tr>
</tbody>
</table>

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

<table>
<thead>
<tr>
<th>Available:</th>
<th>2 nodes (0-1)</th>
</tr>
</thead>
<tbody>
<tr>
<td>node 0 cpus:</td>
<td>0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15</td>
</tr>
<tr>
<td>node 0 size:</td>
<td>251127 MB</td>
</tr>
<tr>
<td>node 0 free:</td>
<td>247752 MB</td>
</tr>
<tr>
<td>node 1 cpus:</td>
<td>16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31</td>
</tr>
<tr>
<td>node 1 size:</td>
<td>251403 MB</td>
</tr>
<tr>
<td>node 1 free:</td>
<td>257418 MB</td>
</tr>
</tbody>
</table>

(Continued on next page)
Dell Inc.
PowerEdge MX750c (Intel Xeon Silver 4314, 2.40 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

SPECspeed®2017_int_base = 11.2
SPECspeed®2017_int_peak = 11.5

Platform Notes (Continued)

node distances:
node 0 1
0: 10 20
1: 20 10

From /proc/meminfo
MemTotal: 527814116 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.3 (Ootpa)"
ID="rheil"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store
Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps
barriers and __user pointer
sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB:
conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

(Continued on next page)
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| SPECspeed®2017_int_base = 11.2 | SPECspeed®2017_int_peak = 11.5 |

---

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

---

**Platform Notes (Continued)**

run-level 3 Apr 30 01:32

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.7-ic2021.1

Filesystem | Type | Size | Used | Avail | Use% | Mounted on
--- | --- | --- | --- | --- | --- | ---
tmpfs | tmpfs | 125G | 4.4G | 121G | 4% | /mnt/ramdisk

From /sys/devices/virtual/dmi/id

Vendor: Dell Inc.  
Product: PowerEdge MX750c  
Product Family: PowerEdge  
Serial: 1234567

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x 002C0632002C 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200, configured at 2666  
16x Not Specified Not Specified

BIOS:

- BIOS Vendor: Dell Inc.  
- BIOS Version: 1.1.2  
- BIOS Date: 04/09/2021  
- BIOS Revision: 1.1

(End of data from sysinfo program)

---

**Compiler Version Notes**

```
C       | 600.perlbench_s(peak)
```

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

```
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

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Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |

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<td>648.exchange2_s(base, peak)</td>
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Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |

### Base Compiler Invocation

- **C benchmarks:** icx

- **C++ benchmarks:** icpx

- **Fortran benchmarks:** ifort
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Test Date: Apr-2021  
Hardware Availability: Apr-2021  
Software Availability: Dec-2020

### Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64  
602.gcc_s: -DSPEC_LP64  
605.mcf_s: -DSPEC_LP64  
620.omnetpp_s: -DSPEC_LP64  
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX  
625.x264_s: -DSPEC_LP64  
631.deepsjeng_s: -DSPEC_LP64  
641.leela_s: -DSPEC_LP64  
648.exchange2_s: -DSPEC_LP64  
657.xz_s: -DSPEC_LP64

### Base Optimization Flags

#### C benchmarks:
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512  
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

#### C++ benchmarks:
-DSPEC_OPENMP -m64 -std=c++11 -Zm -z,muldefs -xCORE-AVX512 -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-mbranches-within-32B-boundaries  
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/  
-lqkmalloc

#### Fortran benchmarks:
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto  
-mbranches-within-32B-boundaries

### Peak Compiler Invocation

C benchmarks (except as noted below):
- icx
- 600.perlbench_s: icc

C++ benchmarks:
- icpx

(Continued on next page)
Dell Inc.
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SPECspeed®2017_int_base = 11.2
SPECspeed®2017_int_peak = 11.5

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<td>Test Sponsor: Dell Inc.</td>
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<td>Tested by: Dell Inc.</td>
<td>Software Availability: Dec-2020</td>
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</tbody>
</table>

### Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifort

### Peak Portability Flags

Same as Base Portability Flags

### Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass l) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

(Continued on next page)
Dell Inc.
PowerEdge MX750c (Intel Xeon Silver 4314, 2.40 GHz)

SPEC CPU®2017 Integer Speed Result
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SPECspeed®2017_int_base = 11.2
SPECspeed®2017_int_peak = 11.5

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Apr-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

Peak Optimization Flags (Continued)

641.leela_s: basepeak = yes
Fortran benchmarks:
648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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