### Dell Inc.

**PowerEdge MX750c (Intel Xeon Gold 6338T, 2.10 GHz)**

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**SPECspeed®2017_int_base = 11.6**  
**SPECspeed®2017_int_peak = 11.8**

<table>
<thead>
<tr>
<th>SPECspeed</th>
<th>Result</th>
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<tbody>
<tr>
<td>SPECspeed®2017_int_base</td>
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<tr>
<td>SPECspeed®2017_int_peak</td>
<td>11.8</td>
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<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed®2017_int_base (11.6)</th>
<th>SPECspeed®2017_int_peak (11.8)</th>
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<tbody>
<tr>
<td>600.perlbench_s</td>
<td>8.09</td>
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<tr>
<td>602.gcc_s</td>
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</tr>
<tr>
<td>605.mcf_s</td>
<td>11.4</td>
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<td>620.omnetpp_s</td>
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<tr>
<td>623.xalancbmk_s</td>
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<td>625.x264_s</td>
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<tr>
<td>631.deepsjeng_s</td>
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<tr>
<td>641.leela_s</td>
<td>17.3</td>
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<tr>
<td>648.exchange2_s</td>
<td>18.8</td>
<td></td>
</tr>
<tr>
<td>657.xz_s</td>
<td>22.9</td>
<td></td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 6338T  
- **Max MHz:** 3400  
- **Nominal:** 2100  
- **Enabled:** 48 cores, 2 chips  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 48 KB D on chip per core  
- **L2:** 1.25 MB I+D on chip per core  
- **L3:** 36 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)  
- **Storage:** 125 GB on tmpfs  
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux 8.3 (Ootpa)  
  4.18.0-240.el8.x86_64  
- **Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++  
  Compiler Build 20201113 for Linux;  
  Fortran: Version 2021.1 of Intel Fortran Compiler  
  Classic Build 20201112 for Linux;  
  C/C++: Version 2021.1 of Intel C/C++ Compiler  
  Classic Build 20201112 for Linux  
- **Parallel:** Yes  
- **Firmware:** Version 1.1.2 released Apr-2021  
- **File System:** tmpfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
SPEC CPU®2017 Integer Speed Result

Dell Inc.
PowerEdge MX750c (Intel Xeon Gold 6338T, 2.10 GHz)

SPECspeed®2017_int_base = 11.6
SPECspeed®2017_int_peak = 11.8

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
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<th>Seconds</th>
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<td>270</td>
<td>22.9</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default
Prior to runcpu invocation

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Dell Inc.

PowerEdge MX750c (Intel Xeon Gold 6338T, 2.10 GHz)

SPEC CPU®2017_int_base = 11.6
SPEC CPU®2017_int_peak = 11.8

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: May-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

General Notes (Continued)

Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:
Logical Processor : Disabled
Virtualization Technology : Disabled

System Profile : Custom
CPU Power Management : Maximum Performance
C1E : Disabled
C States : Autonomous
Memory Patrol Scrub : Disabled
Energy Efficiency Policy : Performance
CPU Interconnect Bus Link
Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.7-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Sat May 1 00:30:28 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6338T CPU @ 2.10GHz
  2 "physical id"s (chips)
  48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 24
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit

(Continued on next page)
**SPEC CPU®2017 Integer Speed Result**

**Dell Inc.**

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**SPECspeed®2017_int_base = 11.6**  
**SPECspeed®2017_int_peak = 11.8**

**Platform Notes (Continued)**

- **Byte Order:** Little Endian
- **CPU(s):** 48
- **On-line CPU(s) list:** 0-47
- **Thread(s) per core:** 1
- **Core(s) per socket:** 24
- **Socket(s):** 2
- **NUMA node(s):** 2
- **Vendor ID:** GenuineIntel
- **CPU family:** 6
- **Model:** 106
- **Model name:** Intel(R) Xeon(R) Gold 6338T CPU @ 2.10GHz
- **Stepping:** 6
- **CPU MHz:** 1219.371
- **BogoMIPS:** 4200.00
- **Virtualization:** VT-x
- **L1d cache:** 48K
- **L1i cache:** 32K
- **L2 cache:** 1280K
- **L3 cache:** 36864K
- **NUMA node0 CPU(s):** 0-23
- **NUMA node1 CPU(s):** 24-47
- **Flags:**
  - fpu
  - vme
  - de
  - pse
  - tac
  - msr
  - pae
  - mce
  - cx8
  - sep
  - mtrr
  - pae
  - mce
  - cx8

/proc/cpuinfo cache data
  - cache size : 36864 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  - available: 2 nodes (0-1)
  - node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
  - node 0 size: 495140 MB
  - node 0 free: 514671 MB
  - node 1 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47
  - node 1 size: 494885 MB
  - node 1 free: 506162 MB

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

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SPECspeed®2017_int_base = 11.6
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CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

SPECspec

SPEC CPU

Copyright 2017-2021 Standard Performance Evaluation Corporation

Dell Inc.
PowerEdge MX750c (Intel Xeon Gold 6338T, 2.10 GHz)

SPECspeed

2017_int_peak = 11.8
SPECspeed

2017_int_base = 11.6

CPU2017 License: 55
Test Date: May-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

Tested by: Dell Inc.

Test Date: May-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

Platform Notes (Continued)

node distances:
node  0  1
  0:  10  20
  1:  20  10

From /proc/meminfo
MemTotal: 1056291648 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.3 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store
Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapsgs
barriers and __user pointer
sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB:
conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

(Continued on next page)
**SPEC CPU®2017 Integer Speed Result**

**Dell Inc.**

PowerEdge MX750c (Intel Xeon Gold 6338T, 2.10 GHz)

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**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Test Date:** May-2021  
**Hardware Availability:** Apr-2021  
**Tested by:** Dell Inc.  
**Software Availability:** Dec-2020

---

**Platform Notes (Continued)**

run-level 3 May 1 00:26

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.7-ic2021.1

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>tmpfs</td>
<td>tmpfs</td>
<td>125G</td>
<td>4.4G</td>
<td>121G</td>
<td>4%</td>
<td>/mnt/ramdisk</td>
</tr>
</tbody>
</table>

From /sys/devices/virtual/dmi/id  
Vendor: Dell Inc.  
Product: PowerEdge MX750c  
Product Family: PowerEdge  
Serial: 1234567

**Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.**

**Memory:**

16x 00AD063200AD HMAA8GR7AJR4N-XN 64 GB 2 rank 3200  
16x Not Specified Not Specified

**BIOS:**

- BIOS Vendor: Dell Inc.  
- BIOS Version: 1.1.2  
- BIOS Date: 04/09/2021  
- BIOS Revision: 1.1

(End of data from sysinfo program)

---

**Compiler Version Notes**

```
C       | 600.perlbench_s(peak)
```

---

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

```
C
600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
625.x264_s(base, peak) 657.xz_s(base, peak)
```

---

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

(Continued on next page)
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Hardware Availability: Apr-2021  
Software Availability: Dec-2020

Compiler Version Notes (Continued)

==============================================================================
C       | 600.perlbench_s(peak) 
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)  
64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)  
| 625.x264_s(base, peak) 657.xz_s(base, peak) 
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)  
| 631.deepsjeng_s(base, peak) 641.leela_s(base, peak) 
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================
Fortran | 648.exchange2_s(base, peak) 
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:  
icx

C++ benchmarks:  
icpx

Fortran benchmarks:  
ifort
SPEC CPU®2017 Integer Speed Result

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Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-lqkmallocc

Fortran benchmarks:
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries

Peak Compiler Invocation

C benchmarks (except as noted below):
icx

600.perlbench_s: icc

C++ benchmarks:
icpx

(Continued on next page)
Dell Inc.

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SPECspeed®2017_int_base = 11.6  
SPECspeed®2017_int_peak = 11.8

CPU2017 License: 55  
Test Date: May-2021  
Test Sponsor: Dell Inc.  
Hardware Availability: Apr-2021  
Tested by: Dell Inc.  
Software Availability: Dec-2020

Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)  
-xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -fno-strict-overflow  
-mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto  
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4  
-mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -DSPEC_OPENMP -Fiopenmp -std=c11 -m64 -Wl,-z,muldefs  
-xCORE-AVX512 -flto -O3 -ffast-math  
-qopt-mem-layout-trans=4 -fno-alias  
-mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

(Continued on next page)
Dell Inc.

PowerEdge MX750c (Intel Xeon Gold 6338T, 2.10 GHz)

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CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: May-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

Peak Optimization Flags (Continued)

641.leela\_s: basepeak = yes

Fortran benchmarks:

648.exchange2\_s: basepeak = yes

The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU\textsuperscript{®}2017 v1.1.7 on 2021-05-01 00:30:27-0400.
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